USER'S Manual

MIC-3000 Series

MIC-3042A/B

4U-High 8-slots CompactPCI Platform with Standard CPCI Power Supply

Advantech CompactPCI™

Modular Industrial Computer

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CE Notification

The MIC-3042 series, developed by Advantech Co., Ltd., has passed the CE test for environment specifications when shielded cables are used for external wiring. We recommend the use of shielded cables.

Product warranty

Advantech warrants to you, the original purchaser, that each of its products will be free from defects in materials and workmanship for one year from the date of purchase.

This warranty does not apply to any products which have been repaired or altered by persons other than repair personnel authorized by Advantech, or which have been subject to misuse, abuse, accident or improper installation. Advantech assumes no liability under the terms of this warranty as a consequence of such events.

Because of Advantech's high quality-control standards and rigorous testing, most of our customers never need to use our repair service. If an Advantech product is defective, it will be repaired or replaced at no charge during the warranty period. For out-of-warranty repairs, you will be billed according to the cost of replacement materials, service time and freight. Please consult your dealer for more details.
If you think you have a defective product, follow these steps:

1. Collect all the information about the problem encountered. For example, CPU speed, Advantech products used, other hardware and software used, etc. Note anything abnormal and list any on-screen messages you get when the problem occurs.
2. Call your dealer and describe the problem. Please have your manual, product, and any helpful information readily available.
3. If your product is diagnosed as defective, obtain an RMA (return merchandise authorization) number from your dealer. This allows us to process your return more quickly.
4. Carefully pack the defective product, a fully-completed Repair and Replacement Order Card and a photocopy proof of purchase date (such as your sales receipt) in a shippable container. A product returned without proof of the purchase date is not eligible for warranty service.
5. Write the RMA number visibly on the outside of the package and ship it prepaid to your dealer.

Packing List

Before installation, ensure that the following materials have been received:

• One MIC-3042 CompactPCI™ enclosure with backplane

• One box of accessories

• One warranty certificate

• One CD-ROM for user manual (PDF file)

If any of these items are missing or damaged, contact your distributor or sales representative immediately.

Technical Support and Sales Assistance

If you have any technical questions about the MIC-3042 or any other Advantech products, please visit our support website at:

• **http://www.advantech.com.tw/support**

For more information about Advantech's products and sales information, please visit:

• **http://www.advantech.com.**

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General Information

1.1   Introduction

The MIC-3042 series are a 4U-high enclosure designed for standard CPCI power supplies which means an extremely low mean-time-to-repair (MTTR) since each power supply can be hot-swapped. It equips with a 6U 8-slot CompactPCI H.110 CT bus backplane and a 500W 2+1 redundant AC/DC power supply. It is suitable for telecommunication, computer telephony integration, and industrial automation. The built-in high quality backplane with 64 bit / 66MHz bus speed offers best impedance control, meets open architecture standards and is compatible with all Advantech CompactPCI boards.

The MIC-3042 series support IEEE 1101.11 rear I/O transition board. Users can route I/O signals to the rear transition board to simplify system cabling. Front boards pop in and out without any hardwiring. Efficient hot swappable fan module gives easy self-maintenance capability. The system slots can support CPU boards featuring desktop Intel® Pentium® IV Processor-M up to 2.2GHz with MIC-3358 series and Intel® Pentium® M processors up to 1.8GHz with MIC-3369 series. With the chassis management module (MIC-3924L-A), the chassis temperature and fan speed can be monitored and the MIC-3924L-A will issue sound to alarm while any abnormal.

The MIC-3042A supports PICMG 2.16 and can be used as blade servers. Moreover, the MIC-3042B supports slim CD-ROM or FDD using MIC-3960 media carrier board. With redundant industry standard CompactPCI power supply plus Advantech's renowned reliability, the MIC-3042 4U enclosure is a rock solid choice for communications and telephony applications.

There are four models of MIC-3042 series :

• **MIC-3042A-A:** MIC-3042A enclosure, w/ 8-slot CompactPCI™ 6U backplane (MIC-3042A) supporting CT bus application and PICMG 2.16, and 500W 2+1 redundant AC CPCI power supplies.

• **MIC-3042A-D:** MIC-3042A enclosure, w/ 8-slot CompactPCI™ 6U backplane (MIC-3042A) supporting CT bus application and PICMG 2.16, and 500W 2+1 redundant DC CPCI power supplies.

• **MIC-3042B-A:** MIC-3042B enclosure, w/ 8-slot CompactPCI™ 6U backplane (MIC-3042B) supporting CT bus application and 500W 2+1 redundant AC CPCI power supplies.

• **MIC-3042B-D:** MIC-3042B enclosure, w/ 8-slot CompactPCI™ 6U backplane

(MIC-3042B) supporting CT bus application and 500W 2+1 redundant DC CPCI power supplies.

1.2 Features

• Eight 6U card slots

• Supports front and rear I/O

• Supports H.110 CT application

• 500W 2+1 redundant AC/DC power supply

• Hot-swap compliant backplane

• A hot-swap fan module

• Integrated intelligent management module,MIC-3924L, which can monitor the chassis temperature and fan speed and issue alarm while any abnormal.

1.3 Specifications

1.3.1 General

• **Construction:** Aluminum frame and galvanized sheet steel

• 8-slot space (32 TE), including one system slot and seven peripheral slots for MIC-3042A and six peripheral slots and one media slot for MIC-3042B

• "Hot swappable" platform complies with PICMG 2.1 R 2.0 Hot Swap Specification

• **Dimensions** (W x H x D, mounting flanges not included):
   4U: 440 x 177 x 320 mm (17.3" x 7" x 12.6")

• **Weight:** 18 kg (39.7 lb)

• **Operating temperature:** 0 ~ 45° C

• **Storage temperatur**e: -20° C ~ 60° C

• **Relative humidity:** 10 ~ 95% @ 40° C, non-condensing

• **Operating altitude:** 0 ~ 3,048 meters (0 ~ 10,000 feet)

• **Storage/transit altitude:** 0 ~ 12,190 meters (40,000 feet)

• **Shock:** 10 G (operating); 30 G (storage/transit)

• **Random vibration:** 1.0 Grms (operating); 2.0 Grms (operating)

1.3.2 Hot-swap Fans

• **Air flow:** One 193-CFM front fan and one 61.3-CFM rear fan

• **Rated fan speed:** 3,000 rpm (front fan); 3,400 rpm (rear fran)

• **Life expectancy:** 80,000 hours @ 40° C (front fan); 50,000 hours @ 25° C (rear fan)

1.3.3 Power Supply

• **Input:**

　AC: 100~240 V @ 47~63 Hz, full range (for MIC-3042X-A models)

　DC: -48V (TYP), -36~-72V range (for MIC-3042X-D models)

• **Output:** 500W+250W 2+1 hot swappable and redundant AC or DC w/PFC

• **Maximum load:** +3.3V @ 36A, +5V @ 50A, +12V @ 10A, -12V @ 1A

• **Minimum load:** +3.3 V @ 0A, + 5V @ 2A, +12 V @ 0A, -12V @ 0A

• **MTBF:** AC: 266,241 hours @ 25° C; DC: 92,859 hours @ 30° C

• **Safety:** UL/CE/TUV

1.4 Dimensions
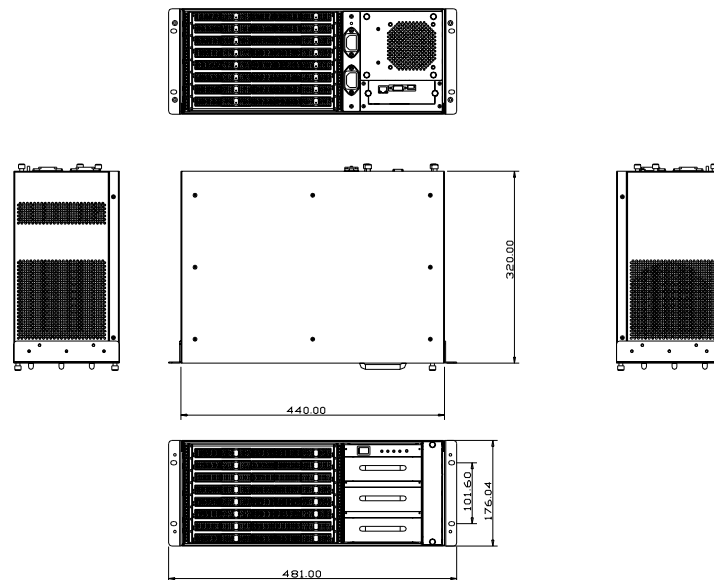
Figure 1-1: MIC-3042 dimensions.

Figure 1-2: Appearance of MIC-3042

2
Installation

2.1 Initial Inspection

We have carefully inspected the MIC-3042 series mechanically and electrically before shipping. It should be free of marks and scratches and in perfect working order upon receipt. As you unpack the MIC-3042 series, check it for signs of shipping damage (damaged box, scratches, dents, etc.). If it is damaged or fails to meet specifications, notify our service department or your local representative immediately. Also notify the carrier. Retain the shipping carton and packing material for inspection by the carrier. After inspection, we will make arrangements to repair or replace the unit.

Warning!!! **We strongly recommend that only qualified, experienced personnel install or remove components. They must exercise extreme caution when doing so.**

Warning!!! **Service personnel must make sure that the power cord has been pull out before striping down any component. After service, ground wire have to be connected certainly before power cord.**

2.2 The MIC-3042 Illustration

The MIC-3042 series are designed to be installed and maintained easily.
Figure 2-1 and Figure 2-2 illustrate important components on the front and hot swappable parts of the enclosure.
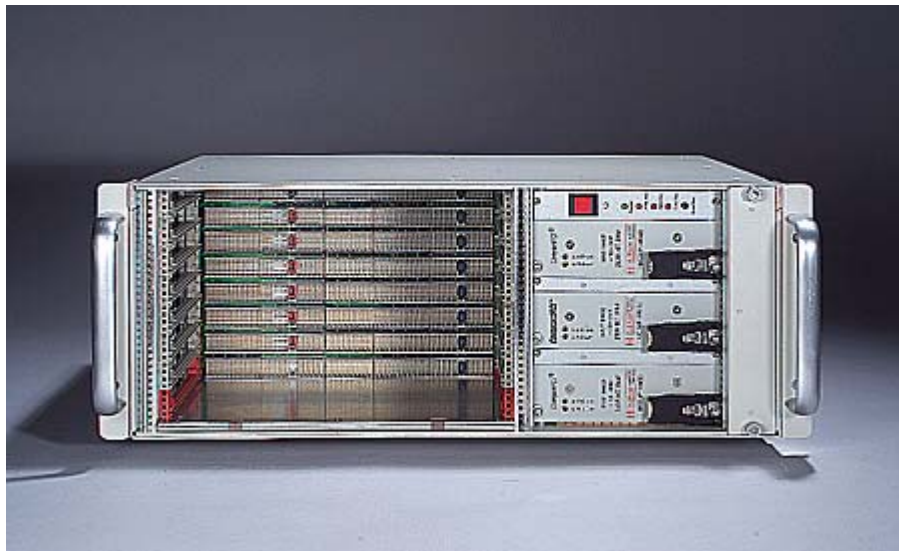
Figure 2-1: Front view of MIC-3042
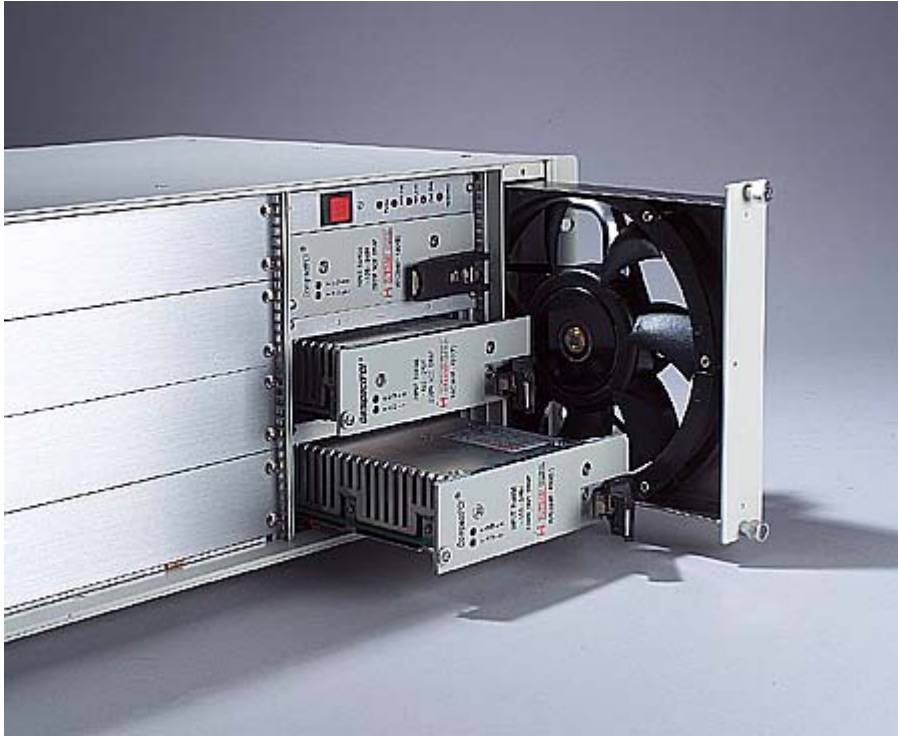
Figure 2-2: Removable parts of MIC-3042

2.3 Installation Procedures

2.3.1 Card Installation and Removal

The CompactPCI™ connectors are firm and rigid, and require careful handling while plugging and unplugging. Improper installation of a card can easily damage the backplane of the chassis.

The system card can be installed only in the system slot. Do not insert the system card into any other slot, or insert a peripheral card into the system slot. The system slot is marked by a triangle enclosing the slot number 1 for MIC-3042A and 2 for MIC-3042B. Please refer to the Appendix A and Appendix B for detailed backplane information

The insert/eject handles on CompactPCI™ cards help users to install and remove the cards easily and safely. Follow the procedures below to install a card into a chassis:

To install a card:

1. Hold the card horizontally. Be sure that the card is oriented correctly.

The components of the card should be pointing to the up side.

2. Be sure that the handles of the card are not latched. Release the handles if they are latched. Handles from different vendors may have different latch designs.

Caution!!! Keep your fingers away from the latch hinges to prevent your fingers from getting pinched.

3. Insert the card into the chassis by sliding the left and right edges of the card into the card guides.

4. Push the card into the slot gently by sliding the card along the card guide until the handles meet the rectangular holes of the cross rails. 1 2
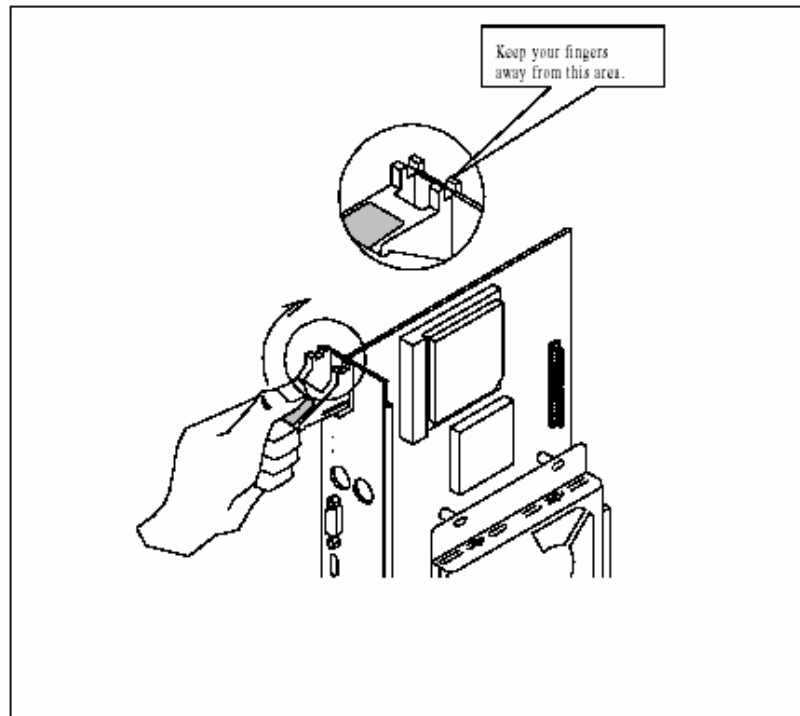
Figure 2-3: Installing a card into the chassis

Note: **If the card is correctly positioned and has been slid all the way into the chassis, the handles should match the rectangular holes. If not, remove the card from the card guide and repeat step 3 again. Do not try to install a card by forcing it into the chassis.**

5. Pull the right handle to the left and pull the left handle to the right to push the card into place and fixed.

6. Secure the card by locking the handles into place.

To remove a card:

1. Unscrew the screws on the card front panel. Release the locking latches on the handles.

2. To push the red buttons on the right and left handles to loosen the card from the backplane.

3. Slide the card out.

2.3.2 Before Operating the System

Before operating your system, check your power supply source firstly.

Adjust the switch on the power supply to the correct voltage.

Two mounting flanges are included for users who would like to install the MIC-3042 on a 19" rack.

2.3.3 Connecting with rear I/O module

The MIC-3042 is limited to be used with rear I/O module. To install the RIO module, please follow the steps below:

1. Remove the blank panel above the system RIO slot. (Suggest to remove all the blank panels for installation)
2. Connecting with the right connector on board, and slide into the card cage.
3. Power up the system and check all the storage devices work properly.

2.3.4 Replacing the Hot-swap Fan

The MIC-3042 provides two hot-swap fans, one is at the front side and another is at the rear side. Please refer to Figure 2-2. It can be removed without turning off the system power or interrupting system operation.

Follow these steps to replace a fan:

1. Unfasten the fan's holder.

2. Slide the fan's holder out.

3. Replace the old fan with a new one.

4. Slide the fan's holder in.

5. Fasten the new fan's holder.

3

Backplane

3.1 General Information

There are two kinds of backplanes for MIC-3042 series. MIC-3042A and MIC-3042B are with H.110 CT bus backplane. Both backplanes provide eight CompactPCI™ slots with one slot dedicated to the CPU board. The MIC-3042 supports front I/O wiring, providing simplified system cabling. The backplane also provides several 3-pin connectors to connect hot-swappable cooling fan module. In order to provide users with a flexible system configuration, the MIC-3042 includes one standard ATX power connector to accept one ATX power supply. The MIC-3042 complies with PICMG 2.1 Hot-Swap Specification, providing full hot-swapping capability. Users can build a hot-swap system using hot-swap plug-in boards and software.

3.2 Features

• Eight CompactPCI™ slots (one system slot and seven peripheral slots)

• 64-bit PCI bus compliant

• Complies with PICMG 2.1 Hot-Swap Specification

• Accepts redundant AC/DC power supply

• Chassis alarm module for environment monitoring.

• Hot-swappable fan interface.

3.3 Specification

• Eight CompactPCI™ slots (one system slot and seven peripheral slots)

• Bus width: 64-bit

• 10-layer PCB (for MIC-3042A); 8-layer PCB (for MIC-3042B); 3.0 mm thick

• Power connector: One ATX power connector for connecting standard ATX power supply

• Complies with CompactPCI™ Specification PICMG 2.0, R.3.0

• Complies with CompactPCI™ Hot Swap Specification PICMG 2.1, R2.0.

• Complies with CompactPCI™ Computer Telephony PICMG2.5, R1.0 (optional)

• Dimensions: 440 x 177 x 320 mm (17.3″ x 7″ x 12.6″)

• Operating temperature: -5 ° C ~ 80° C

3.4 Slot Assignments

The CompactPCI™ specification defines slot numbering separation for physical and logical slots. Each slot has a physical number and a logical number (refer to the CompactPCI™ specification version 2.0 R3.0 for further information on slot assignments). The physical numbers are printed on the backplane, enclosed in circles or triangles. Slot1 of MIB-3042A and slot2 of MIC-3042B, marked by a triangle, is the system slot and can only be used by a CPU board. The other slots are

peripheral slots. The logical number of each slot is defined according to the IDSEL signal and the associated address used to select the slot. Table 3-1 shows the system slot and peripheral slots relationships on the backplane. The system slot has a logical number of 1 (for MIC-3042A) or 2 (for MIC-3042B), and the peripheral slot has a logical number of 2~8 for MIC-3042A or 1, 3-8 for MIC-3042B. The connectors in logical slot 1 are designated as 1-P1, 1-P2, and 1-P3 from the bottom up.

Nomenclature for connectors in the other slot is similar, such as 2-P1 and 2-P2. Connector P1 on the system slot is a keyed connector providing 32-bit CompactPCI™ bus between the system slot and the peripheral slot. Connector P2 on the system slot is an un-keyed connector providing 64-bit CompactPCI™ bus between the system slot and the peripheral slots. Connector P3 on the system slot is open for user definition.

Please check appendix A for the pin assignment for all the connectors on the backplane.

Table 3-1 : MIC-3042A System to peripheral slot signal assignment.

System Slot (Logical Slot 1)		Peripheral Slot (Logical Slot 2)	
CLK0	P1:D6	CLK	P1:D6
AD31	P1:E6	IDSEL	P1:B9
REQ0#	P1:A6	REQ#	P1:A6
GNT0#	P1:E5	GNT#	P1:E5
System Slot (Logical Slot 1)		**Peripheral Slot (Logical Slot 3)**	
CLK1	P2:A1	CLK	P1:D6
AD30	P1:A7	IDSEL	P1:B9
REQ1#	P2:C1	REQ#	P1:A6
GNT1#	P2:D1	GNT#	P1:E5
System Slot (Logical Slot 1)		**Peripheral Slot (Logical Slot 4)**	
CLK2	P2:A2	CLK	P1:D6
AD29	P1:B7	IDSEL	P1:B9
REQ2#	P2:E2	REQ#	P1:A6
GNT2#	P2:D2	GNT#	P1:E5
System Slot (Logical Slot 1)		**Peripheral Slot (Logical Slot 5)**	
CLK3	P2:B2	CLK	P1:D6
AD28	P1:C7	IDSEL	P1:B9
REQ3#	P2:E2	REQ#	P1:A6
GNT3#	P2:C3	GNT#	P1:E5
System Slot (Logical Slot 1)		**Peripheral Slot (Logical Slot 6)**	
CLK4	P2:A3	CLK	P1:D6
AD27	P1:E7	IDSEL	P1:B9
REQ4#	P2:A3	REQ#	P1:A6
GNT4#	P2:E3	GNT#	P1:E5
System Slot (Logical Slot 1)		**Peripheral Slot (Logical Slot 7)**	
CLK5	P2:A20	CLK	P1:D6
AD26	P1:A8	IDSEL	P1:B9
REQ5#	P2:D15	REQ#	P1:A6

GNT5#	P2:E15	GNT#	P1:E5
System Slot (Logical Slot 1)		**Peripheral Slot (Logical Slot 8)**	
CLK6	P2:A21	CLK	P1:D6
AD25	P1:D8	IDSEL	P1:B9
REQ6#	P2:A17	REQ#	P1:A6
GNT6#	P2:E17	GNT#	P1:E5

Table 3-2 : MIC-3042B System to peripheral slot signal assignment.

System Slot (Logical Slot 2)		**Peripheral Slot (Logical Slot 1)**	
CLK0	P1:D6	CLK	P1:D6
AD31	P1:E6	IDSEL	P1:B9
REQ0#	P1:A6	REQ#	P1:A6
GNT0#	P1:E5	GNT#	P1:E5
System Slot (Logical Slot 2)		**Peripheral Slot (Logical Slot 3)**	
CLK1	P2:A1	CLK	P1:D6
AD30	P1:A7	IDSEL	P1:B9
REQ1#	P2:C1	REQ#	P1:A6
GNT1#	P2:D1	GNT#	P1:E5
System Slot (Logical Slot 2)		**Peripheral Slot (Logical Slot 4)**	
CLK2	P2:A2	CLK	P1:D6
AD29	P1:B7	IDSEL	P1:B9
REQ2#	P2:E2	REQ#	P1:A6
GNT2#	P2:D2	GNT#	P1:E5
System Slot (Logical Slot 2)		**Peripheral Slot (Logical Slot 5)**	
CLK3	P2:B2	CLK	P1:D6
AD28	P1:C7	IDSEL	P1:B9
REQ3#	P2:E2	REQ#	P1:A6
GNT3#	P2:C3	GNT#	P1:E5
System Slot (Logical Slot 2)		**Peripheral Slot (Logical Slot 6)**	
CLK4	P2:A3	CLK	P1:D6
AD27	P1:E7	IDSEL	P1:B9
REQ4#	P2:A3	REQ#	P1:A6
GNT4#	P2:E3	GNT#	P1:E5
System Slot (Logical Slot 2)		**Peripheral Slot (Logical Slot 7)**	
CLK5	P2:A20	CLK	P1:D6
AD26	P1:A8	IDSEL	P1:B9
REQ5#	P2:D15	REQ#	P1:A6
GNT5#	P2:E15	GNT#	P1:E5
System Slot (Logical Slot 2)		**Peripheral Slot (Logical Slot 8)**	
CLK6	P2:A21	CLK	P1:D6
AD25	P1:D8	IDSEL	P1:B9
REQ6#	P2:A17	REQ#	P1:A6
GNT6#	P2:E17	GNT#	P1:E5

Figure 3-1: MIC-3042A backplane slot numbering
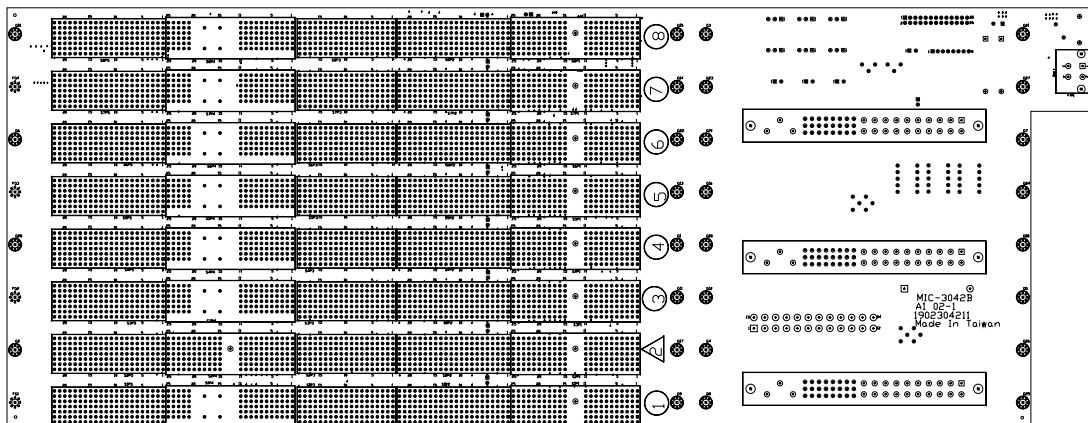
Figure 3-2: MIC-3042B backplane slot numbering

3.5 Connector and Jumper Locations

The backplane provides connectors and jumpers for users to configure the backplane for specific application. Table 3-2 gives a brief description to each connector on the backplane. Figure 3-3 to Figure 3-4 illustrate the connector locations of the backplane.

Table 3-3: Backplane's connector and jumper description

Function	MIB-3042A	MIB-3042B
ATX power connector 1	AC_DCIN1	AC_DCIN1
CPCI power connector	POWERIN1~ POWERIN3	POWERIN1~ POWERIN3
Led board connector	CN11	CN11
Fan module connectors	FAN1,CN5~CN9	FAN1,CN5~CN9
Power switch connector	CN12	CN12
V I/O voltage selections	T1~T3	T1~T3
64-bit CompactPCI™ bus	P1, P2	P1, P2
I/O transition	P3, P5	P3, P5
H.110 CT bus / Non-CT bus (slot 2~6)	P4	P4
Alarm Module Connector	CN10	CN10
M66EN	JP13	JP13
64EN	JP1~JP8	JP1~JP8
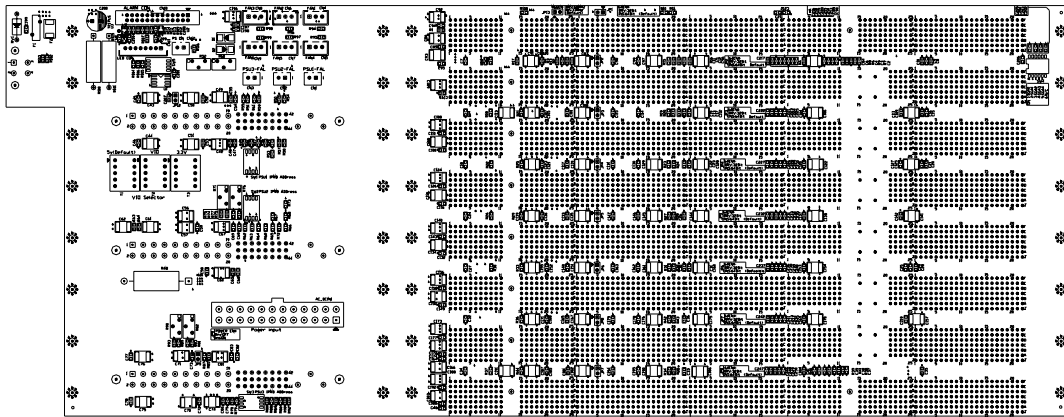
Figure 3-3: The connector and jumper locations on the rear side. (MIC-3042A)
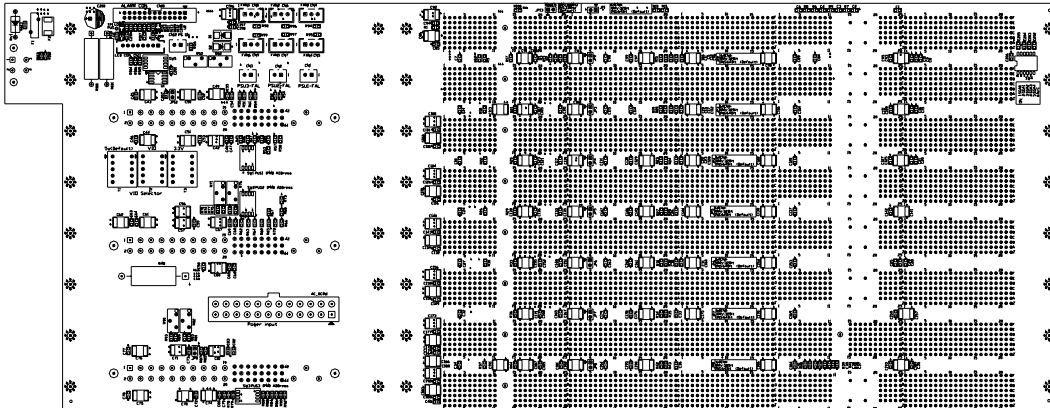
Figure 3-4: The connector and jumper locations on the rear side. (MIC-3042B)

3.5.1 ATX Power Connector (ATX1)

This connector accepts one standard ATX power supply.

Note: Do not use ATX power supply and plug-in power module at the same time.

3.5.2 Power Switch

This connector provides power on/off control of the ATX power supply or the plug-in power module. If the CompactPCI™ chassis provides a 2-pin power switch cord, connect this cord to the CN12 connector and users can control the power on/off by the power switch. Or users can directly short this connector by a jumper and control the power on/off by the ATX power supply switch.

3.5.3 V I/O Voltage Selection

This jumper is used to select the V I/O voltage. The backplane allows V I/O to be set to either 5 V or 3.3 V. Since the default is configured for use with 5V CompactPCI™ boards (blue keyed connectors), once the jumper is set to 3.3 V, the CompactPCI™ keys must be changed to 3.3 V at the same time (as yellow keyed connectors).

VIO for +5V   **VIO for +3.3V**
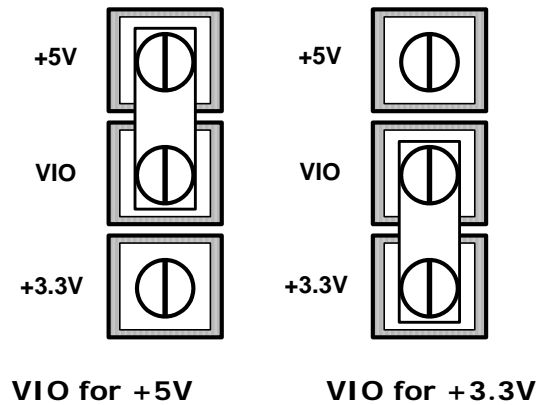
Figure 3-5: V I/O voltage selection

3.5.4 Fan Module Connector

The FAN connectors provide +12 V power for fan operation and accepts the tachometer output from the fans. Note the fan4 is reserved as factory default.

3.5.5 LED Status Connector

This connector provides digital outputs for the alarm module (MIC-3924L-A) to the chassis front panel notification used. Three alarm LED (red) and one power status LED (green) is included for Fan_fail, Power_fail, and Temp_fail for alert notification.

3.6 Clock Routing Configuration

The backplane is configured to comply with the clock routing specified in the CompactPCI™ Specification, PICMG 2.0, R3.0. This Specification requires that each slot be independently clocked.

Appendix A

Pin Assignments of

MIB-3042A backplane

A1. Pin assignment of other connectors in MIC-3042A backplane

A1.1-1 Fan connector (CN5 – CN9)

1   3

□ ○ ○
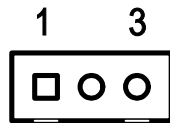
Pin	Signal
1	+12V
2	GND
3	Fan Speed

A1.1-2 Fan connector (FAN1)

1   2

3   4
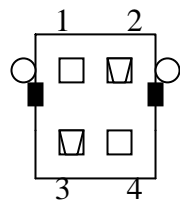
Pin	Signal
1	+12V
2	GND
3	Fan Speed
4	NC

A1.2 LED Connector (CN11)

8   1

○○○○○○○□

Pin	Signal	Pin	Signal
1	+3.3V	2	NC
3	+5V	4	NC
5	+12V	6	GND
7	GND	8	NC

A1.3 POWER ON Connector(CN12)

Pin	Signal
1	PS_ON#
2	GND

A1.4 Alarm board Connector (CN10)

```
25                        1
┌──────── ── ────────────┐
│○○○○○○○○○○○○○□│
│○○○○○○○○○○○○○○│
└─────────────────────────┘
26                        2
```

Pin	Signal	Pin	Signal
1	+5V	2	+5V
3	+5V	4	NC
5	+5V	6	+3.3V
7	FAN1	8	NC
9	FAN2	10	-12V
11	FAN3	12	NC
13	FAN4	14	+12V
15	+5VSB	16	NC
17	FAN5	18	T_SCLK (Clock of I^2C bus to temperature sensor)
19	FAN6	20	T_SDAT (Data of I^2C bus to temperature sensor)
21	B_SCLK (Clock of I^2C bus to system)	22	POWER_ATX_ON#
23	B_SDAT (Data of I^2C bus to system)	24	NC
25	GND	26	GND

A1.5 Shelf ID DIP Switch (SW4)

Pin	Signal	Pin	Signal
1-12	SGA0	2-11	SGA1
3-10	SGA2	4-9	SGA3
5-8	SGA4	6-7	NC

A1.6 power fail indicator Connector (CN1~CN3)

Pin	Signal
1	PSUx-FAL-1#
2	GND

Note: "x" means CNx #, i.e. CN #2 would have "PSU1-FAL-1#",and so does other connector

A1.7 power in Connector (AC_DCIN1)

Pin	Signal	Pin	Signal
1	ACL/-DC-IN-1	13	ACL/-DC-IN-1
2	N/C	14	N/C
3	ACN/+DC-IN-1	15	ACN/+DC-IN-1
4	N/C	16	N/C
5	ACL/-DC-IN-2	17	ACL/-DC-IN-2
6	N/C	18	N/C
7	ACN/+DC-IN-2	19	ACN/+DC-IN-2
8	N/C	20	N/C
9	ACL/-DC-IN-3	21	ACL/-DC-IN-3
10	N/C	22	N/C
11	ACN/+DC-IN-3	23	ACN/+DC-IN-3
12	N/C	24	N/C

A1.8 CPCI-POSITRONIC-PWR-CONN (POWERIN1~ POWERIN3)

Pin	Signal	Pin	Signal
1	+5V	26	RSV
2	+5V	27	EN#
3	+5V	28	GA1
4	+5V	29	+5VADJ
5	GND	30	+5VSENSE
6	GND	31	GA2
7	GND	32	+3.3VADJ
8	GND	33	+3.3VSENSE
9	GND	34	SRTN
10	GND	35	+5VSHARE
11	GND	36	+12VSENSE
12	GND	37	IPMBSCL
13	+3.3V	38	DEG#
14	+3.3V	39	INH#
15	+3.3V	40	IPMBSDA
16	+3.3V	41	+3.3VSHARE
17	+3.3V	42	FAL#
18	+3.3V	43	IPMBPWR
19	GND	44	+12VSHARE
20	+12V	45	CGND
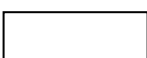
21	-12V	46	CAN/+DCIN
22	GND	47	ACL/-DCIN
23	RSV	48	FIXPIN
24	GND	49	FIXPIN
25	GA0		

A2. Pin assignment of P1~P5 connectors in MIC-3042A backplane

A2.1 System Slot S1P1 Connector

Table A2-1: System slot S1P1 connector

Pin	Z	A	B	C	D	E	F
25	GND	+5V	REQ64#	ENUM#	+3.3V	+5V	GND
24	GND	AD[1]	+5V	V(I/O)	AD[0]	ACK64#	GND
23	GND	+3.3V	AD[4]	AD[3]	+5V	AD[2]	GND
22	GND	AD[7]	GND	+3.3V	AD[6]	AD[5]	GND
21	GND	+3.3V	AD[9]	AD[8]	M66EN	C/BE[0]#	GND
20	GND	AD[12]	GND	V(I/O)	AD[11]	AD[10]	GND
19	GND	+3.3V	AD[15]	AD[14]	GND	AD[13]	GND
18	GND	SERR#	GND	+3.3V	PAR	C/BE[1]#	GND
17	GND	+3.3V	IPMBSCL	IPMBSDA	GND	PERR#	GND
16	GND	DEVSEL#	GND	V(I/O)	STOP#	LOCK#	GND
15	GND	+3.3V	FRAME#	IRDY#	GND(BD_SEL#)	TRDY#	GND
12-14				Key Area			
11	GND	AD[18]	AD[17]	AD[16]	GND	C/BE[2]#	GND
10	GND	AD[21]	GND	+3.3V	AD[20]	AD[19]	GND
9	GND	C/BE[3]#	GND(IDSEL)	AD[23]	GND	AD[22]	GND
8	GND	AD[26]	GND	V(I/O)	AD[25]	AD[24]	GND
7	GND	AD[30]	AD[29]	AD[28]	GND	AD[27]	GND
6	GND	REQ0#	GND	+3.3V	CLK0	AD[31]	GND
5	GND	BRSVP1A5	RRSVP1B5	PCI_RST#	GND	GNT0#	GND
4	GND	IPMBPWR	Healthy#	V(I/O)	INTP	INTS	GND
3	GND	INTA#	INTB#	INTC#	+5V	INTD#	GND
2	GND	TCK	+5V	TMS	N/A	N/A	GND
1	GND	+5V	-12V	TRST#	+12V	+5V	GND

☐ = long pins   ☐ = short pins   ☐ = medium length pins

#: Low active

A2.2 System Slot S1P2 Connector

Table A2-2: System slot S1P2 connector

Pin	Z	A	B	C	D	E	F
22	GND	GA4	GA3	GA2	GA1	GA0	GND
21	GND	CLK6	GND	RSV	RSV	RSV	GND
20	GND	CLK5	GND	RSV	GND	RSV	GND
19	GND	GND	GND	SMB_SDA	SMB_SCL	N/C	GND
18	GND	BRSVP2A18	BRSVP2B18	BRSVP2C18	GND	BRSVP2E18	GND
17	GND	BRSVP2A17	GND	PRST#	REQ6#	GNT6#	GND
16	GND	BRSVP2A16	BRSVP2B16	N/A	GND	BRSVP2E16	GND
15	GND	BRSVP2A15	GND	FAL#	REQ5#	GNT5#	GND
14	GND	AD[35]	AD[34]	AD[33]	GND	AD[32]	GND
13	GND	AD[38]	GND	V(I/O)	AD[37]	AD[36]	GND
12	GND	AD[42]	AD[41]	AD[40]	GND	AD[39]	GND
11	GND	AD[45]	GND	V(I/O)	AD[44]	AD[43]	GND
10	GND	AD[49]	AD[48]	AD[47]	GND	AD[46]	GND
9	GND	AD[52]	GND	V(I/O)	AD[51]	AD[50]	GND
8	GND	AD[56]	AD[55]	AD[54]	GND	AD[53]	GND
7	GND	AD[59]	GND	V(I/O)	AD[58]	AD[57]	GND
6	GND	AD[63]	AD[62]	AD[61]	GND	AD[60]	GND
5	GND	C/BE[5]#	GND	V(I/O)	C/BE[4]#	PAR64	GND
4	GND	V(I/O)	BRSVP2B4	C/BE[7]#	GND	C/BE[6]#	GND
3	GND	CLK4	GND	GNT3#	REQ4#	GNT4#	GND
2	GND	CLK2	CLK3	GND(SYSEN#)	GNT2#	REQ3#	GND
1	GND	CLK1	GND	REQ1#	GNT1#	REQ2#	GND

#: Low active

Note: GA[4…0] shall be used for geographic addressing on the backplane

A2.3 System Slot S1P3 Connector

Table A2-3: System slot S1P3 connector

Pin	Z	A	B	C	D	E	F
19	GND	SGA4	SGA3	SGA2	SGA1	SGA0	GND
18	GND	LP1A_DA+	LP1A_DA-	GND	LP1A_DC+	LP1A_DC-	GND
17	GND	LP1A_DB+	LP1A_DB-	GND	LP1A_DD+	LP1A_DD-	GND
16	GND	LP2B_DA+	LP2B_DA-	GND	LP2B_DC+	LP2B_DC-	GND
15	GND	LP2B_DB+	LP2B_DB-	GND	LP2B_DD+	LP2B_DD-	GND
14	GND	N/C	N/C	N/C	N/C	N/C	GND
13	GND	N/C	N/C	N/C	N/C	N/C	GND
12	GND	N/C	N/C	N/C	N/C	N/C	GND
11	GND	N/C	N/C	N/C	N/C	N/C	GND
10	GND	N/C	N/C	N/C	N/C	N/C	GND
9	GND	N/C	N/C	N/C	N/C	N/C	GND
8	GND	N/C	N/C	N/C	N/C	N/C	GND
7	GND	N/C	N/C	N/C	N/C	N/C	GND
6	GND	N/C	N/C	N/C	N/C	N/C	GND
5	GND	N/C	N/C	N/C	N/C	N/C	GND
4	GND	N/C	N/C	N/C	N/C	N/C	GND
3	GND	N/C	N/C	N/C	N/C	N/C	GND
2	GND	N/C	N/C	N/C	N/C	N/C	GND
1	GND	N/C	N/C	N/C	N/C	N/C	GND

#: Low active

A2.4 System Slot S1P4 Connector

Table A2-4: System slot S1P4 connector

Pin	Z	A	B	C	D	E	F
25	GND	N/C	N/C	N/C	N/C	N/C	GND
24	GND	N/C	N/C	N/C	N/C	N/C	GND
23	GND	N/C	N/C	N/C	N/C	N/C	GND
22	GND	N/C	N/C	N/C	N/C	N/C	GND
21	GND	N/C	N/C	N/C	N/C	N/C	GND
20	GND	N/C	N/C	N/C	N/C	N/C	GND
19	GND	N/C	N/C	N/C	N/C	N/C	GND
18	GND	N/C	N/C	N/C	N/C	N/C	GND
17	GND	N/C	N/C	N/C	N/C	N/C	GND
16	GND	N/C	N/C	N/C	N/C	N/C	GND
15	GND	N/C	N/C	N/C	N/C	N/C	GND
12-14	Key Area						
11	GND	N/C	N/C	N/C	N/C	N/C	GND
10	GND	N/C	N/C	N/C	N/C	N/C	GND
9	GND	N/C	N/C	N/C	N/C	N/C	GND
8	GND	N/C	N/C	N/C	N/C	N/C	GND
7	GND	N/C	N/C	N/C	N/C	N/C	GND
6	GND	N/C	N/C	N/C	N/C	N/C	GND
5	GND	N/C	N/C	N/C	N/C	N/C	GND
4	GND	N/C	N/C	N/C	N/C	N/C	GND
3	GND	N/C	N/C	N/C	N/C	N/C	GND
2	GND	N/C	N/C	N/C	N/C	N/C	GND
1	GND	N/C	N/C	N/C	N/C	N/C	GND

A2.5 System Slot S1P5 Connector
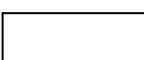
Table A2-5: System slot S1P5 connector

Pin	Z	A	B	C	D	E	F
22	GND	N/C	N/C	N/C	N/C	N/C	GND
21	GND	N/C	N/C	N/C	N/C	N/C	GND
20	GND	N/C	N/C	N/C	N/C	N/C	GND
19	GND	N/C	N/C	N/C	N/C	N/C	GND
18	GND	N/C	N/C	N/C	N/C	N/C	GND
17	GND	N/C	N/C	N/C	N/C	N/C	GND
16	GND	N/C	N/C	N/C	N/C	N/C	GND
15	GND	N/C	N/C	N/C	N/C	N/C	GND
14	GND	N/C	N/C	N/C	N/C	N/C	GND
13	GND	N/C	N/C	N/C	N/C	N/C	GND
12	GND	N/C	N/C	N/C	N/C	N/C	GND
11	GND	N/C	N/C	N/C	N/C	N/C	GND
10	GND	N/C	N/C	N/C	N/C	N/C	GND
9	GND	N/C	N/C	N/C	N/C	N/C	GND
8	GND	N/C	N/C	N/C	N/C	N/C	GND
7	GND	N/C	N/C	N/C	N/C	N/C	GND
6	GND	N/C	N/C	N/C	N/C	N/C	GND
5	GND	N/C	N/C	N/C	N/C	N/C	GND
4	GND	N/C	N/C	N/C	N/C	N/C	GND
3	GND	N/C	N/C	N/C	N/C	N/C	GND
2	GND	N/C	N/C	N/C	N/C	N/C	GND
1	GND	N/C	N/C	N/C	N/C	N/C	GND

A2.6 Peripheral Slots (S2~S7) P1 Connector

Table A2-6: Peripheral Slot (S2~S7) P1 Connector

Pin	Z	A	B	C	D	E	F
25	GND	+5V	REQ64#	ENUM#	+3.3V	+5V	GND
24	GND	AD[1]	+5V	V(I/O)	AD[0]	ACK64#	GND
23	GND	+3.3V	AD[4]	AD[3]	+5V	AD[2]	GND
22	GND	AD[7]	GND	+3.3V	AD[6]	AD[5]	GND
21	GND	+3.3V	AD[9]	AD[8]	M66EN	C/BE[0]#	GND
20	GND	AD[12]	GND	V(I/O)	AD[11]	AD[10]	GND
19	GND	+3.3V	AD[15]	AD[14]	GND	AD[13]	GND
18	GND	SERR#	GND	+3.3V	PAR	C/BE[1]#	GND
17	GND	+3.3V	IPMBSCL	IPMBSDA	GND	PERR#	GND
16	GND	DEVSEL#	GND	V(I/O)	STOP#	LOCK#	GND
15	GND	+3.3V	FRAME#	IRDY#	BD_SEL	TRDY#	GND
12-14	Key Area						
11	GND	AD[18]	AD[17]	AD[16]	GND	C/BE[2]#	GND
10	GND	AD[21]	GND	+3.3V	AD[20]	AD[19]	GND
9	GND	C/BE[3]#	IDSEL	AD[23]	GND	AD[22]	GND
8	GND	AD[26]	GND	V(I/O)	AD[25]	AD[24]	GND
7	GND	AD[30]	AD[29]	AD[28]	GND	AD[27]	GND
6	GND	**REQ#**	GND	+3.3V	**CLK#**	AD[31]	GND
5	GND	BRSVP1A5	RRSVP1B5	PCI_RST#	GND	**GNT#**	GND
4	GND	IPMBPWR	Healthy#	V(I/O)	INTP	INTS	GND
3	GND	INTA#	INTB#	INTC#	+5V	INTD#	GND
2	GND	TCK	+5V	TMS	N/A	N/A	GND
1	GND	+5V	-12V	TRST#	+12V	+5V	GND

= long pins   = short pins   = medium length pins

#: Low active

A2.7 Peripheral Slots (S2~S7) P2 Connector

Table A2-7: Peripheral Slot (S2~S7) P2 Connector

Pin	Z	A	B	C	D	E	F
22	GND	GA4	GA3	GA2	GA1	GA0	GND
21	GND	RSV	GND	RSV	RSV	RSV	GND
20	GND	RSV	RSV	RSV	GND	RSV	GND
19	GND	RSV	GND	SMB_SDA	SMB_SCL	RSV	GND
18	GND	BRSVP2A18	BRSVP2B18	BRSVP2C18	BRSVP2D18	BRSVP2E18	GND
17	GND	BRSVP2A17	GND	PRST#	RSV	RSV	GND
16	GND	BRSVP2A16	BRSVP2B16	RSV	GND	BRSVP2E16	GND
15	GND	BRSVP2A15	GND	FAL#	RSV	RSV	GND
14	GND	AD[35]	AD[34]	AD[33]	GND	AD[32]	GND
13	GND	AD[38]	GND	V(I/O)	AD[37]	AD[36]	GND
12	GND	AD[42]	AD[41]	AD[40]	GND	AD[39]	GND
11	GND	AD[45]	GND	V(I/O)	AD[44]	AD[43]	GND
10	GND	AD[49]	AD[48]	AD[47]	GND	AD[46]	GND
9	GND	AD[52]	GND	V(I/O)	AD[51]	AD[50]	GND
8	GND	AD[56]	AD[55]	AD[54]	GND	AD[53]	GND
7	GND	AD[59]	GND	V(I/O)	AD[58]	AD[57]	GND
6	GND	AD[63]	AD[62]	AD[61]	GND	AD[60]	GND
5	GND	C/BE[5]#	GND	V(I/O)	C/BE[4]#	PAR64	GND
4	GND	V(I/O)	BRSVP2B4	C/BE[7]#	GND	C/BE[6]#	GND
3	GND	RSV	GND	RSV	RSV	RSV	GND
2	GND	RSV	RSV	UNC	RSV	RSV	GND
1	GND	RSV	GND	RSV	RSV	RSV	GND

#: Low active

Note: GA[4...0] shall be used for geographic addressing on the backplane

A2.8 Peripheral Slots (S2~S7) P3 Connector

Table A2-8: Peripheral Slot (S3~S6,S8~S10,S12~S15,S17~S19) P3 Connector

Pin	Z	A	B	C	D	E	F
19	GND	SGA4	SGA3	SGA2	SGA1	SGA0	GND
18	GND	LPxA_DA+	LPxA_DA-	GND	LPxA_DC+	LPxA_DC-	GND
17	GND	LPxA_DB+	LPxA_DB-	GND	LPxA_DD+	LPxA_DD-	GND
16	GND	LPxB_DA+	LPxB_DA-	GND	LPxB_DC+	LPxB_DC-	GND
15	GND	LPxB_DB+	LPxB_DB-	GND	LPxB_DD+	LPxB_DD-	GND
14	GND	N/C	N/C	N/C	N/C	N/C	GND
13	GND	N/C	N/C	N/C	N/C	N/C	GND
12	GND	N/C	N/C	N/C	N/C	N/C	GND
11	GND	N/C	N/C	N/C	N/C	N/C	GND
10	GND	N/C	N/C	N/C	N/C	N/C	GND
9	GND	N/C	N/C	N/C	N/C	N/C	GND
8	GND	N/C	N/C	N/C	N/C	N/C	GND
7	GND	N/C	N/C	N/C	N/C	N/C	GND
6	GND	N/C	N/C	N/C	N/C	N/C	GND
5	GND	N/C	N/C	N/C	N/C	N/C	GND
4	GND	N/C	N/C	N/C	N/C	N/C	GND
3	GND	N/C	N/C	N/C	N/C	N/C	GND
2	GND	N/C	N/C	N/C	N/C	N/C	GND
1	GND	N/C	N/C	N/C	N/C	N/C	GND
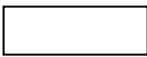
#: Low active

Note: "x" means slot #, i.e. Slot #2 would have "LP4B_DB+" in A15, "LP3B_DB+" in A17,and so does other pins.

A2.9 Peripheral Slots (S2~S7) P4 Connector

Table A2-9: Peripheral Slot (S2~S7) P4 Connector

Pin	Z	A	B	C	D	E	F
25	N/C	SGA4	SGA3	SGA2	SGA1	SGA0	FGND
24	N/C	GA4	GA3	GA2	GA1	GA0	FGND
23	N/C	+12V	#CT_RT	#CT_EN	-12V	CT_MC	FGND
22	N/C	#PF_S0	RSV	RSV	RSV	RSV	FGND
21	N/C	-SEL_Vbat	#PF_S1	RSV	RSV	SELVbatRtn	FGND
20	N/C	N/C	N/C	N/C	N/C	N/C	GND
19	N/C	N/C	N/C	N/C	N/C	N/C	GND
18	N/C	VRG	N/C	N/C	N/C	VRGRtn	GND
17	N/C	N/C	N/C	N/C	N/C	N/C	GND
16	N/C	N/C	N/C	N/C	N/C	N/C	GND
15	N/C	-Vbat	N/C	N/C	N/C	VbatRtn	GND
12-14	Key Area						
11	N/C	CT_D29	CT_D30	CT_D31	V(I/O)	#CT_FA	GND
10	N/C	CT_D27	+3.3V	CT_D28	+5V	#CT_FB	GND
9	N/C	CT_D24	CT_D25	CT_D26	GND	#FR_CP	GND
8	N/C	CT_D21	CT_D22	CT_D23	+5V	CT_C8A	GND
7	N/C	CT_D19	+5V	CT_D20	GND	CT_C8B	GND
6	N/C	CT_D16	CT_D17	CT_D18	GND	CT_N1	GND
5	N/C	CT_D13	CT_D14	CT_D15	+3.3V	CT_N2	GND
4	N/C	CT_D11	+5V	CT_D12	+3.3V	SCLK	GND
3	N/C	CT_D8	CT_D9	CT_D10	GND	SCLK-D	GND
2	N/C	CT_D4	CT_D5	CT_D6	CT_D7	GND	GND
1	N/C	CT_D0	+3.3V	CT_D1	CT_D2	CT_D3	GND

= long pins   = short pins   = medium length pins

#: Low active

A2.10 Peripheral Slots (S2~S7) P5 Connector

Table A2-10: Peripheral Slot (S2~S7) P5 Connector

Pin	Z	A	B	C	D	E	F
22	GND	N/C	N/C	N/C	N/C	N/C	GND
21	GND	N/C	N/C	N/C	N/C	N/C	GND
20	GND	N/C	N/C	N/C	N/C	N/C	GND
19	GND	N/C	N/C	N/C	N/C	N/C	GND
18	GND	N/C	N/C	N/C	N/C	N/C	GND
17	GND	N/C	N/C	N/C	N/C	N/C	GND
16	GND	N/C	N/C	N/C	N/C	N/C	GND
15	GND	N/C	N/C	N/C	N/C	N/C	GND
14	GND	N/C	N/C	N/C	N/C	N/C	GND
13	GND	N/C	N/C	N/C	N/C	N/C	GND
12	GND	N/C	N/C	N/C	N/C	N/C	GND
11	GND	N/C	N/C	N/C	N/C	N/C	GND
10	GND	N/C	N/C	N/C	N/C	N/C	GND
9	GND	N/C	N/C	N/C	N/C	N/C	GND
8	GND	N/C	N/C	N/C	N/C	N/C	GND
7	GND	N/C	N/C	N/C	N/C	N/C	GND
6	GND	N/C	N/C	N/C	N/C	N/C	GND
5	GND	N/C	N/C	N/C	N/C	N/C	GND
4	GND	N/C	N/C	N/C	N/C	N/C	GND
3	GND	N/C	N/C	N/C	N/C	N/C	GND
2	GND	N/C	N/C	N/C	N/C	N/C	GND
1	GND	N/C	N/C	N/C	N/C	N/C	GND

Note: "x" means slot #, i.e. Slot #2 would have "LP3B_DB+" in A15, and so does other pins.

A2.11 Fabric Slot S8P1 Connector

Table A2-11: Fabric Slot S8P1 Connector

Pin	Z	A	B	C	D	E	F
25	GND	+5V	REQ64#	ENUM#	+3.3V	+5V	GND
24	GND	AD[1]	+5V	V(I/O)	AD[0]	ACK64#	GND
23	GND	+3.3V	AD[4]	AD[3]	+5V	AD[2]	GND
22	GND	AD[7]	GND	+3.3V	AD[6]	AD[5]	GND
21	GND	+3.3V	AD[9]	AD[8]	M66EN	C/BE[0]#	GND
20	GND	AD[12]	GND	V(I/O)	AD[11]	AD[10]	GND
19	GND	+3.3V	AD[15]	AD[14]	GND	AD[13]	GND
18	GND	SERR#	GND	+3.3V	PAR	C/BE[1]#	GND
17	GND	+3.3V	IPMBSCL	IPMBSDA	GND	PERR#	GND
16	GND	DEVSEL#	GND	V(I/O)	STOP#	LOCK#	GND
15	GND	+3.3V	FRAME#	IRDY#	BD_SEL	TRDY#	GND
12-14				Key Area			
11	GND	AD[18]	AD[17]	AD[16]	GND	C/BE[2]#	GND
10	GND	AD[21]	GND	+3.3V	AD[20]	AD[19]	GND
9	GND	C/BE[3]#	IDSEL	AD[23]	GND	AD[22]	GND
8	GND	AD[26]	GND	V(I/O)	AD[25]	AD[24]	GND
7	GND	AD[30]	AD[29]	AD[28]	GND	AD[27]	GND
6	GND	**REQ#**	GND	+3.3V	**CLK#**	AD[31]	GND
5	GND	BRSVP1A5	RRSVP1B5	PCI_RST#	GND	**GNT#**	GND
4	GND	IPMBPWR	Healthy#	V(I/O)	INTP	INTS	GND
3	GND	INTA#	INTB#	INTC#	+5V	INTD#	GND
2	GND	TCK	+5V	TMS	N/A	N/A	GND
1	GND	+5V	-12V	TRST#	+12V	+5V	GND

▭ = long pins   ▭ = short pins   ▭ = medium length pins

#: Low active

A2.12 Fabric Slot S8P2 Connector

Table A2-12: Fabric Slot S8P2 Connector

Pin	Z	A	B	C	D	E	F
22	GND	GA4	GA3	GA2	GA1	GA0	GND
21	GND	RSV	GND	RSV	RSV	RSV	GND
20	GND	RSV	RSV	RSV	GND	RSV	GND
19	GND	RSV	GND	SMB_SDA	SMB_SCL	RSV	GND
18	GND	BRSVP2A18	BRSVP2B18	BRSVP2C18	BRSVP2D18	BRSVP2E18	GND
17	GND	BRSVP2A17	GND	PRST#	RSV	RSV	GND
16	GND	BRSVP2A16	BRSVP2B16	RSV	GND	BRSVP2E16	GND
15	GND	BRSVP2A15	GND	FAL#	RSV	RSV	GND
14	GND	AD[35]	AD[34]	AD[33]	GND	AD[32]	GND
13	GND	AD[38]	GND	V(I/O)	AD[37]	AD[36]	GND
12	GND	AD[42]	AD[41]	AD[40]	GND	AD[39]	GND
11	GND	AD[45]	GND	V(I/O)	AD[44]	AD[43]	GND
10	GND	AD[49]	AD[48]	AD[47]	GND	AD[46]	GND
9	GND	AD[52]	GND	V(I/O)	AD[51]	AD[50]	GND
8	GND	AD[56]	AD[55]	AD[54]	GND	AD[53]	GND
7	GND	AD[59]	GND	V(I/O)	AD[58]	AD[57]	GND
6	GND	AD[63]	AD[62]	AD[61]	GND	AD[60]	GND
5	GND	C/BE[5]#	GND	V(I/O)	C/BE[4]#	PAR64	GND
4	GND	V(I/O)	BRSVP2B4	C/BE[7]#	GND	C/BE[6]#	GND
3	GND	RSV	GND	RSV	RSV	RSV	GND
2	GND	RSV	RSV	UNC	RSV	RSV	GND
1	GND	RSV	GND	RSV	RSV	RSV	GND

#: Low active

Note: GA[4…0] shall be used for geographic addressing on the backplane

A2.13 Fabric Slot S8P3 Connector

Table A2-13: Fabric slot S8P3 connector

Pin	Z	A	B	C	D	E	F
19	GND	SGA4	SGA3	SGA2	SGA1	SGA0	GND
18	GND	N/C	N/C	GND	N/C	N/C	GND
17	GND	N/C	N/C	GND	N/C	N/C	GND
16	GND	LP8B_DB+	LP8B_DB-	GND	LP8B_DD+	LP8B_DD-	GND
15	GND	LP8B_DA+	LP8B_DA-	GND	LP8B_DC+	LP8B_DC-	GND
14	GND	LP7B_DB+	LP7B_DB-	GND	LP7B_DD+	LP7B_DD-	GND
13	GND	LP7B_DA+	LP7B_DA-	GND	LP7B_DC+	LP7B_DC-	GND
12	GND	LP6B_DB+	LP6B_DB-	GND	LP6B_DD+	LP6B_DD-	GND
11	GND	LP6B_DA+	LP6B_DA-	GND	LP6B_DC+	LP6B_DC-	GND
10	GND	LP5B_DB+	LP5B_DB-	GND	LP5B_DD+	LP5B_DD-	GND
9	GND	LP5B_DA+	LP5B_DA-	GND	LP5B_DC+	LP5B_DC-	GND
8	GND	LP4B_DB+	LP4B_DB-	GND	LP4B_DD+	LP4B_DD-	GND
7	GND	LP4B_DA+	LP4B_DA-	GND	LP4B_DC+	LP4B_DC-	GND
6	GND	LP3B_DB+	LP3B_DB-	GND	LP3B_DD+	LP3B_DD-	GND
5	GND	LP3B_DA+	LP3B_DA-	GND	LP3B_DC+	LP3B_DC-	GND
4	GND	LP2B_DB+	LP2B_DB-	GND	LP2B_DD+	LP2B_DD-	GND
3	GND	LP2B_DA+	LP2B_DA-	GND	LP2B_DC+	LP2B_DC-	GND
2	GND	LP1B_DB+	LP2B_DB-	GND	LP1B_DD+	LP1B_DD-	GND
1	GND	LP1B_DA+	LP2B_DA-	GND	LP1B_DC+	LP1B_DC-	GND

#: Low active

A2.14 Fabric Slot S8P4 Connector

Table A2-14: Fabric slot S8P4 connector

Pin	Z	A	B	C	D	E	F
25	GND	N/C	N/C	GND	N/C	N/C	GND
24	GND	N/C	N/C	GND	N/C	N/C	GND
23	GND	N/C	N/C	GND	N/C	N/C	GND
22	GND	N/C	N/C	GND	N/C	N/C	GND
21	GND	N/C	N/C	GND	N/C	N/C	GND
20	GND	N/C	N/C	GND	N/C	N/C	GND
19	GND	N/C	N/C	GND	N/C	N/C	GND
18	GND	N/C	N/C	GND	N/C	N/C	GND
17	GND	N/C	N/C	GND	N/C	N/C	GND
16	GND	N/C	N/C	GND	N/C	N/C	GND
15	GND	N/C	N/C	GND	N/C	N/C	GND
12-14	Key Area						
11	GND	N/C	N/C	N/C	N/C	N/C	GND
10	GND	N/C	N/C	N/C	N/C	N/C	GND
9	GND	N/C	N/C	N/C	N/C	N/C	GND
8	GND	N/C	N/C	N/C	N/C	N/C	GND
7	GND	N/C	N/C	N/C	N/C	N/C	GND
6	GND	N/C	N/C	N/C	N/C	N/C	GND
5	GND	N/C	N/C	N/C	N/C	N/C	GND
4	GND	N/C	N/C	N/C	N/C	N/C	GND
3	GND	N/C	N/C	N/C	N/C	N/C	GND
2	GND	N/C	N/C	N/C	N/C	N/C	GND
1	GND	N/C	N/C	N/C	N/C	N/C	GND

A2.15 Fabric Slot　S8P5 Connector

Table A2-15: Fabric slot　S8P5 connector

Pin	Z	A	B	C	D	E	F
22	GND	N/C	N/C	GND	N/A	N/A	GND
21	GND	N/C	N/C	GND	N/A	N/A	GND
20	GND	N/C	N/C	GND	N/C	N/C	GND
19	GND	N/C	N/C	GND	N/C	N/C	GND
18	GND	N/C	N/C	GND	N/C	N/C	GND
17	GND	N/C	N/C	GND	N/C	N/C	GND
16	GND	N/C	N/C	GND	N/C	N/C	GND
15	GND	N/C	N/C	GND	N/C	N/C	GND
14	GND	N/C	N/C	GND	N/C	N/C	GND
13	GND	N/C	N/C	GND	N/C	N/C	GND
12	GND	LP14B_DB+	LP14B_DB-	GND	LP14B_DD+	LP14B_DD-	GND
11	GND	LP14B_DA+	LP14B_DA-	GND	LP14B_DC+	LP14B_DC-	GND
10	GND	LP13B_DB+	LP13B_DB-	GND	LP13B_DD+	LP13B_DD-	GND
9	GND	LP13B_DA+	LP13B_DA-	GND	LP13B_DC+	LP13B_DC-	GND
8	GND	LP12B_DB+	LP12B_DB-	GND	LP12B_DD+	LP12B_DD-	GND
7	GND	LP12B_DA+	LP12B_DA-	GND	LP12B_DC+	LP12B_DC-	GND
6	GND	LP11B_DB+	LP11B_DB-	GND	LP11B_DD+	LP11B_DD-	GND
5	GND	LP11B_DA+	LP11B_DA-	GND	LP11B_DC+	LP11B_DC-	GND
4	GND	LP10B_DB+	LP10B_DB-	GND	LP10B_DD+	LP10B_DD-	GND
3	GND	LP10B_DA+	LP10B_DA-	GND	LP10B_DC+	LP10B_DC-	GND
2	GND	LP9B_DB+	LP9B_DB-	GND	LP9B_DD+	LP9B_DD-	GND
1	GND	LP9B_DA+	LP9B_DA-	GND	LP9B_DC+	LP9B_DC-	GND

Appendix B

Pin Assignments of

MIB-3042B backplane

B1. Pin assignment of other connectors in MIC-3042BA backplane

B1.1-1 Fan connector (CN5 – CN9)
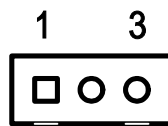
Pin	Signal
1	+12V
2	GND
3	Fan Speed

B1.1-2 Fan connector (FAN1)
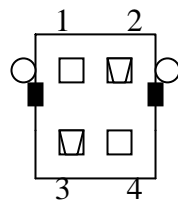
Pin	Signal
1	+12V
2	GND
3	Fan Speed
4	NC

B1.2 LED Connector (CN11)

Pin	Signal	Pin	Signal
1	+3.3V	2	NC
3	+5V	4	NC
5	+12V	6	GND
7	GND	8	NC

B1.3 POWER ON Connector(CN12)

Pin	Signal
1	PS_ON#
2	GND

B1.4 Alarm board Connector (CN10)

Pin	Signal	Pin	Signal
1	+5V	2	+5V
3	+5V	4	NC
5	+5V	6	+3.3V
7	FAN1	8	NC
9	FAN2	10	-12V
11	FAN3	12	NC
13	FAN4	14	+12V
15	+5VSB	16	NC
17	FAN5	18	T_SCLK (Clock of I^{2}C bus to temperature sensor)
19	FAN6	20	T_SDAT (Data of I^{2}C bus to temperature sensor)
21	B_SCLK (Clock of I^{2}C bus to system)	22	POWER_ATX_ON#
23	B_SDAT (Data of I^{2}C bus to system)	24	NC
25	GND	26	GND

B1.5 Shelf ID DIP Switch (SW4)

Pin	Signal	Pin	Signal
1-12	SGA0	2-11	SGA1
3-10	SGA2	4-9	SGA3
5-8	SGA4	6-7	NC

B1.6 power fail indicator Connector (CN1~CN3)

Pin	Signal
1	PSUx-FAL-1#
2	GND

Note: "x" means CNx #, i.e. CN #2 would have "PSU1-FAL-1#",and so does other connector

B1.7 power in Connector (AC_DCIN1)

Table A-7: Power in Connector

Pin	Signal	Pin	Signal
1	ACL/-DC-IN-1	13	ACL/-DC-IN-1
2	N/C	14	N/C
3	ACN/+DC-IN-1	15	ACN/+DC-IN-1
4	N/C	16	N/C
5	ACL/-DC-IN-2	17	ACL/-DC-IN-2
6	N/C	18	N/C
7	ACN/+DC-IN-2	19	ACN/+DC-IN-2
8	N/C	20	N/C
9	ACL/-DC-IN-3	21	ACL/-DC-IN-3
10	N/C	22	N/C
11	ACN/+DC-IN-3	23	ACN/+DC-IN-3
12	N/C	24	N/C

B1.8 CPCI-POSITRONIC-PWR-CONN (POWERIN1~ POWERIN3)

Pin	Signal	Pin	Signal
1	+5V	26	RSV
2	+5V	27	EN#
3	+5V	28	GA1
4	+5V	29	+5VADJ
5	GND	30	+5VSENSE
6	GND	31	GA2
7	GND	32	+3.3VADJ
8	GND	33	+3.3VSENSE
9	GND	34	SRTN
10	GND	35	+5VSHARE
11	GND	36	+12VSENSE
12	GND	37	IPMBSCL
13	+3.3V	38	DEG#
14	+3.3V	39	INH#
15	+3.3V	40	IPMBSDA
16	+3.3V	41	+3.3VSHARE
17	+3.3V	42	FAL#
18	+3.3V	43	IPMBPWR
19	GND	44	+12VSHARE
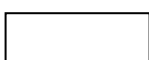
20	+12V	45	CGND
21	-12V	46	CAN/+DCIN
22	GND	47	ACL/-DCIN
23	RSV	48	FIXPIN
24	GND	49	FIXPIN
25	GA0		

B2. Pin assignment of P1~P5 connectors in MIC-3042 backplane

B2.1 System Slot S2P1 Connector

Table B2-1: System slot S2P1 connector

Pin	Z	A	B	C	D	E	F
25	GND	+5V	REQ64#	ENUM#	+3.3V	+5V	GND
24	GND	AD[1]	+5V	V(I/O)	AD[0]	ACK64#	GND
23	GND	+3.3V	AD[4]	AD[3]	+5V	AD[2]	GND
22	GND	AD[7]	GND	+3.3V	AD[6]	AD[5]	GND
21	GND	+3.3V	AD[9]	AD[8]	M66EN	C/BE[0]#	GND
20	GND	AD[12]	GND	V(I/O)	AD[11]	AD[10]	GND
19	GND	+3.3V	AD[15]	AD[14]	GND	AD[13]	GND
18	GND	SERR#	GND	+3.3V	PAR	C/BE[1]#	GND
17	GND	+3.3V	IPMBSCL	IPMBSDA	GND	PERR#	GND
16	GND	DEVSEL#	GND	V(I/O)	STOP#	LOCK#	GND
15	GND	+3.3V	FRAME#	IRDY#	GND(BD_SEL#)	TRDY#	GND
12-14				Key Area			
11	GND	AD[18]	AD[17]	AD[16]	GND	C/BE[2]#	GND
10	GND	AD[21]	GND	+3.3V	AD[20]	AD[19]	GND
9	GND	C/BE[3]#	GND(IDSEL)	AD[23]	GND	AD[22]	GND
8	GND	AD[26]	GND	V(I/O)	AD[25]	AD[24]	GND
7	GND	AD[30]	AD[29]	AD[28]	GND	AD[27]	GND
6	GND	REQ0#	GND	+3.3V	CLK0	AD[31]	GND
5	GND	BRSVP1A5	RRSVP1B5	PCI_RST#	GND	GNT0#	GND
4	GND	IPMBPWR	Healthy#	V(I/O)	INTP	INTS	GND
3	GND	INTA#	INTB#	INTC#	+5V	INTD#	GND
2	GND	TCK	+5V	TMS	N/A	N/A	GND
1	GND	+5V	-12V	TRST#	+12V	+5V	GND

▨ = long pins   ▢ = short pins   ▭ = medium length pins

#: Low active

B2.2 System Slot S2P2 Connector

Table B2-2: System slot S2P2 connector

Pin	Z	A	B	C	D	E	F
22	GND	GA4	GA3	GA2	GA1	GA0	GND
21	GND	CLK6	GND	RSV	RSV	RSV	GND
20	GND	CLK5	GND	RSV	GND	RSV	GND
19	GND	GND	GND	SMB_SDA	SMB_SCL	N/C	GND
18	GND	BRSVP2A18	BRSVP2B18	BRSVP2C18	GND	BRSVP2E18	GND
17	GND	BRSVP2A17	GND	PRST#	REQ6#	GNT6#	GND
16	GND	BRSVP2A16	BRSVP2B16	N/A	GND	BRSVP2E16	GND
15	GND	BRSVP2A15	GND	FAL#	REQ5#	GNT5#	GND
14	GND	AD[35]	AD[34]	AD[33]	GND	AD[32]	GND
13	GND	AD[38]	GND	V(I/O)	AD[37]	AD[36]	GND
12	GND	AD[42]	AD[41]	AD[40]	GND	AD[39]	GND
11	GND	AD[45]	GND	V(I/O)	AD[44]	AD[43]	GND
10	GND	AD[49]	AD[48]	AD[47]	GND	AD[46]	GND
9	GND	AD[52]	GND	V(I/O)	AD[51]	AD[50]	GND
8	GND	AD[56]	AD[55]	AD[54]	GND	AD[53]	GND
7	GND	AD[59]	GND	V(I/O)	AD[58]	AD[57]	GND
6	GND	AD[63]	AD[62]	AD[61]	GND	AD[60]	GND
5	GND	C/BE[5]#	GND	V(I/O)	C/BE[4]#	PAR64	GND
4	GND	V(I/O)	BRSVP2B4	C/BE[7]#	GND	C/BE[6]#	GND
3	GND	CLK4	GND	GNT3#	REQ4#	GNT4#	GND
2	GND	CLK2	CLK3	GND(SYSEN#)	GNT2#	REQ3#	GND
1	GND	CLK1	GND	REQ1#	GNT1#	REQ2#	GND

#: Low active

Note: GA[4…0] shall be used for geographic addressing on the backplane

B2.3 System Slot S2P3 Connector

Table B2-3: System slot S2P3 connector

Pin	Z	A	B	C	D	E	F
19	GND	N/C	N/C	N/C	N/C	N/C	GND
18	GND	N/C	N/C	N/C	N/C	N/C	GND
17	GND	N/C	N/C	N/C	N/C	N/C	GND
16	GND	N/C	N/C	N/C	N/C	N/C	GND
15	GND	N/C	N/C	N/C	N/C	N/C	GND
14	GND	N/C	N/C	N/C	N/C	N/C	GND
13	GND	HDD_D0	HDD_D2	N/C	N/C	N/C	GND
12	GND	HDD_D1	HDD_D3	HDD_D12	HDD_D4	HDD_D7	GND
11	GND	HDD_D5	HDD_D9	HDD_D13	HDD_D6	HDD_D8	GND
10	GND	HDD_D15	HDD_D14	HDD_IOR#	HDD_D10	HDD_D11	GND
9	GND	HDD_LED	N/C	HDD_IOW#	HDD_DMREQ	HDD_CS3#	GND
8	GND	HDD_DRQ	HDD_CBDET	HDD_DA0	HDD_DA1	HDD_DA2	GND
7	GND	FDD_DIR#	HDD_DMACK#	HDD_RST#	HDD_IORDY	HDD_CS1#	GND
6	GND	N/C	N/C	FDD_WGATE#	FDD_HDSEL#	FDD_DS0#	GND
5	GND	N/C	N/C	FDD_STEP#	FDD_MTR0#	FDD_WDATA#	GND
4	GND	N/C	N/C	FDD_RDATA#	FDD_WRTPRT#	FDD_TRK0#	GND
3	GND	N/C	N/C	FDD_DRVEN0#	FDD_INDEX#	FDD_DSKCHG#	GND
2	GND	N/C	N/C	N/C	N/C	N/C	GND
1	GND	N/C	N/C	N/C	N/C	N/C	GND

#: Low active

B2.4 System Slot S2P4 Connector

Table B2-4: System slot S2P4 connector

Pin	Z	A	B	C	D	E	F
25	GND	N/C	N/C	N/C	N/C	N/C	GND
24	GND	N/C	N/C	N/C	N/C	N/C	GND
23	GND	N/C	N/C	N/C	N/C	N/C	GND
22	GND	N/C	N/C	N/C	N/C	N/C	GND
21	GND	N/C	N/C	N/C	N/C	N/C	GND
20	GND	N/C	N/C	N/C	N/C	N/C	GND
19	GND	N/C	N/C	N/C	N/C	N/C	GND
18	GND	N/C	N/C	N/C	N/C	N/C	GND
17	GND	N/C	N/C	N/C	N/C	N/C	GND
16	GND	N/C	N/C	N/C	N/C	N/C	GND
15	GND	N/C	N/C	N/C	N/C	N/C	GND
12-14	Key Area						
11	GND	N/C	N/C	N/C	N/C	N/C	GND
10	GND	N/C	N/C	N/C	N/C	N/C	GND
9	GND	N/C	N/C	N/C	N/C	N/C	GND
8	GND	N/C	N/C	N/C	N/C	N/C	GND
7	GND	N/C	N/C	N/C	N/C	N/C	GND
6	GND	N/C	N/C	N/C	N/C	N/C	GND
5	GND	N/C	N/C	N/C	N/C	N/C	GND
4	GND	N/C	N/C	N/C	N/C	N/C	GND
3	GND	N/C	N/C	N/C	N/C	N/C	GND
2	GND	N/C	N/C	N/C	N/C	N/C	GND
1	GND	N/C	N/C	N/C	N/C	N/C	GND

B2.5 System Slot S2P5 Connector
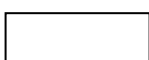
Table B2-5: System slot S2P5 connector

Pin	Z	A	B	C	D	E	F
22	GND	N/C	N/C	N/C	N/C	N/C	GND
21	GND	N/C	N/C	N/C	N/C	N/C	GND
20	GND	N/C	N/C	N/C	N/C	N/C	GND
19	GND	N/C	N/C	N/C	N/C	N/C	GND
18	GND	N/C	N/C	N/C	N/C	N/C	GND
17	GND	N/C	N/C	N/C	N/C	N/C	GND
16	GND	N/C	N/C	N/C	N/C	N/C	GND
15	GND	N/C	N/C	N/C	N/C	N/C	GND
14	GND	N/C	N/C	N/C	N/C	N/C	GND
13	GND	N/C	N/C	N/C	N/C	N/C	GND
12	GND	N/C	N/C	N/C	N/C	N/C	GND
11	GND	N/C	N/C	N/C	N/C	N/C	GND
10	GND	N/C	N/C	N/C	N/C	N/C	GND
9	GND	N/C	N/C	N/C	N/C	N/C	GND
8	GND	N/C	N/C	N/C	N/C	N/C	GND
7	GND	N/C	N/C	N/C	N/C	N/C	GND
6	GND	N/C	N/C	N/C	N/C	N/C	GND
5	GND	N/C	N/C	N/C	N/C	N/C	GND
4	GND	N/C	N/C	N/C	N/C	N/C	GND
3	GND	N/C	N/C	N/C	N/C	N/C	GND
2	GND	N/C	N/C	N/C	N/C	N/C	GND
1	GND	N/C	N/C	N/C	N/C	N/C	GND

B2.6 Peripheral Slots (S1, S3~S8) P1 Connector

Table B2-6: Peripheral Slot (S1, S3~S8) P1 Connector

Pin	Z	A	B	C	D	E	F
25	GND	+5V	REQ64#	ENUM#	+3.3V	+5V	GND
24	GND	AD[1]	+5V	V(I/O)	AD[0]	ACK64#	GND
23	GND	+3.3V	AD[4]	AD[3]	+5V	AD[2]	GND
22	GND	AD[7]	GND	+3.3V	AD[6]	AD[5]	GND
21	GND	+3.3V	AD[9]	AD[8]	M66EN	C/BE[0]#	GND
20	GND	AD[12]	GND	V(I/O)	AD[11]	AD[10]	GND
19	GND	+3.3V	AD[15]	AD[14]	GND	AD[13]	GND
18	GND	SERR#	GND	+3.3V	PAR	C/BE[1]#	GND
17	GND	+3.3V	IPMBSCL	IPMBSDA	GND	PERR#	GND
16	GND	DEVSEL#	GND	V(I/O)	STOP#	LOCK#	GND
15	GND	+3.3V	FRAME#	IRDY#	BD_SEL	TRDY#	GND
12-14	Key Area						
11	GND	AD[18]	AD[17]	AD[16]	GND	C/BE[2]#	GND
10	GND	AD[21]	GND	+3.3V	AD[20]	AD[19]	GND
9	GND	C/BE[3]#	IDSEL	AD[23]	GND	AD[22]	GND
8	GND	AD[26]	GND	V(I/O)	AD[25]	AD[24]	GND
7	GND	AD[30]	AD[29]	AD[28]	GND	AD[27]	GND
6	GND	**REQ#**	GND	+3.3V	**CLK#**	AD[31]	GND
5	GND	BRSVP1A5	RRSVP1B5	PCI_RST#	GND	**GNT#**	GND
4	GND	IPMBPWR	Healthy#	V(I/O)	INTP	INTS	GND
3	GND	INTA#	INTB#	INTC#	+5V	INTD#	GND
2	GND	TCK	+5V	TMS	N/A	N/A	GND
1	GND	+5V	-12V	TRST#	+12V	+5V	GND

▭ = long pins   ▭ = short pins   ▭ = medium length pins

\#: Low active

B2.7 Peripheral Slots (S1, S3~S8) P2 Connector

Table B2-7: Peripheral Slot (S1, S3~S8) P2 Connector

Pin	Z	A	B	C	D	E	F
22	GND	GA4	GA3	GA2	GA1	GA0	GND
21	GND	RSV	GND	RSV	RSV	RSV	GND
20	GND	RSV	RSV	RSV	GND	RSV	GND
19	GND	RSV	GND	SMB_SDA	SMB_SCL	RSV	GND
18	GND	BRSVP2A18	BRSVP2B18	BRSVP2C18	BRSVP2D18	BRSVP2E18	GND
17	GND	BRSVP2A17	GND	PRST#	RSV	RSV	GND
16	GND	BRSVP2A16	BRSVP2B16	RSV	GND	BRSVP2E16	GND
15	GND	BRSVP2A15	GND	FAL#	RSV	RSV	GND
14	GND	AD[35]	AD[34]	AD[33]	GND	AD[32]	GND
13	GND	AD[38]	GND	V(I/O)	AD[37]	AD[36]	GND
12	GND	AD[42]	AD[41]	AD[40]	GND	AD[39]	GND
11	GND	AD[45]	GND	V(I/O)	AD[44]	AD[43]	GND
10	GND	AD[49]	AD[48]	AD[47]	GND	AD[46]	GND
9	GND	AD[52]	GND	V(I/O)	AD[51]	AD[50]	GND
8	GND	AD[56]	AD[55]	AD[54]	GND	AD[53]	GND
7	GND	AD[59]	GND	V(I/O)	AD[58]	AD[57]	GND
6	GND	AD[63]	AD[62]	AD[61]	GND	AD[60]	GND
5	GND	C/BE[5]#	GND	V(I/O)	C/BE[4]#	PAR64	GND
4	GND	V(I/O)	BRSVP2B4	C/BE[7]#	GND	C/BE[6]#	GND
3	GND	RSV	GND	RSV	RSV	RSV	GND
2	GND	RSV	RSV	UNC	RSV	RSV	GND
1	GND	RSV	GND	RSV	RSV	RSV	GND

#: Low active

Note: GA[4...0] shall be used for geographic addressing on the backplane

B2.8 Peripheral Slots (S3~S8) P3 Connector

Table B2-8: Peripheral Slot (S3~S8) P3 Connector

Pin	Z	A	B	C	D	E	F
19	GND	N/C	N/C	N/C	N/C	N/C	GND
18	GND	N/C	N/C	N/C	N/C	N/C	GND
17	GND	N/C	N/C	N/C	N/C	N/C	GND
16	GND	N/C	N/C	N/C	N/C	N/C	GND
15	GND	N/C	N/C	N/C	N/C	N/C	GND
14	GND	N/C	N/C	N/C	N/C	N/C	GND
13	GND	N/C	N/C	N/C	N/C	N/C	GND
12	GND	N/C	N/C	N/C	N/C	N/C	GND
11	GND	N/C	N/C	N/C	N/C	N/C	GND
10	GND	N/C	N/C	N/C	N/C	N/C	GND
9	GND	N/C	N/C	N/C	N/C	N/C	GND
8	GND	N/C	N/C	N/C	N/C	N/C	GND
7	GND	N/C	N/C	N/C	N/C	N/C	GND
6	GND	N/C	N/C	N/C	N/C	N/C	GND
5	GND	N/C	N/C	N/C	N/C	N/C	GND
4	GND	N/C	N/C	N/C	N/C	N/C	GND
3	GND	N/C	N/C	N/C	N/C	N/C	GND
2	GND	N/C	N/C	N/C	N/C	N/C	GND
1	GND	N/C	N/C	N/C	N/C	N/C	GND

#: Low active

B2.9 Peripheral Slots S1P3 Connector
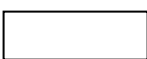
Table B2-9: Peripheral Slot S1P3 Connector

Pin	Z	A	B	C	D	E	F
19	GND	HDD_D3	HDD_D6	HDD_D2	HDD_D10	HDD_D14	GND
18	GND	HDD_D8	HDD_D5	HDD_D1	HDD_D11	HDD_D15	GND
17	GND	HDD_D9	HDD_D4	HDD_D0	HDD_D12	HDD_D13	GND
16	GND	HDD_D7	HDD_CS1#	HDD_IOR#	HDD_CS3#	HDD_IORDY	GND
15	GND	HDD_DMREQ	HDD_DMACK#	HDD_DA2	HDD_DRQ	HDD_IOW#	GND
14	GND	N/C	N/C	HDD_DA1	GND	HDD_DA0	GND
13	GND	N/C	N/C	N/C	N/C	N/C	GND
12	GND	N/C	N/C	N/C	N/C	N/C	GND
11	GND	N/C	N/C	N/C	N/C	N/C	GND
10	GND	N/C	N/C	N/C	N/C	N/C	GND
9	GND	N/C	N/C	N/C	N/C	N/C	GND
8	GND	N/C	N/C	+5V	N/C	N/C	GND
7	GND	HDD_LED	N/C	HDD_RST#	N/C	N/C	GND
6	GND	N/C	+5V	GND	N/C	N/C	GND
5	GND	+5V	N/C	N/C	N/C	N/C	GND
4	GND	N/C	N/C	HDD_CBDET	N/C	N/C	GND
3	GND	FDD_DSKCHG#	FDD_MTR0#	FDD_STEP#	FDD_HDSEL#	FDD_TRK0#	GND
2	GND	FDD_DS0#	N/C	FDD_DRVEN0#	FDD_RDATA#	FDD_WRTPRT#	GND
1	GND	FDD_INDEX#	N/C	FDD_DIR#	FDD_WGATE#	FDD_WDATA#	GND

B2.10 Peripheral Slots (S1,S3~S8) P4 Connector

Table B2-10: Peripheral Slot (S1, S3~S8) P4 Connector

Pin	Z	A	B	C	D	E	F
25	N/C	SGA4	SGA3	SGA2	SGA1	SGA0	FGND
24	N/C	GA4	GA3	GA2	GA1	GA0	FGND
23	N/C	+12V	#CT_RT	#CT_EN	-12V	CT_MC	FGND
22	N/C	#PF_S0	RSV	RSV	RSV	RSV	FGND
21	N/C	-SEL_Vbat	#PF_S1	RSV	RSV	SELVbatRtn	FGND
20	N/C	N/C	N/C	N/C	N/C	N/C	GND
19	N/C	N/C	N/C	N/C	N/C	N/C	GND
18	N/C	VRG	N/C	N/C	N/C	VRGRtn	GND
17	N/C	N/C	N/C	N/C	N/C	N/C	GND
16	N/C	N/C	N/C	N/C	N/C	N/C	GND
15	N/C	-Vbat	N/C	N/C	N/C	VbatRtn	GND
12-14	Key Area						
11	N/C	CT_D29	CT_D30	CT_D31	V(I/O)	#CT_FA	GND
10	N/C	CT_D27	+3.3V	CT_D28	+5V	#CT_FB	GND
9	N/C	CT_D24	CT_D25	CT_D26	GND	#FR_CP	GND
8	N/C	CT_D21	CT_D22	CT_D23	+5V	CT_C8A	GND
7	N/C	CT_D19	+5V	CT_D20	GND	CT_C8B	GND
6	N/C	CT_D16	CT_D17	CT_D18	GND	CT_N1	GND
5	N/C	CT_D13	CT_D14	CT_D15	+3.3V	CT_N2	GND
4	N/C	CT_D11	+5V	CT_D12	+3.3V	SCLK	GND
3	N/C	CT_D8	CT_D9	CT_D10	GND	SCLK-D	GND
2	N/C	CT_D4	CT_D5	CT_D6	CT_D7	GND	GND
1	N/C	CT_D0	+3.3V	CT_D1	CT_D2	CT_D3	GND

☐ = long pins   ☐ = short pins   ☐ = medium length pins

\#: Low active

B2.11 Peripheral Slots (S1, S3~S8) P5 Connector

Table B2-11: Peripheral Slot (S1, S3~S8) P5 Connector

Pin	Z	A	B	C	D	E	F
22	GND	N/C	N/C	N/C	N/C	N/C	GND
21	GND	N/C	N/C	N/C	N/C	N/C	GND
20	GND	N/C	N/C	N/C	N/C	N/C	GND
19	GND	N/C	N/C	N/C	N/C	N/C	GND
18	GND	N/C	N/C	N/C	N/C	N/C	GND
17	GND	N/C	N/C	N/C	N/C	N/C	GND
16	GND	N/C	N/C	N/C	N/C	N/C	GND
15	GND	N/C	N/C	N/C	N/C	N/C	GND
14	GND	N/C	N/C	N/C	N/C	N/C	GND
13	GND	N/C	N/C	N/C	N/C	N/C	GND
12	GND	N/C	N/C	N/C	N/C	N/C	GND
11	GND	N/C	N/C	N/C	N/C	N/C	GND
10	GND	N/C	N/C	N/C	N/C	N/C	GND
9	GND	N/C	N/C	N/C	N/C	N/C	GND
8	GND	N/C	N/C	N/C	N/C	N/C	GND
7	GND	N/C	N/C	N/C	N/C	N/C	GND
6	GND	N/C	N/C	N/C	N/C	N/C	GND
5	GND	N/C	N/C	N/C	N/C	N/C	GND
4	GND	N/C	N/C	N/C	N/C	N/C	GND
3	GND	N/C	N/C	N/C	N/C	N/C	GND
2	GND	N/C	N/C	N/C	N/C	N/C	GND
1	GND	N/C	N/C	N/C	N/C	N/C	GND

Appendix C

Ordering Information

• **MIC-3042A-A:** MIC-3042A CompactPCI™ enclosure, w/ 8-slot CompactPCI™ 6U backplane (MIC-3042A) supporting CT bus application and PICMG 2.16, and 500W 2+1 redundant AC CPCI power supplies.

• **MIC-3042A-D:** MIC-3042A CompactPCI™ enclosure, w/ 8-slot CompactPCI™ 6U backplane (MIC-3042A) supporting CT bus application and PICMG 2.16, and 500W 2+1 redundant DC CPCI power supplies.

• **MIC-3042B-A:** MIC-3042B CompactPCI™ enclosure, w/ 8-slot CompactPCI™ 6U backplane (MIC-3042B) supporting CT bus application and 500W 2+1 redundant AC CPCI power supplies.

• **MIC-3042B-D:** MIC-3042B CompactPCI™ enclosure, w/ 8-slot CompactPCI™ 6U backplane (MIC-3042B) supporting CT bus application and 500W 2+1 redundant DC CPCI power supplies.

Recommend 6U CompactPCI™ SBC: User can refer to below table for the SBC and RIO accommodation.

Chassis	CPU Board	Rear I/O Board	Alarm module
MIC-3042A-A MIC-3042A-D	MIC-3369A-Mx	RIO-3309C-A	968A390022 (MIC-3924L-A)
	MIC-3358A-Mx	RIO-3309C-A	
	MIC-3358L	RIO-3309L	
	MIC-3359	RIO-3309L	
	MIC-3368E-A	RIO-3308C-A	
	MIC-3368E1-A	RIO-3308C-A	
MIC-3042B-A MIC-3042B-D	MIC-3369A-Mx	RIO-3309C-A	968A390022 (MIC-3924L-A)
	MIC-3358A-Mx	RIO-3309C-A	
	MIC-3358L	RIO-3309L	
	MIC-3359	RIO-3309L	

Default Alarm Module:

• 968A390022 (MIC-3924L-A): Chassis management module without remote control for general purpose chassis, with single SBC application.

Upgradeable Alarm Module

• 968A390002 (MIC-3924A-A): Chassis management module for general purpose chassis, with single SBC application.

• 968A390012 (MIC-3924B-A): Chassis management module for multi-SBC application.