

# EVA-X4300 Reference Design Revision History

EVA-X4300 REV.A1 01-1 Date: 2007/10/19

1.Initial version

EVA-X4300 REV.A1 01-2 Date: 2008/04/3

- 1.PG07:Change bead B5 from 60 ohm @100MHz to 2000 ohm @100MHz
- 2.PG07:Add R191-R194 and C208,C209 for LAN\_MDI signals
- 3.PG12:Modify +V1.8 PWM IC to ENPIRON EN5312
- 4.PG14:Modify PCI VGA to SMI SM712
- 5.PG20:Add external 8 bits ISA BIOS

EVA-X4300 REV.A1 01-3 Date: 2008/09/08

- 1.PG03:Modify Notes.
- 2.PG08:Change capacitance C115,C116 from 33pF to 47pF.
- 3.PG08:Change capacitance C117,C120 from 12pF to 15pF.
- 4.PG08:Change capacitance C123 from 1000pF to 4.7uF.
- 5.PG08:Change resistance R83 from 1Kohm to 10Kohm.

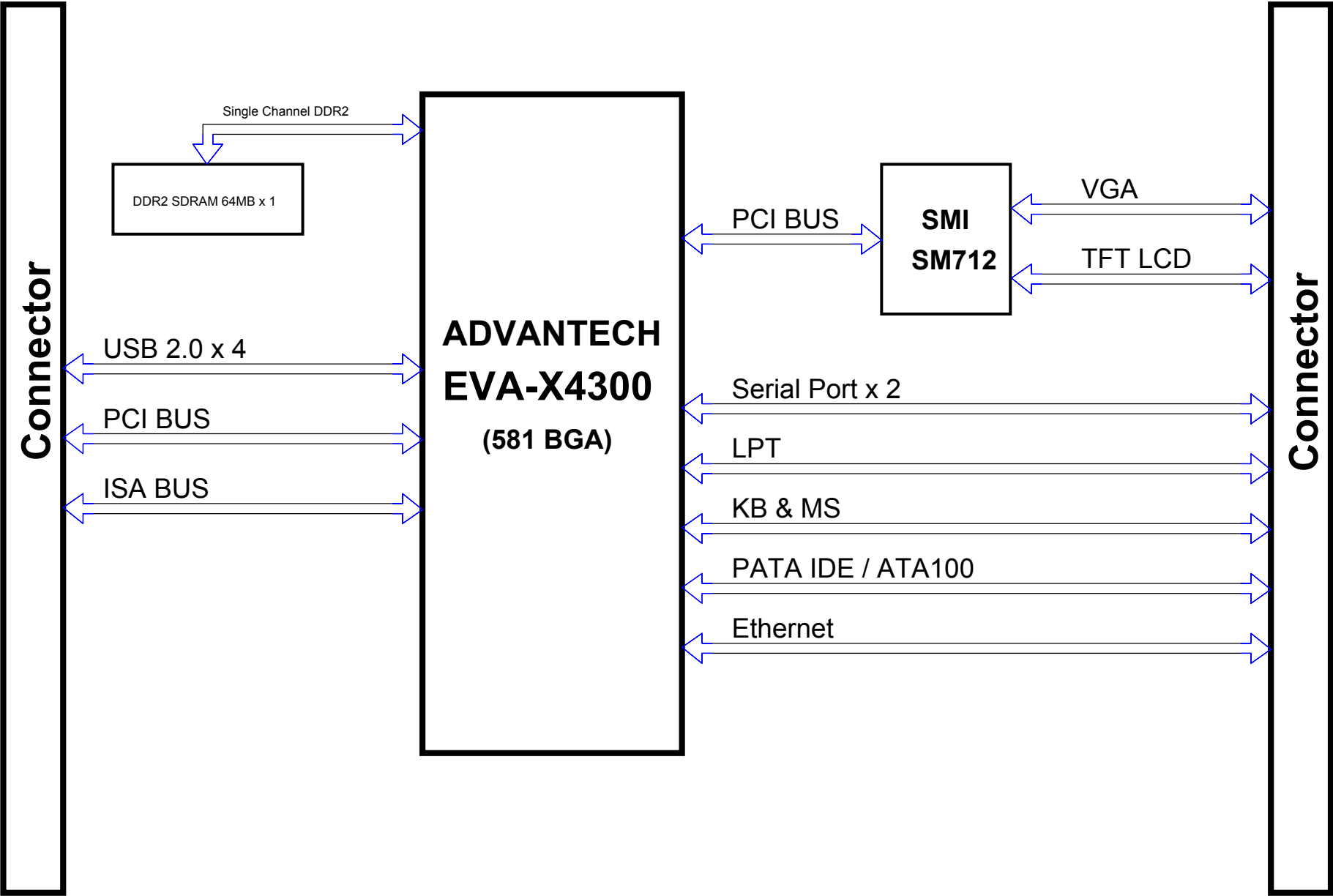
# EVA-X4300 Schematics    Revision : A1013

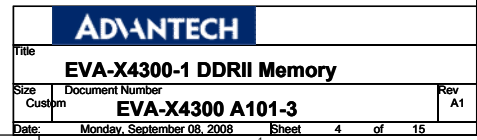
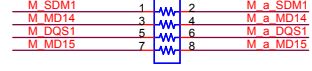
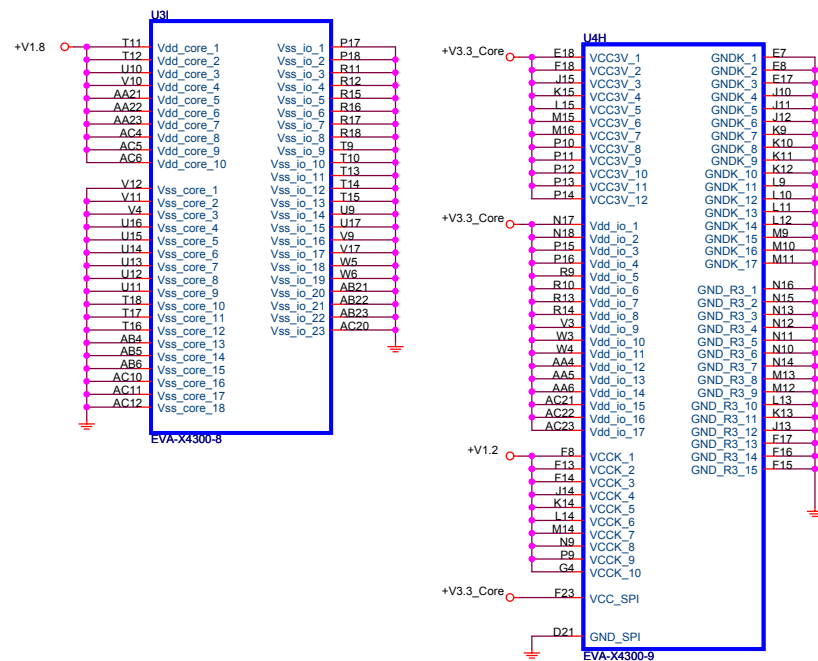
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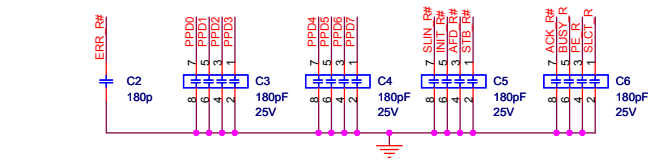
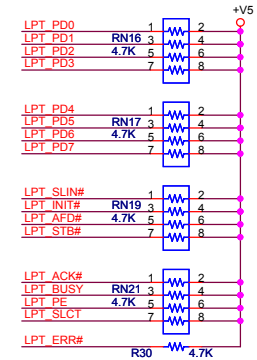
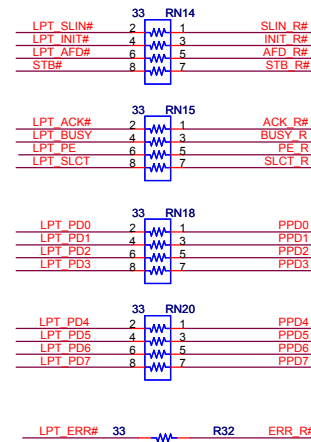
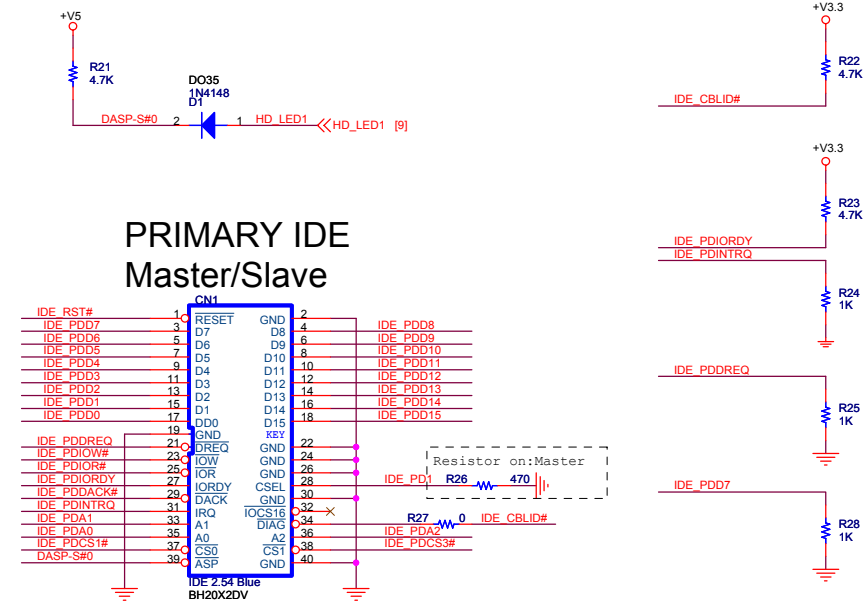
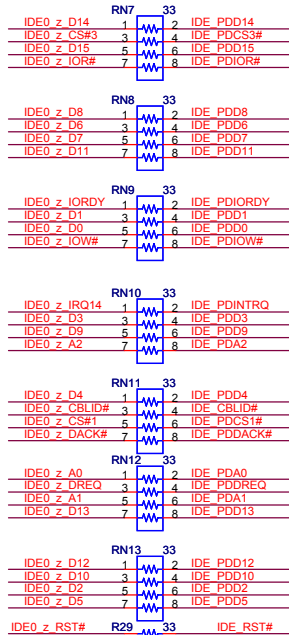
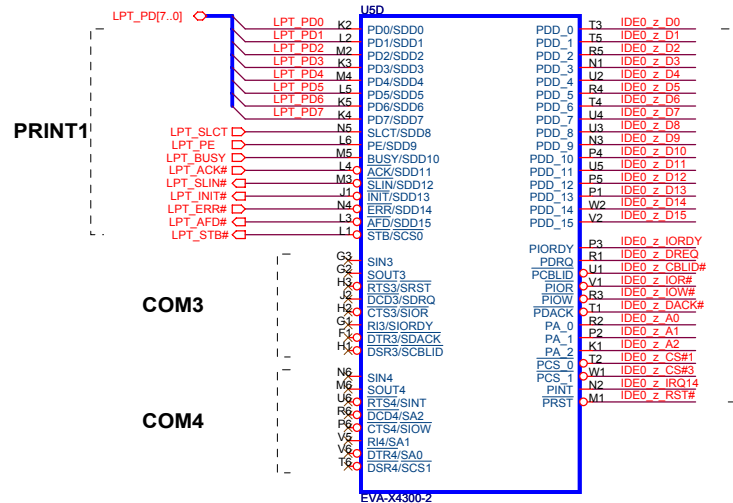
- |    |                            |
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| 04 | EVA-X4300-1 DDRII Memory   |
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<b>Cover Page</b>		
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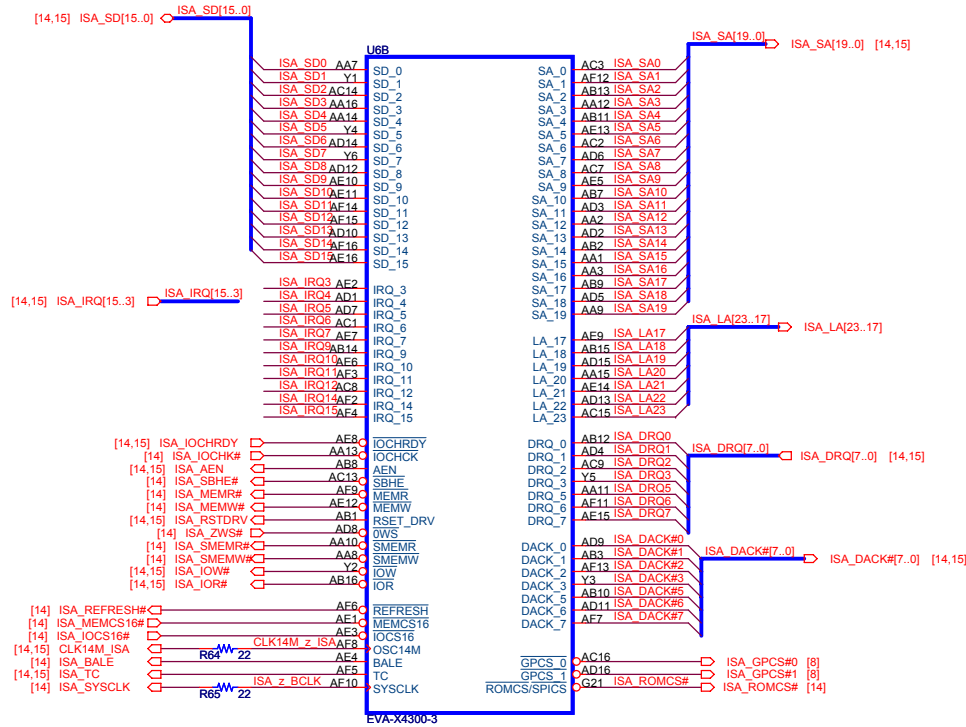
BLOCK DIAGRAM



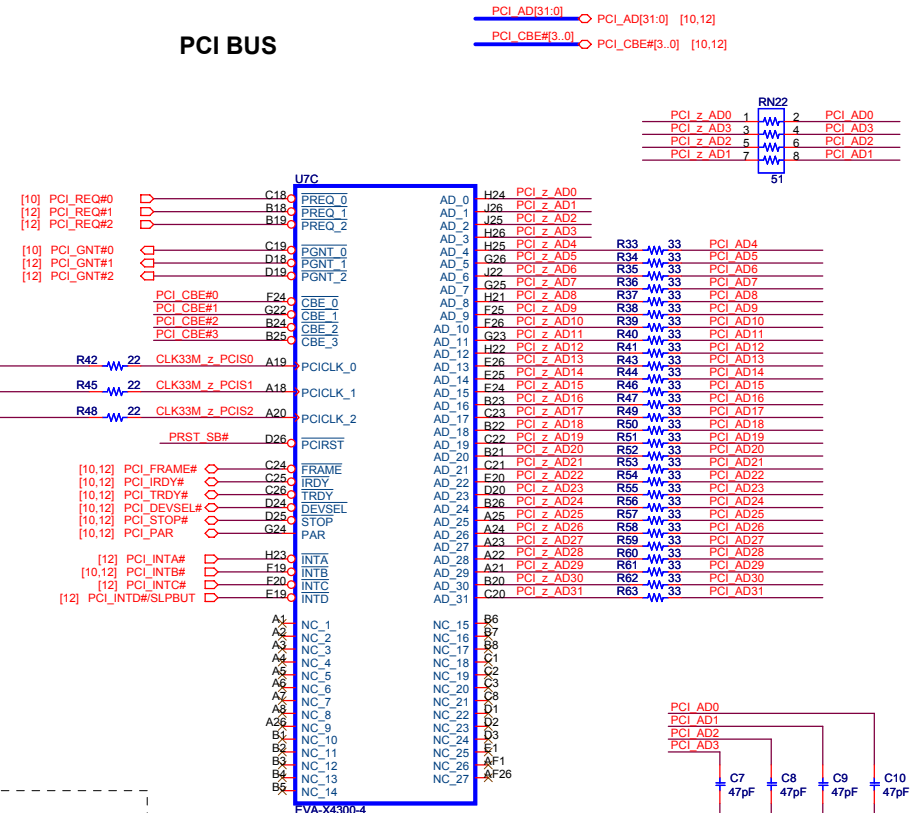




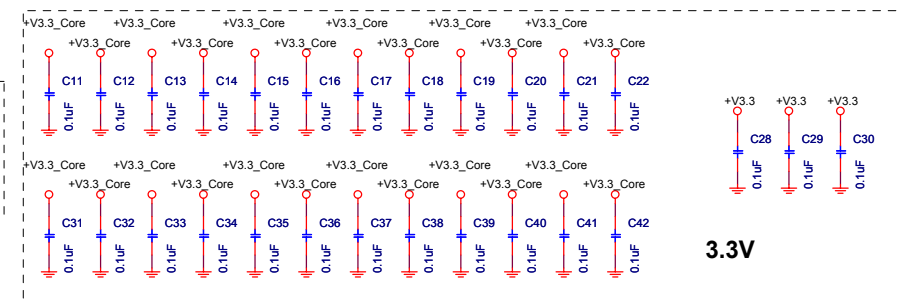
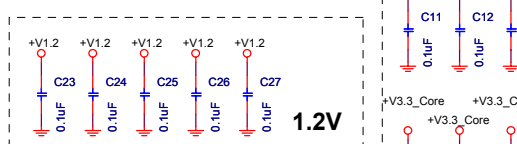
## ISA BUS



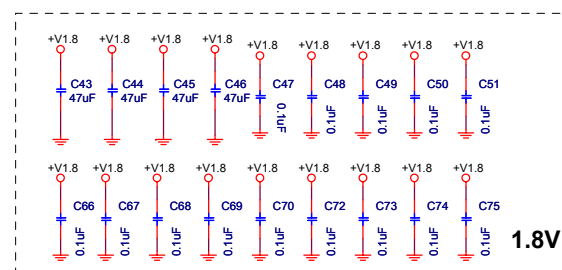
## PCI BUS



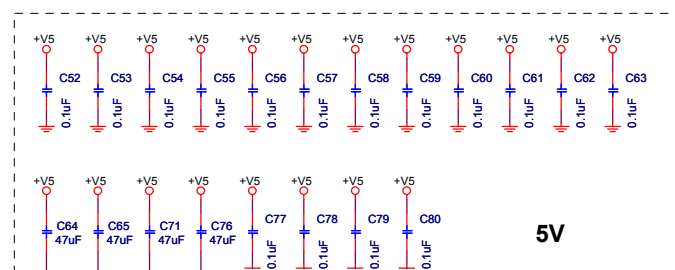
## BYPASS



3.3V

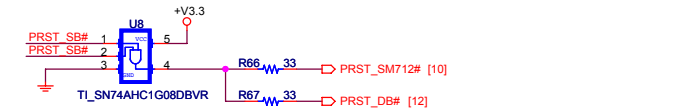


1.8V



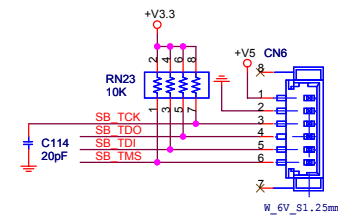
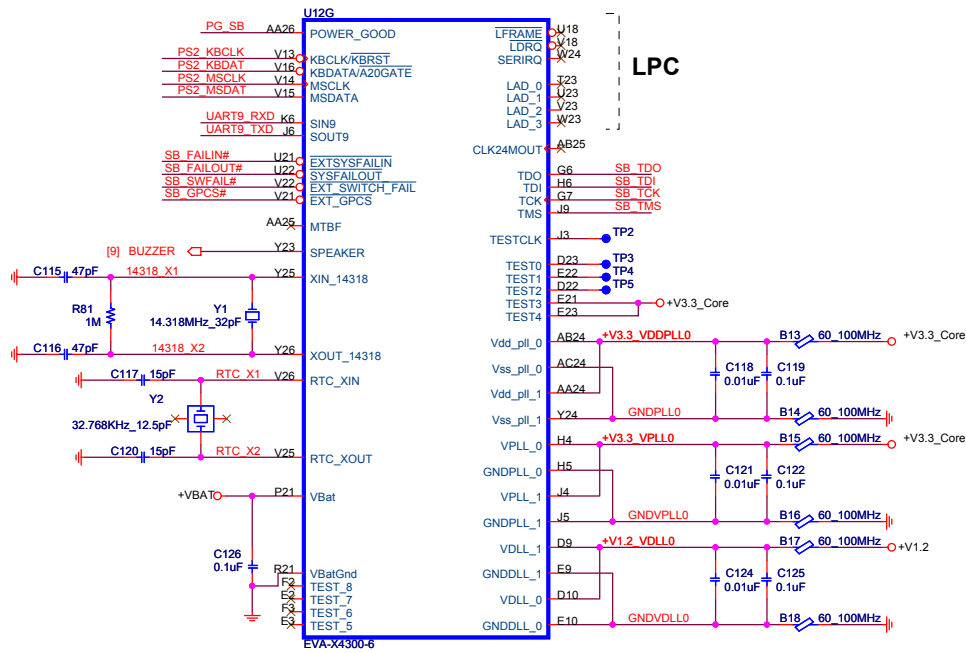
5V

## PCI Reset

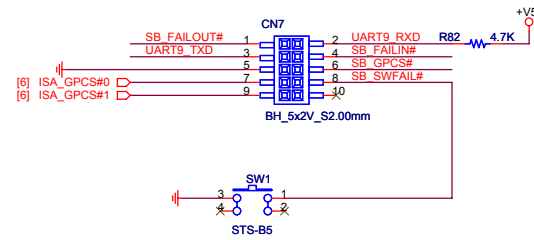


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EVA-X4300-3 PCI / ISA bus			
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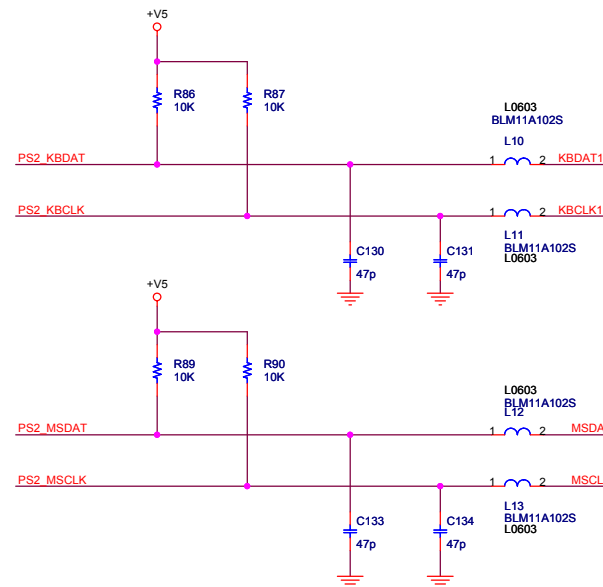
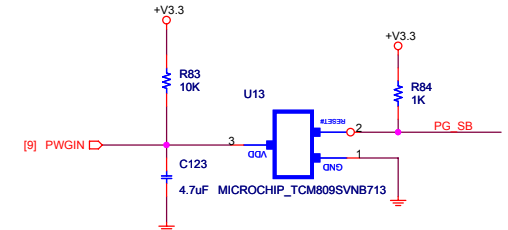




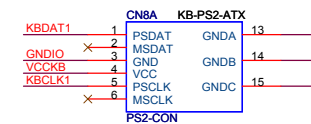
## REDUNDANCY



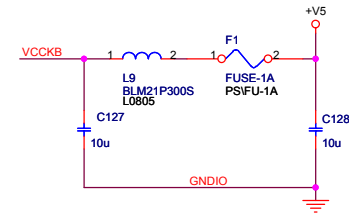
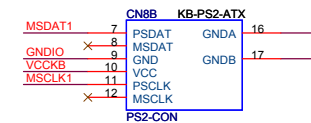
## POWER GOOD



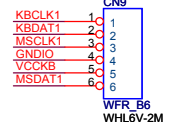
## KBD/MS PS/2



## KBD/MS PS/2

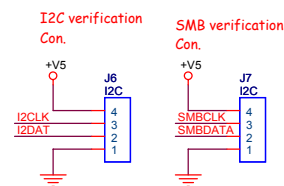
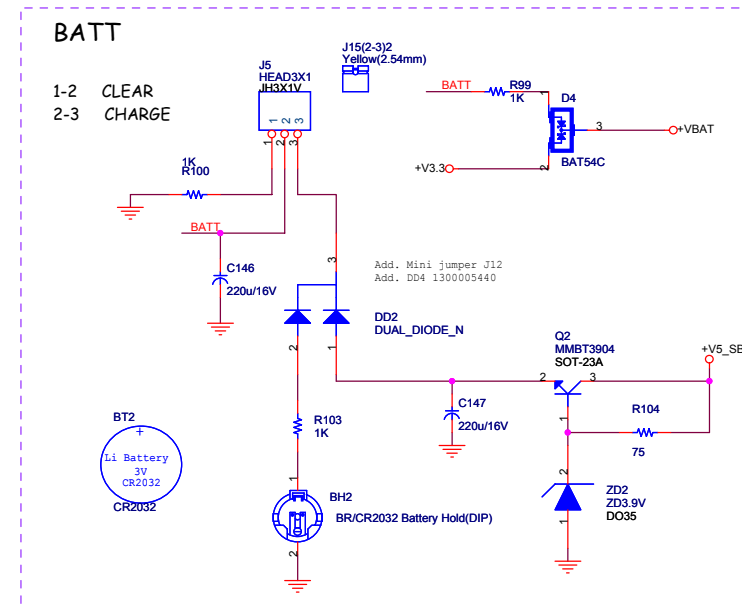
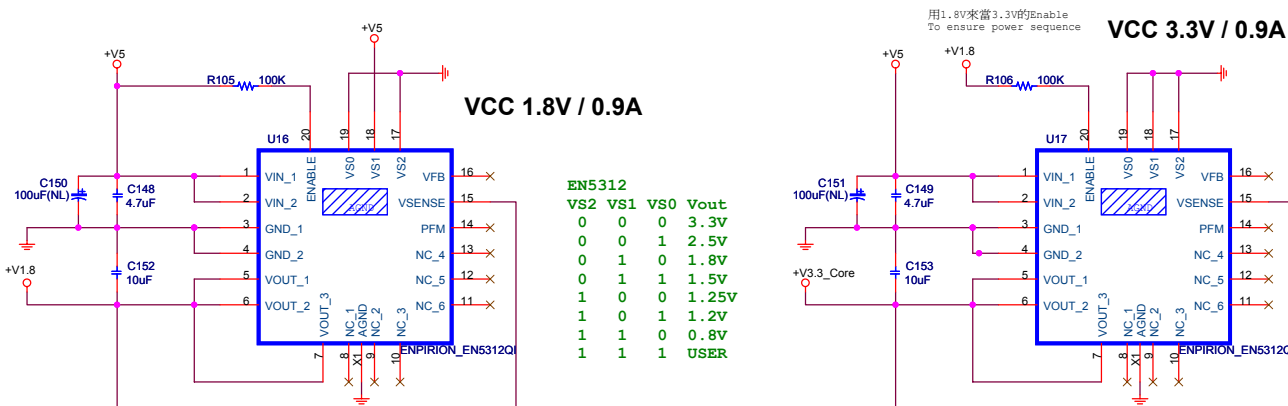
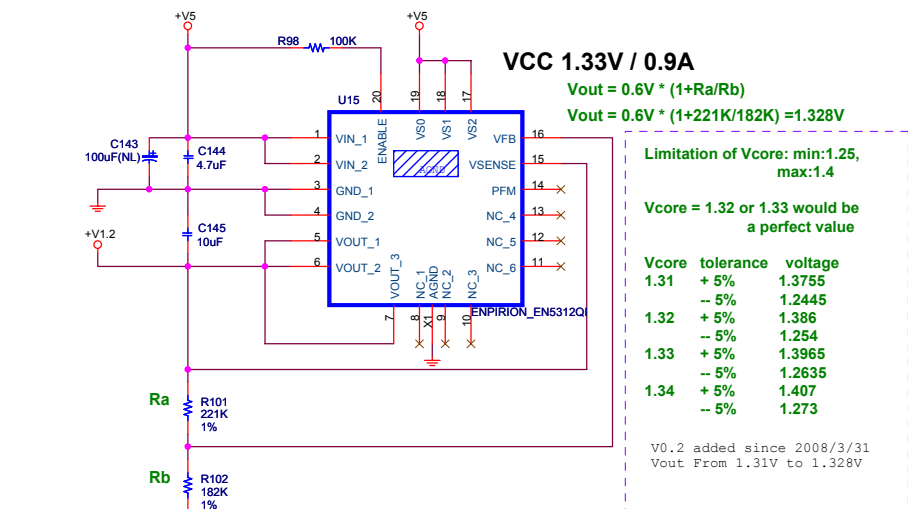
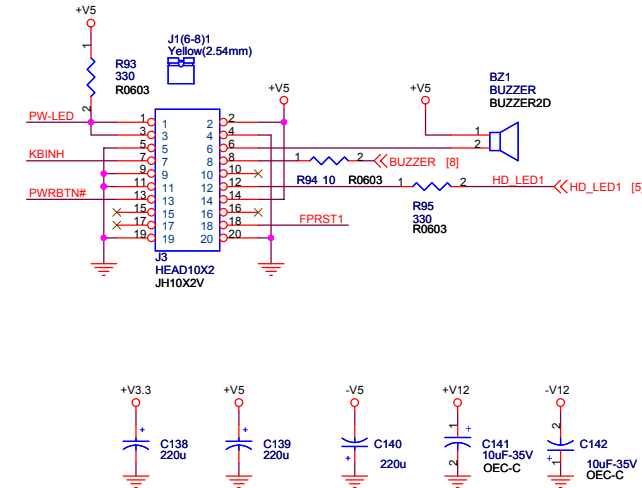
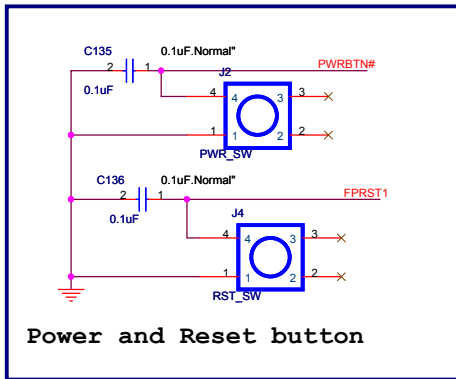
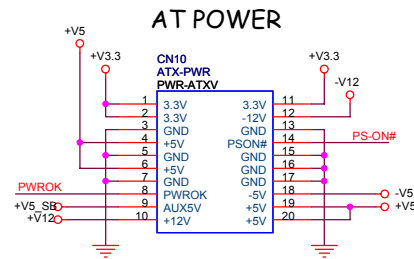
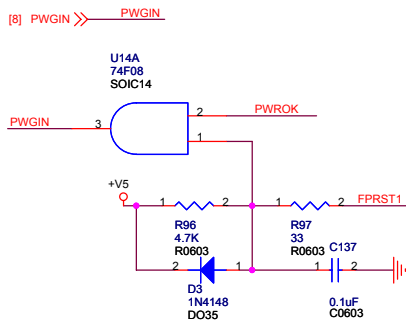


## KBD/MS CONN 2.0mm

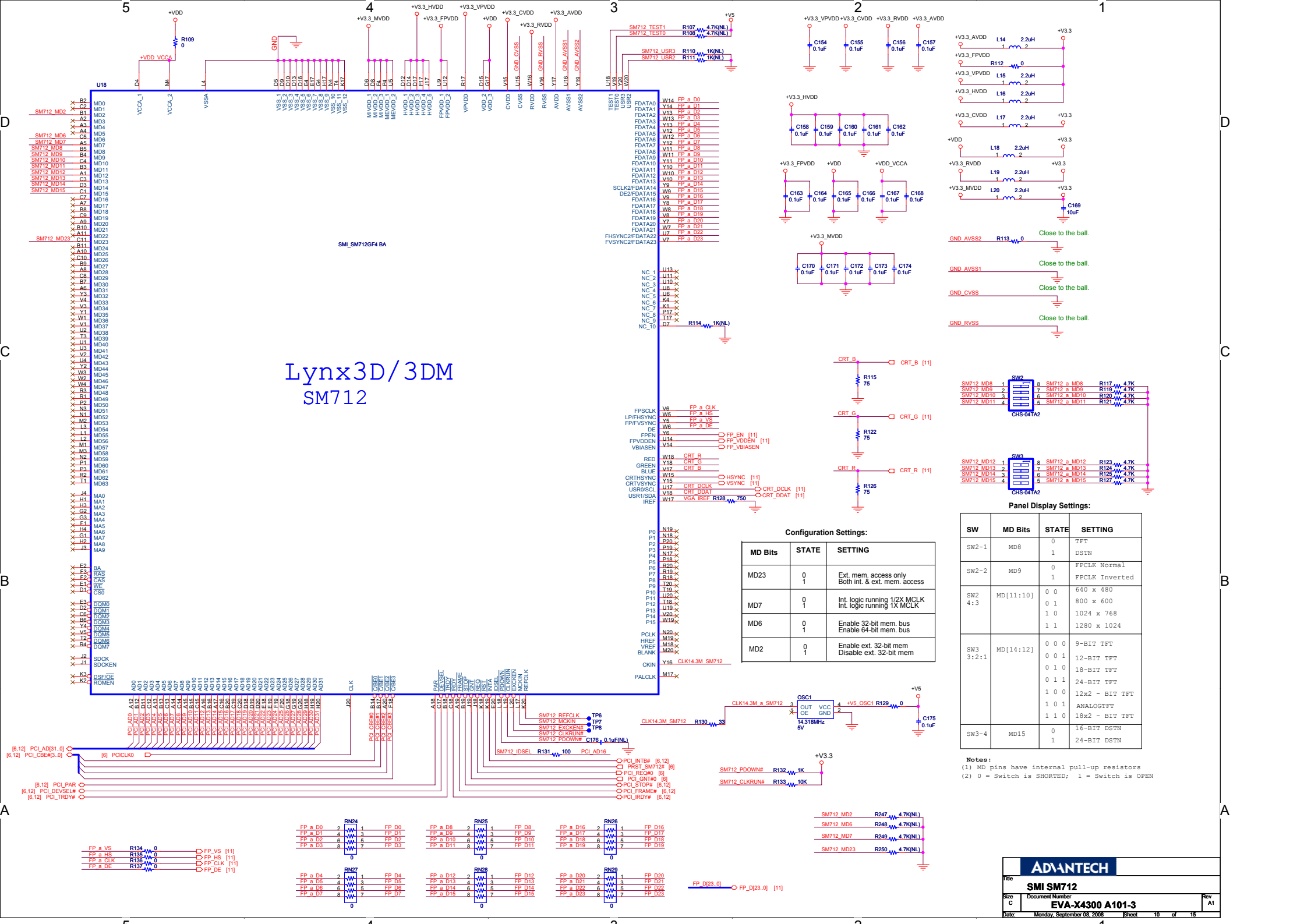


ADVANTECH			
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EVA-X4300-5 KB/MS/LPC/JTAG			
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# Lynx3D/3DM SM712



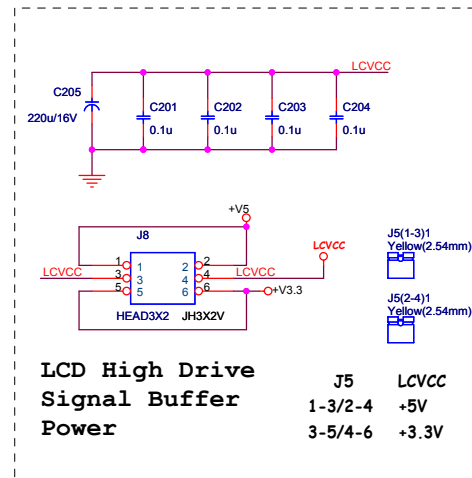
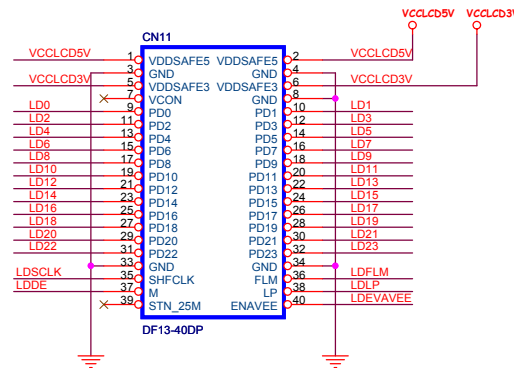
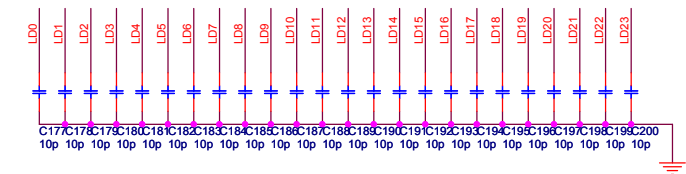
[10] FP\_D[23..0] FP D[23..0]

[10] FP\_DE R138 0 LDDE-R  
[10] FP\_HS R139 0 LDLP-R  
[10] FP\_VS R141 0 LDFLM-R  
[10] FP\_EN R142 0 LDENAVEE-R  
[10] FP\_CLK R140 0 LDSCLK-R

LDSCLK-R L24 2 1 0R LDSCLK  
LDPLM-R L31 2 1 0R LDPLM  
LDLP-R L27 2 1 0R LDLP  
LDDE-R L32 2 1 0R LDDE  
LDENAVEE-R L33 2 1 0R LDENAVEE

LD0-R L21 2 1 0R LD0  
LD1-R L22 2 1 0R LD1  
LD2-R L23 2 1 0R LD2  
LD3-R L25 2 1 0R LD3  
LD4-R L26 2 1 0R LD4  
LD5-R L28 2 1 0R LD5  
LD6-R L29 2 1 0R LD6  
LD7-R L30 2 1 0R LD7  
LD8-R L34 2 1 0R LD8  
LD9-R L35 2 1 0R LD9  
LD10-R L36 2 1 0R LD10  
LD11-R L37 2 1 0R LD11  
LD12-R L38 2 1 0R LD12  
LD13-R L39 2 1 0R LD13  
LD14-R L40 2 1 0R LD14  
LD15-R L41 2 1 0R LD15  
LD16-R L42 2 1 0R LD16  
LD17-R L43 2 1 0R LD17  
LD18-R L44 2 1 0R LD18  
LD19-R L45 2 1 0R LD19  
LD20-R L46 2 1 0R LD20  
LD21-R L47 2 1 0R LD21  
LD22-R L48 2 1 0R LD22  
LD23-R L49 2 1 0R LD23

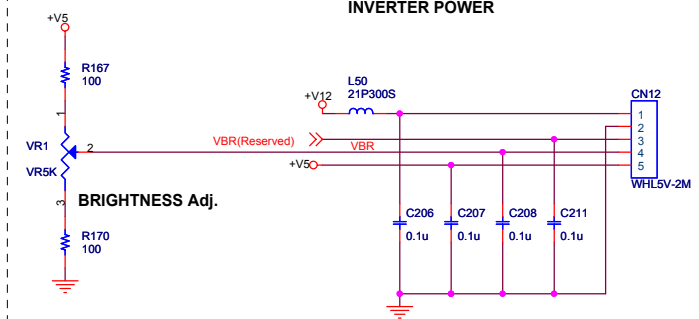
FP D15 R143 0 LD15-R  
FP D14 R145 0 LD14-R  
FP D9 R149 0 LD9-R  
FP D8 R152 0 LD8-R  
FP D7 R144 0 LD7-R  
FP D6 R147 0 LD6-R  
FP D1 R150 0 LD1-R  
FP D0 R153 0 LD0-R  
FP D3 R157 0 LD3-R  
FP D2 R160 0 LD2-R  
FP D5 R163 0 LD5-R  
FP D4 R166 0 LD4-R  
FP D19 R156 0 LD19-R  
FP D18 R158 0 LD18-R  
FP D21 R161 0 LD21-R  
FP D20 R164 0 LD20-R



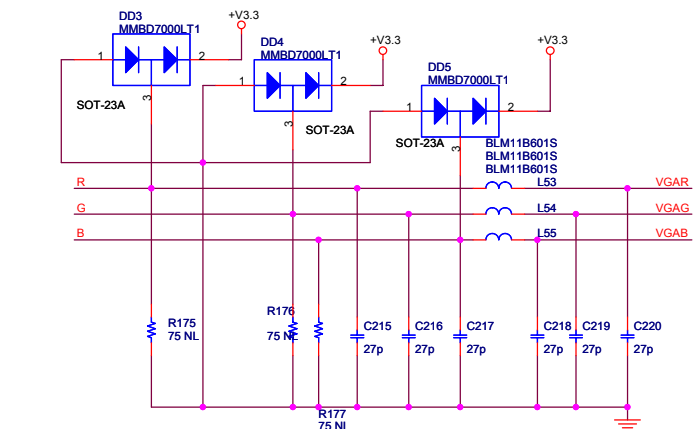
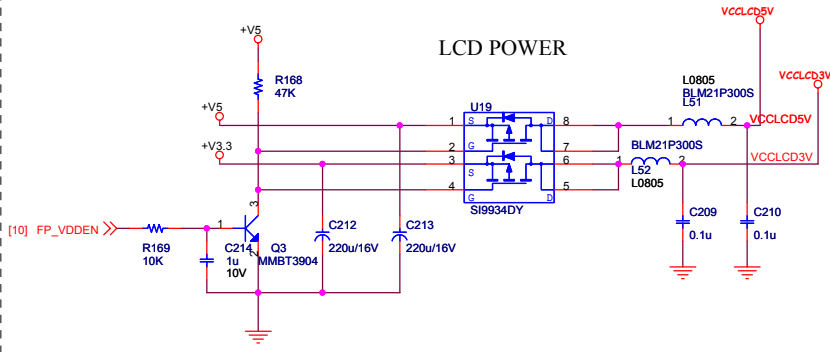
LCD High Drive  
Signal Buffer  
Power

J5 LCVCC  
1-3/2-4 +5V  
3-5/4-6 +3.3V

### INVERTER POWER

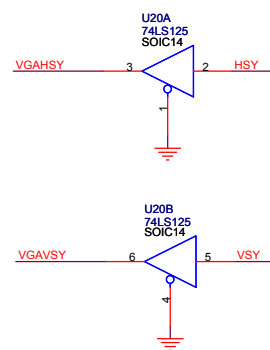
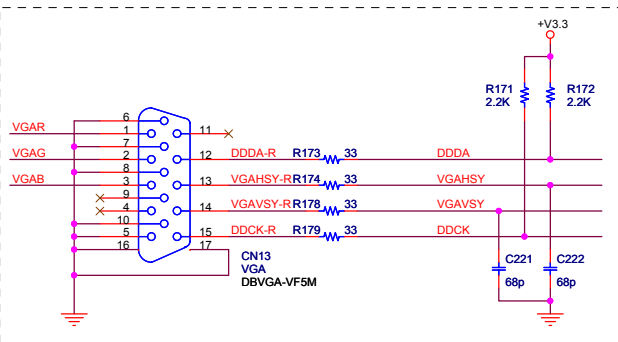


### LCD POWER

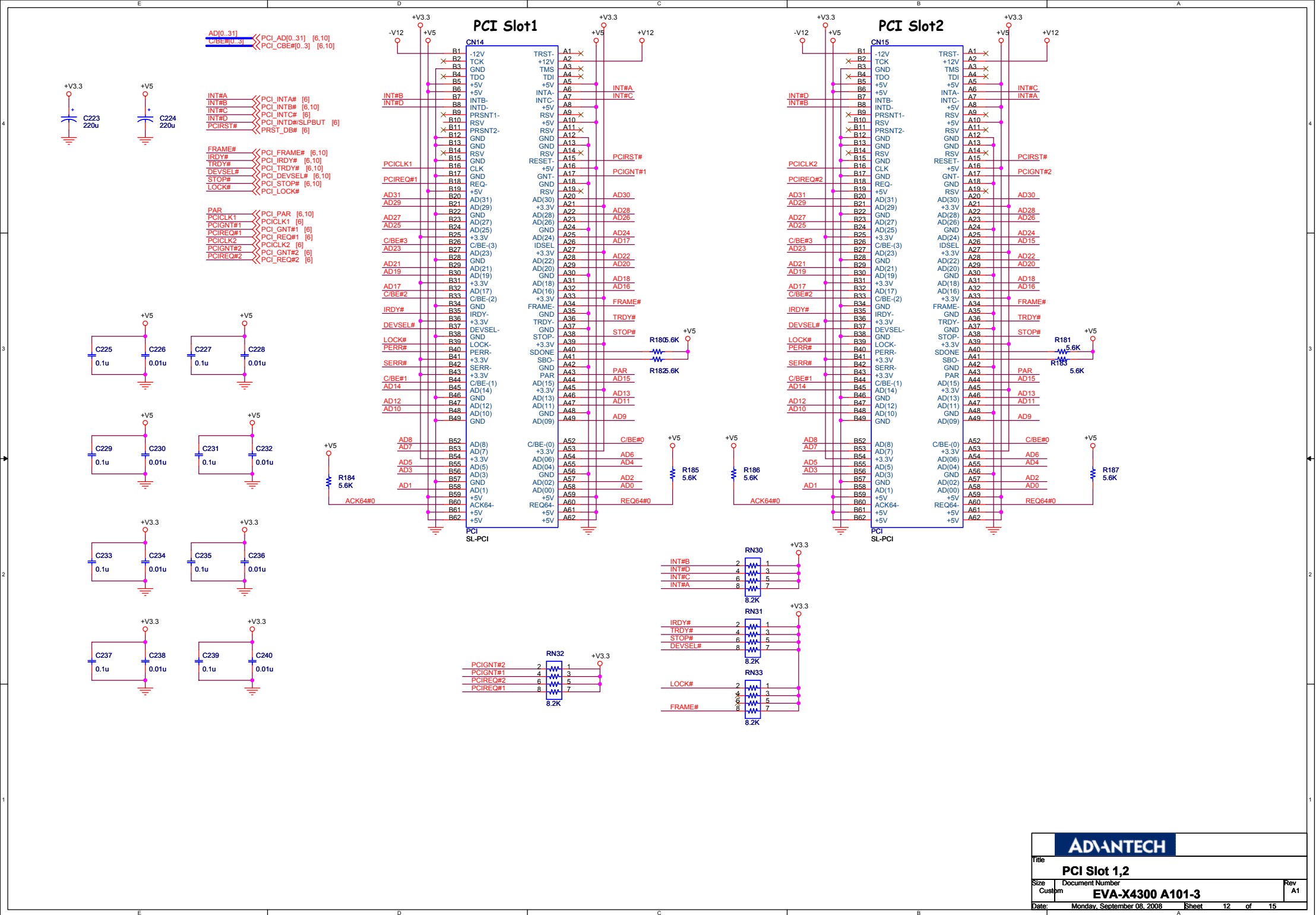


[10] CRT\_R >> R  
[10] CRT\_G >> G  
[10] CRT\_B >> B  
[10] HSY >> HSY  
[10] VSYNC >> VSY  
[10] VSYNC >> VSY  
[10] CRT\_DDAT >> DDDA  
[10] CRT\_DCLK >> DDCK

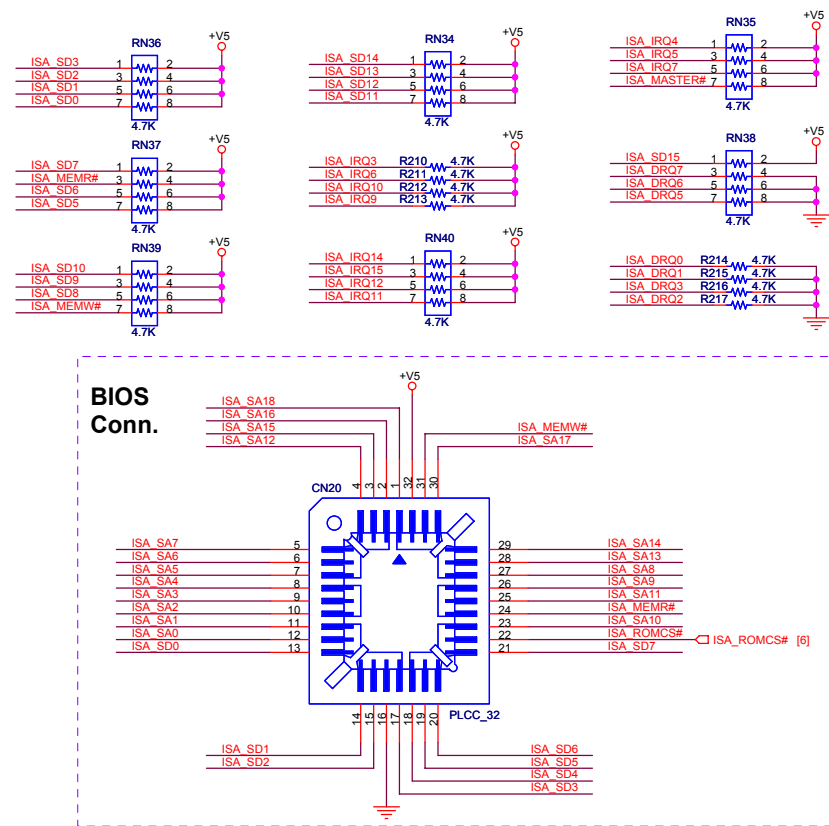
### CRT Conn



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Title LCD DF13 40P / Inverter / CRT Conn			
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