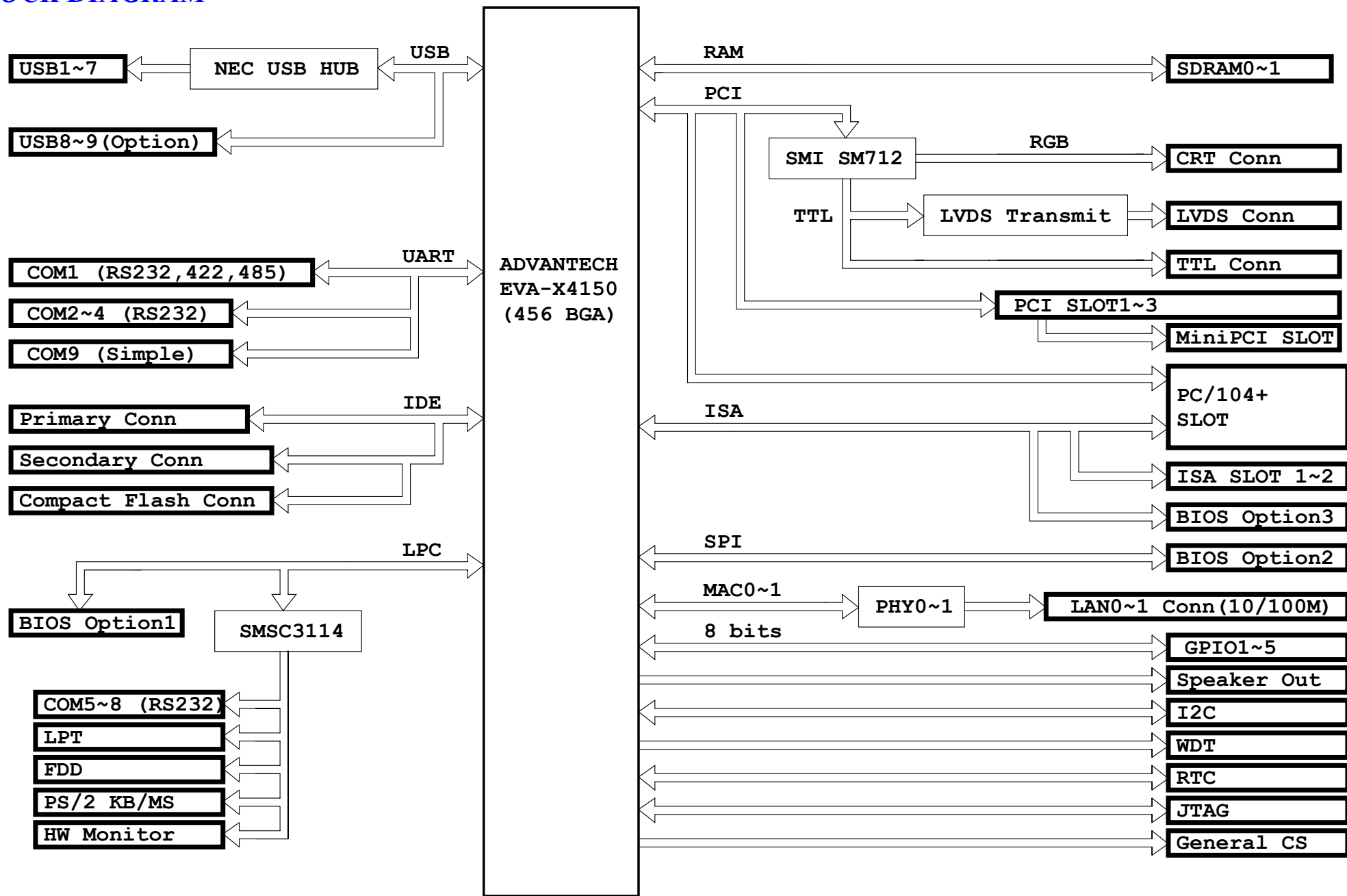
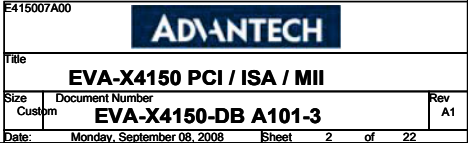
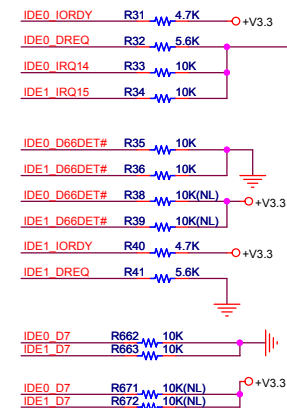
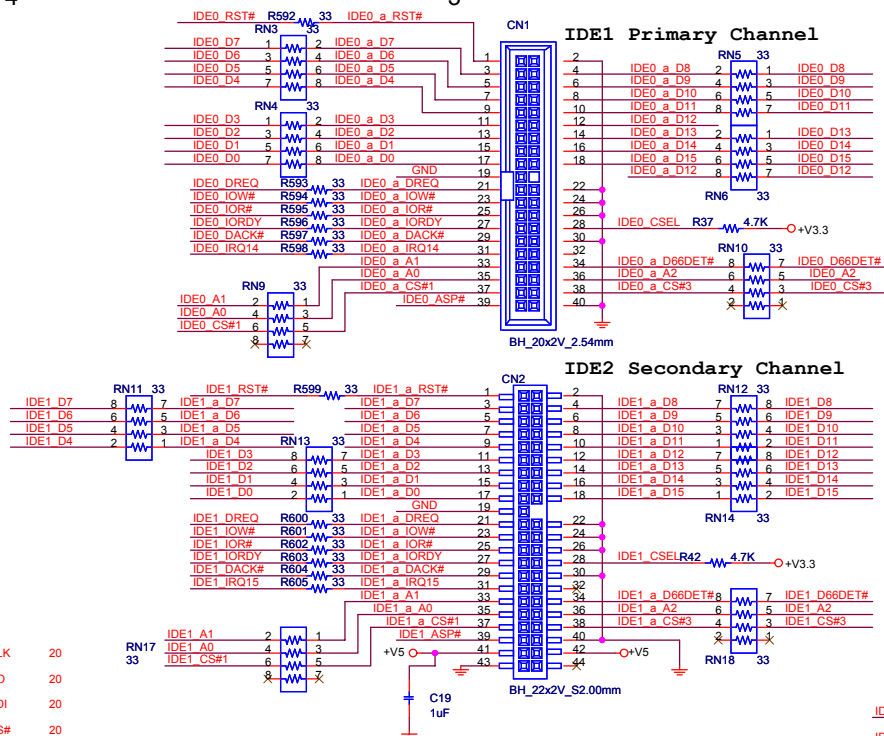
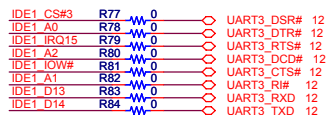
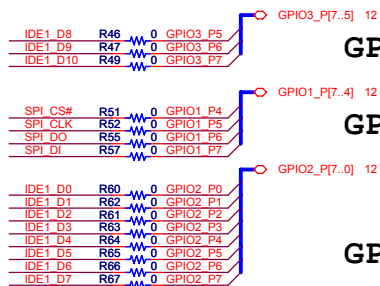
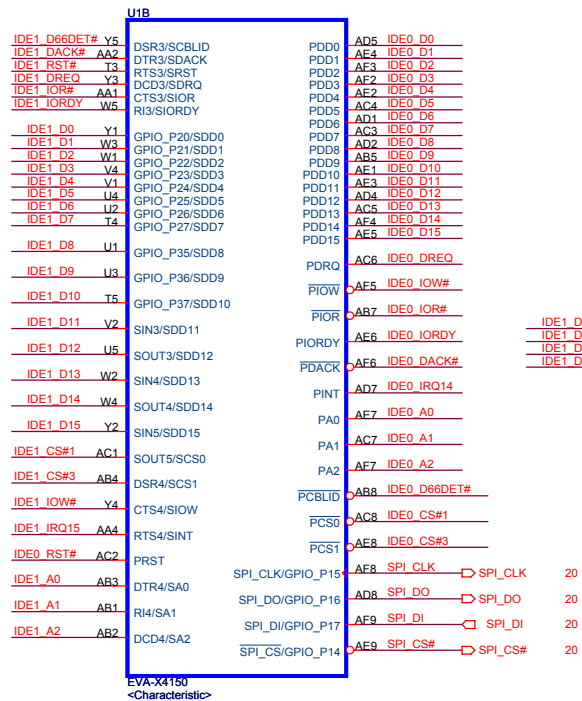


BLOCK DIAGRAM



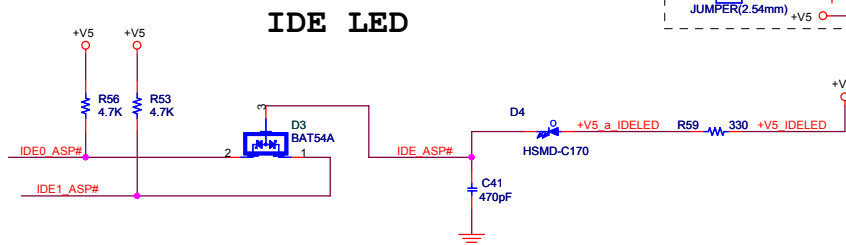
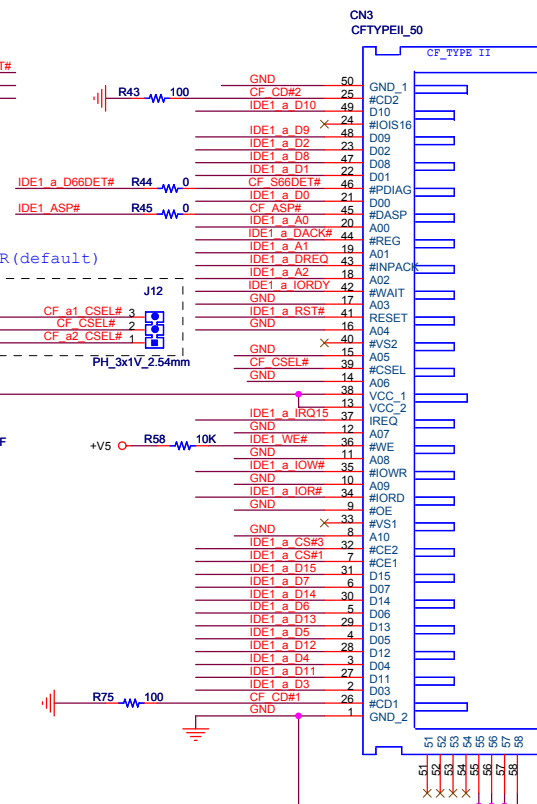


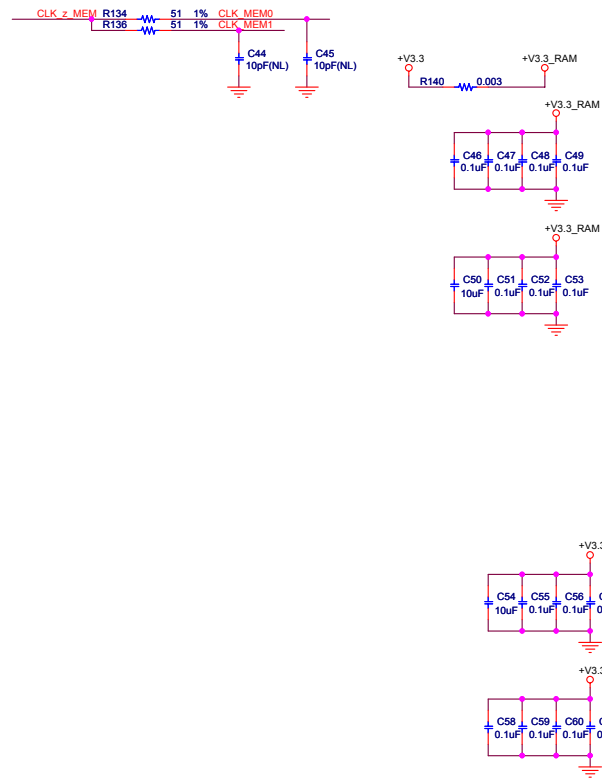
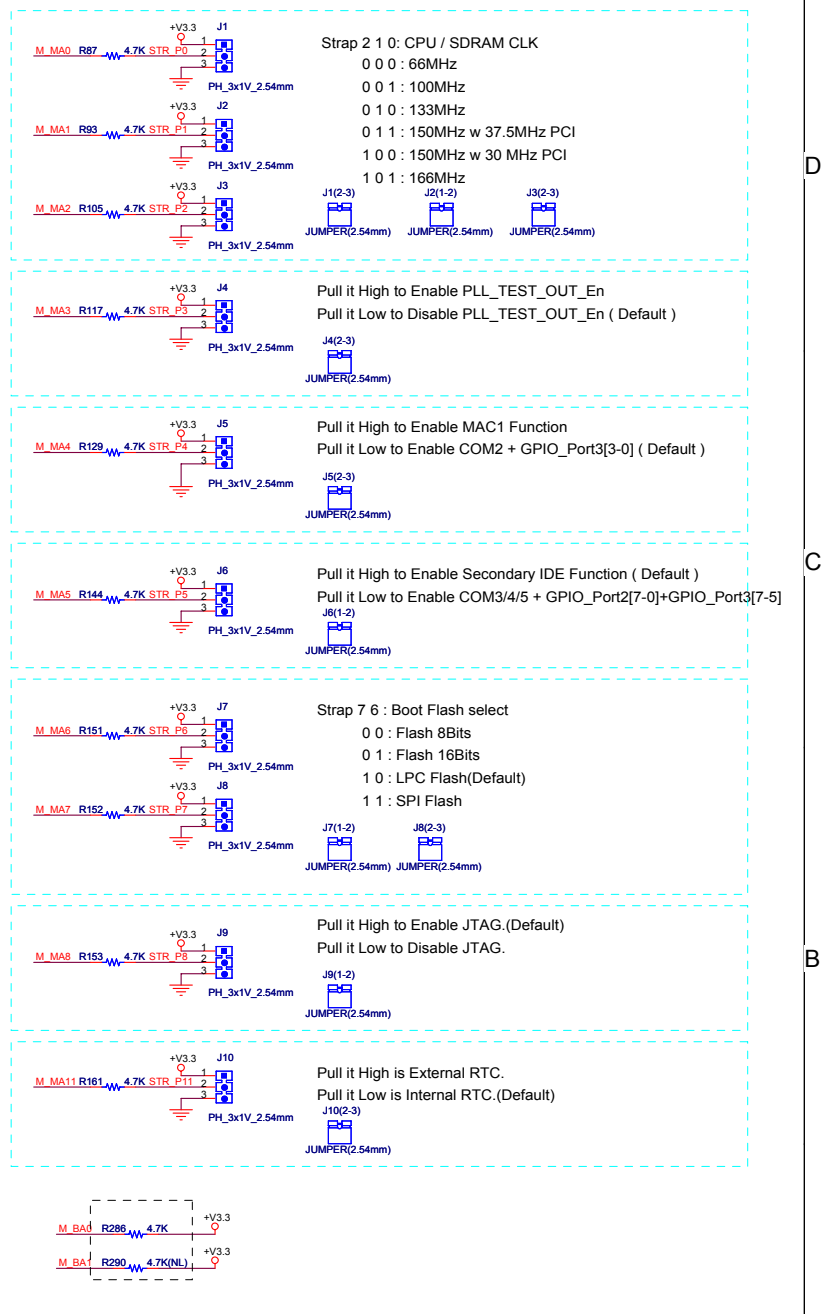
IDE & COM3/4/5



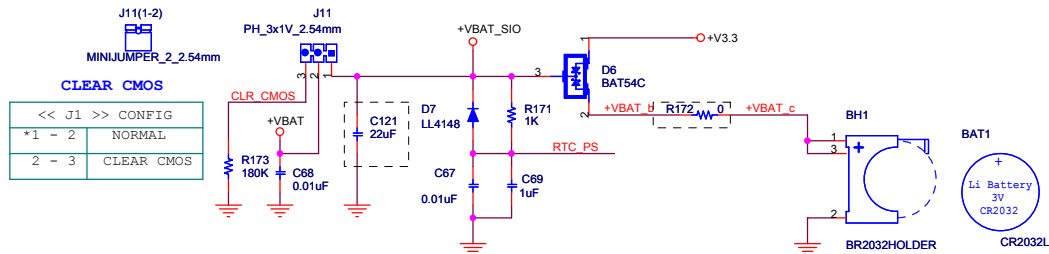
CF select

Low: Master mode

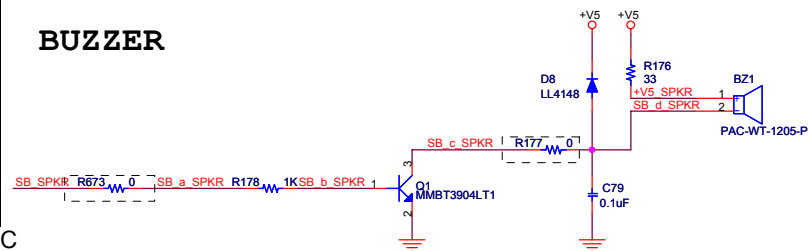


[illegible]

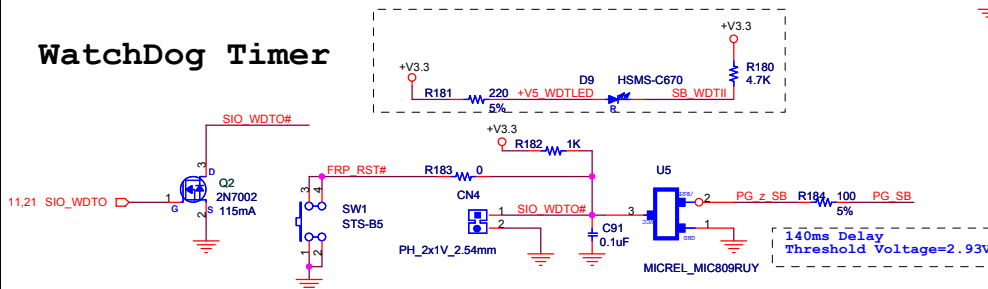
CLEAR CMOS



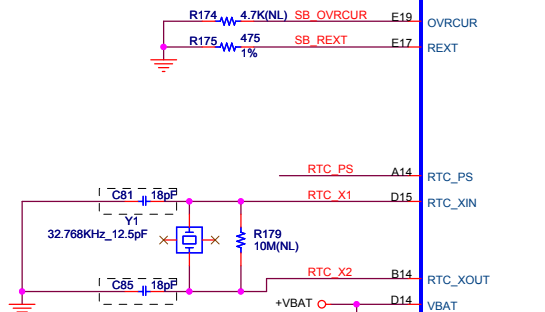
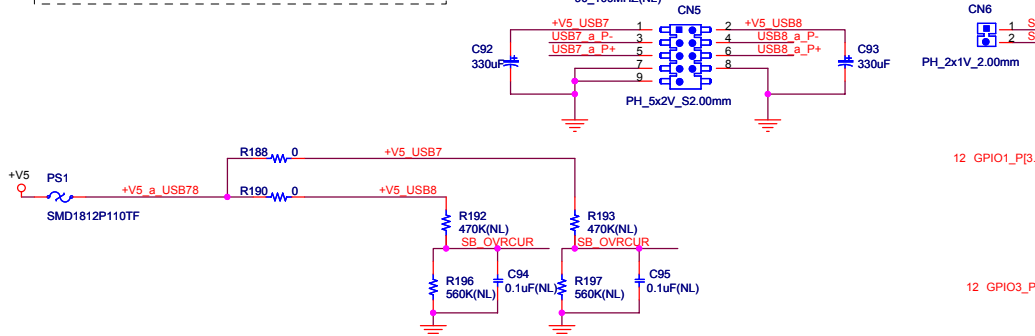
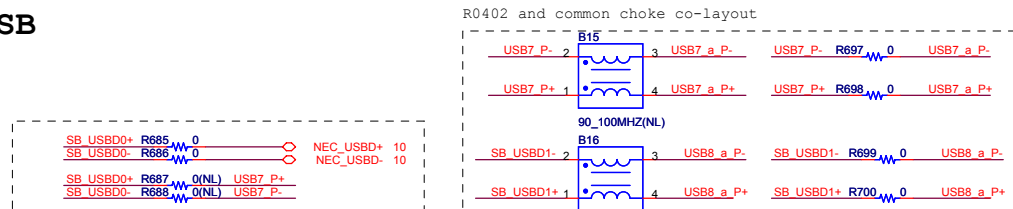
BUZZER



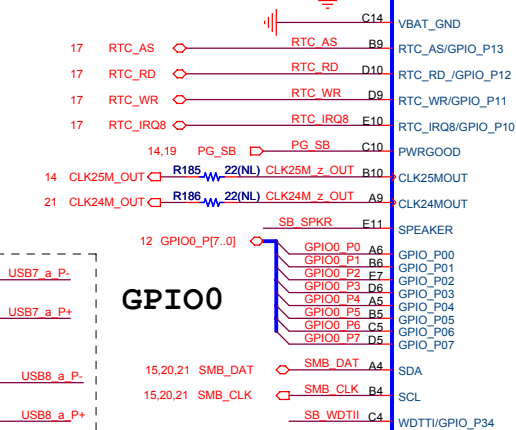
WatchDog Timer



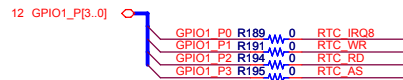
USB



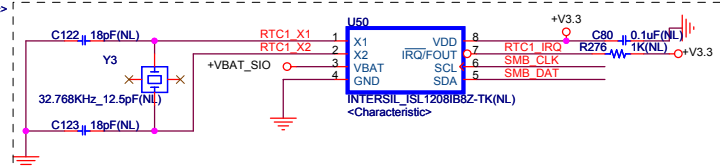
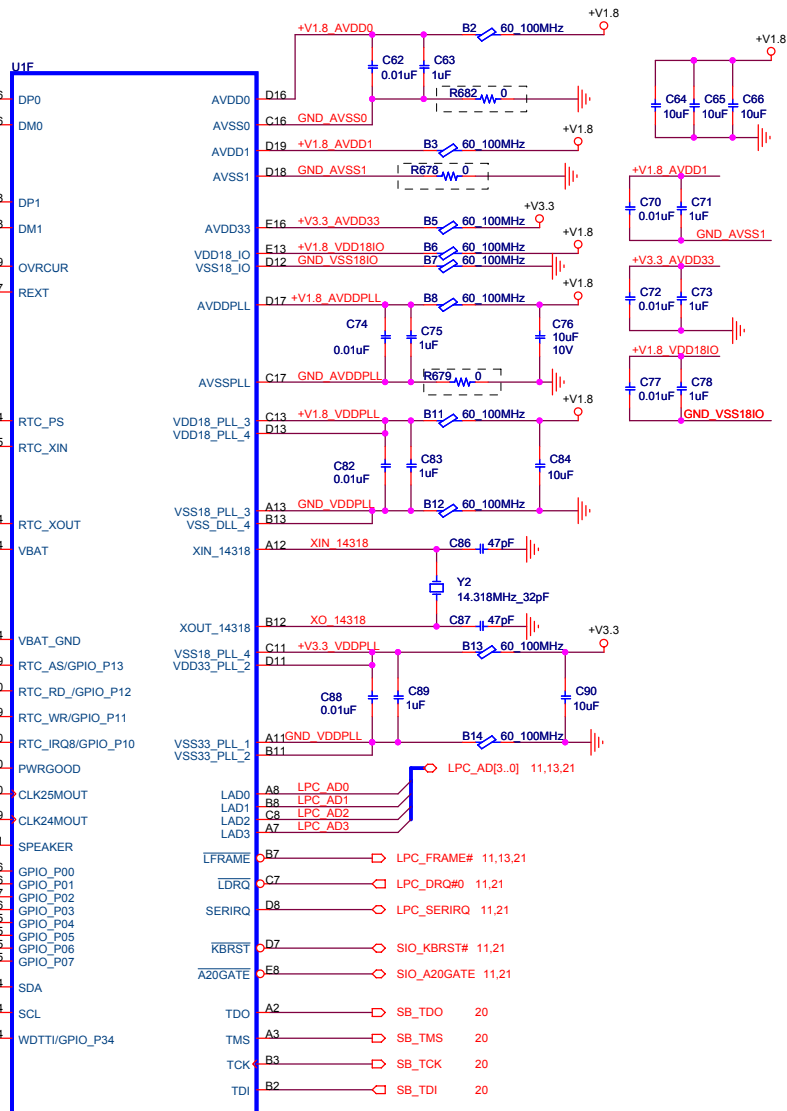
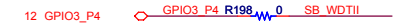
GPIO0

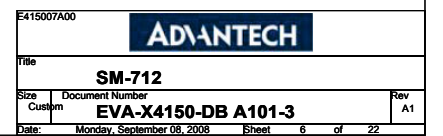


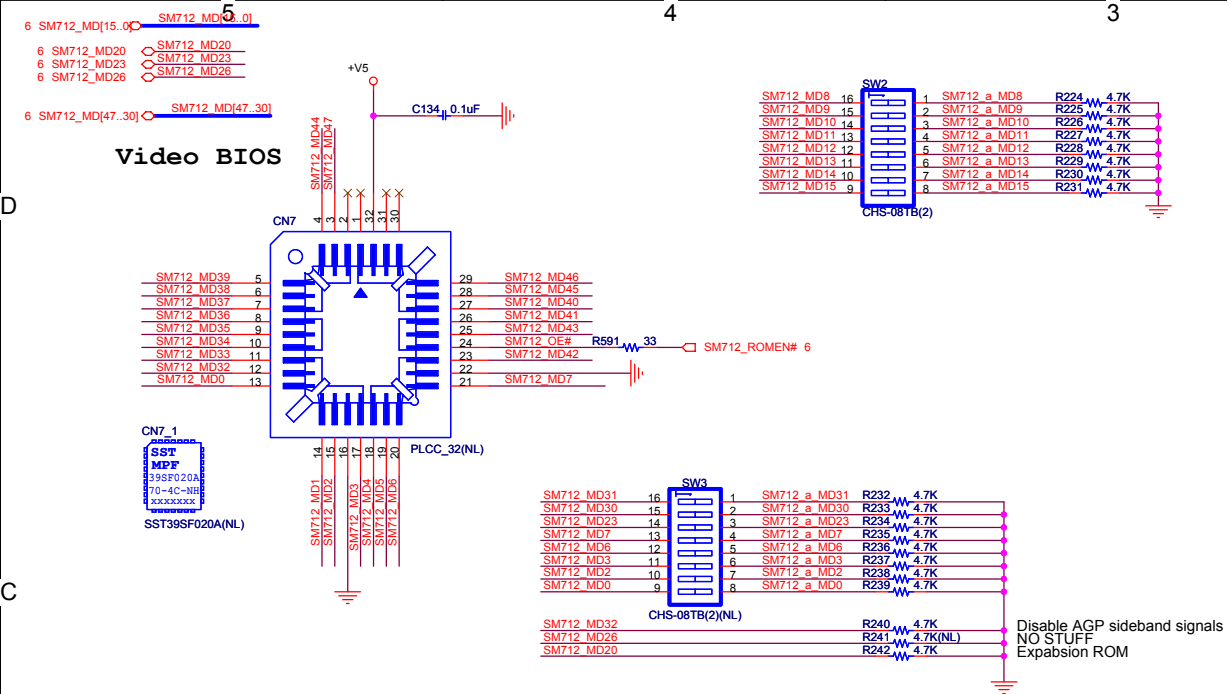
GPIO1



GPI03





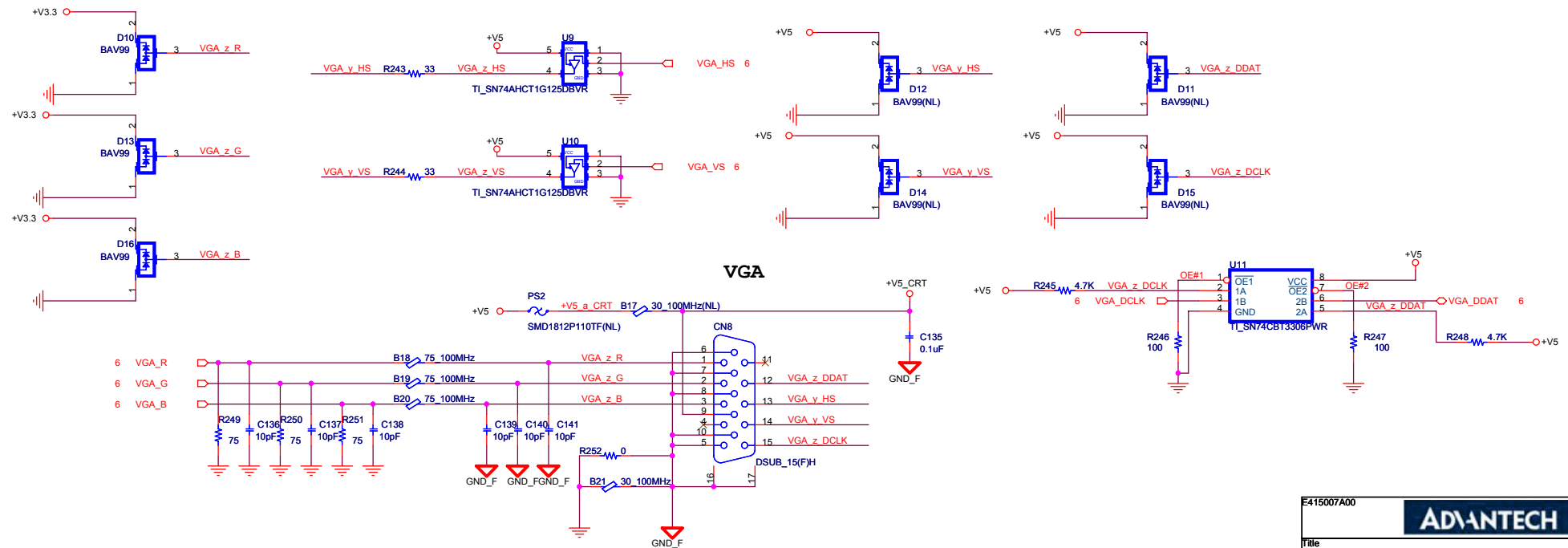


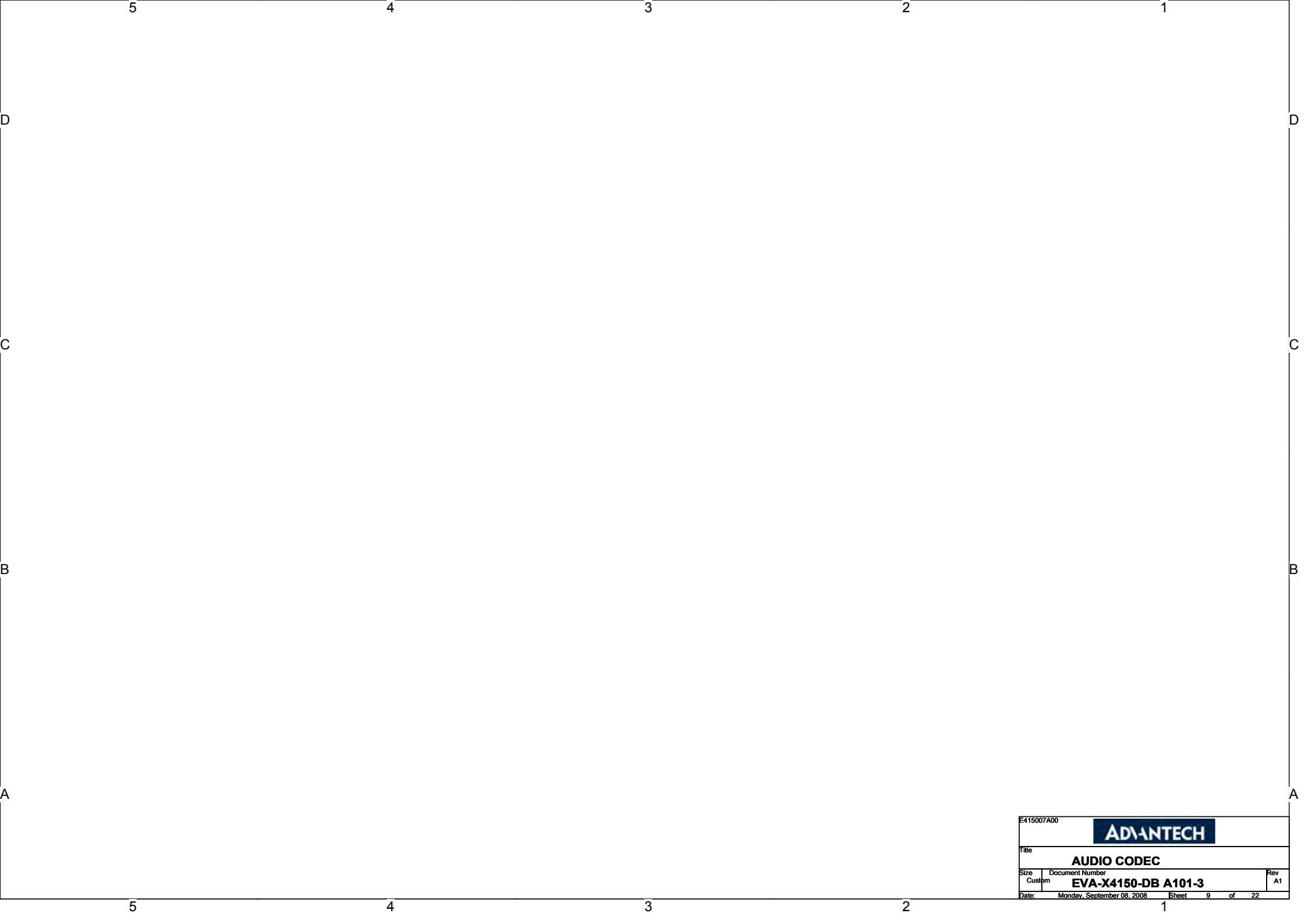
Panel Display Settings:			
SW	MD Bits	STATE	SETTING
1	MD8	0 1	*TFT DSTN
2	MD9	0 1	*FPClk Normal FPClk Inverted
4:3	MD[11:10]	0 0 0 1 1 0 1 1	640 x 480 *800 x 600 1024 x 768 1280 x 1024
7:6:5	MD[14:12]	0 0 0 0 0 1 0 1 0 0 1 1 1 0 0 1 0 1 1 1 0	9-BIT TFT 12-BIT TFT *18-BIT TFT 24-BIT TFT 12x2 - BIT TFT ANALOGTFT 18x2 - BIT TFT
8	MD15	0 1	16-BIT DSTN 24-BIT DSTN


Notes:

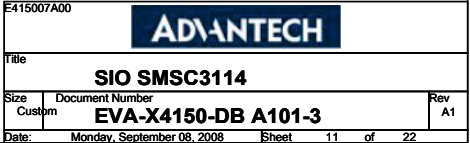
- (1) MD pins have internal pull-up resistors
- (2) 0 = Switch is ON; 1 = Switch is OFF

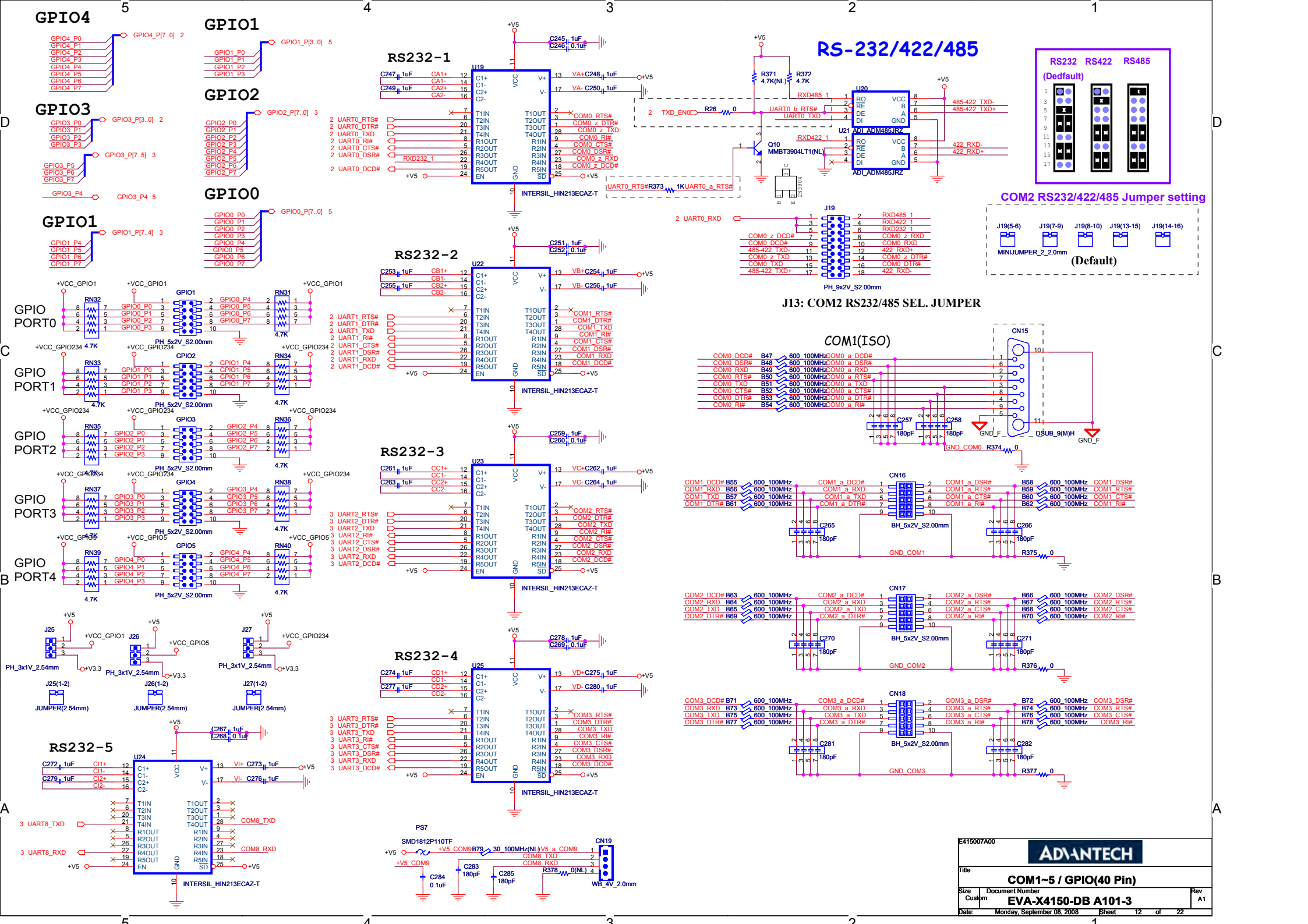
SW	MD Bits	STATE	SETTING	
1:2	MD[31:30]	00 01 11 10	Reserved	
3	MD23	0 1	Ext. mem. access only Both int. & ext. mem. access	
4	MD7	0 1	Int. logic running 1/2X MCLK Int. logic running 1X MCLK	
5	MD6	0 1	Enable 32-bit mem. bus Enable 64-bit mem. bus	
6	MD3	0 1	Reserved	
7:8	MD2		0	Enable ext. 32-bit mem
			1	Disable ext. 32-bit mem
	MD0			



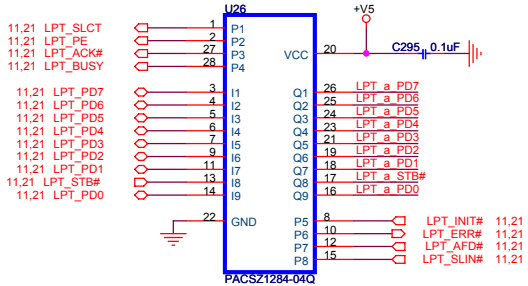


E415007A00			
Title			
AUDIO CODEC			
Size	Document Number		Rev
Custom	EVA-X4150-DB A101-3		A1
Date:	Monday, September 08, 2008		Sheet 9 of 22

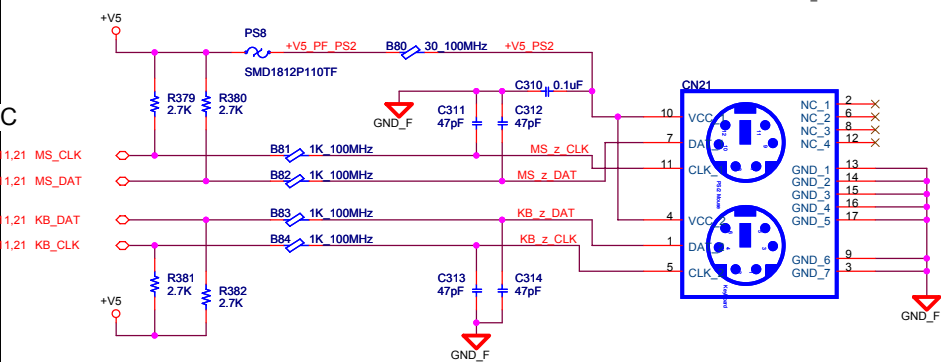




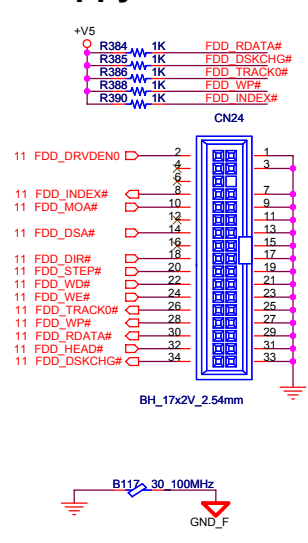
Printer Port



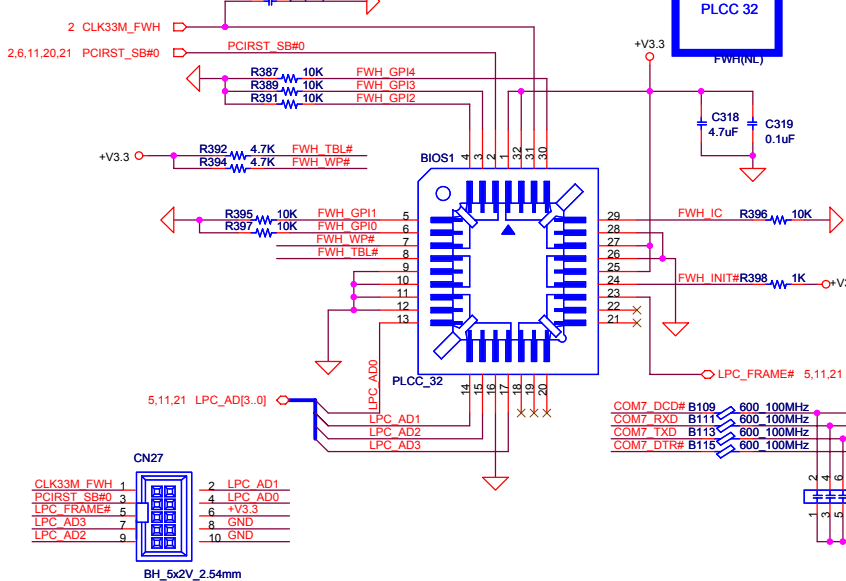
K.B. & MOUSE



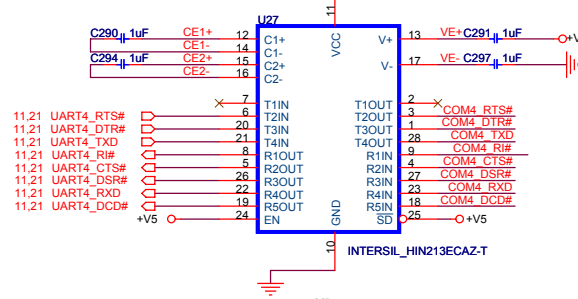
Floppy Port



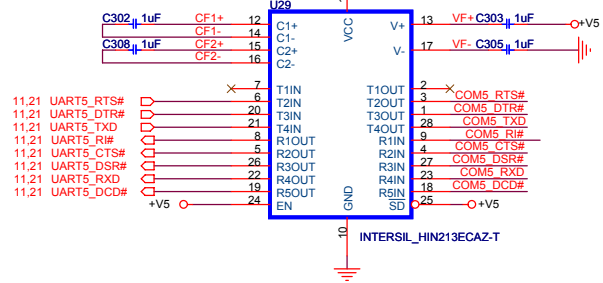
LPC Flash



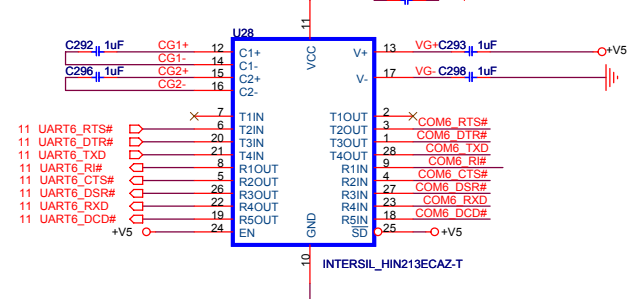
RS232-6



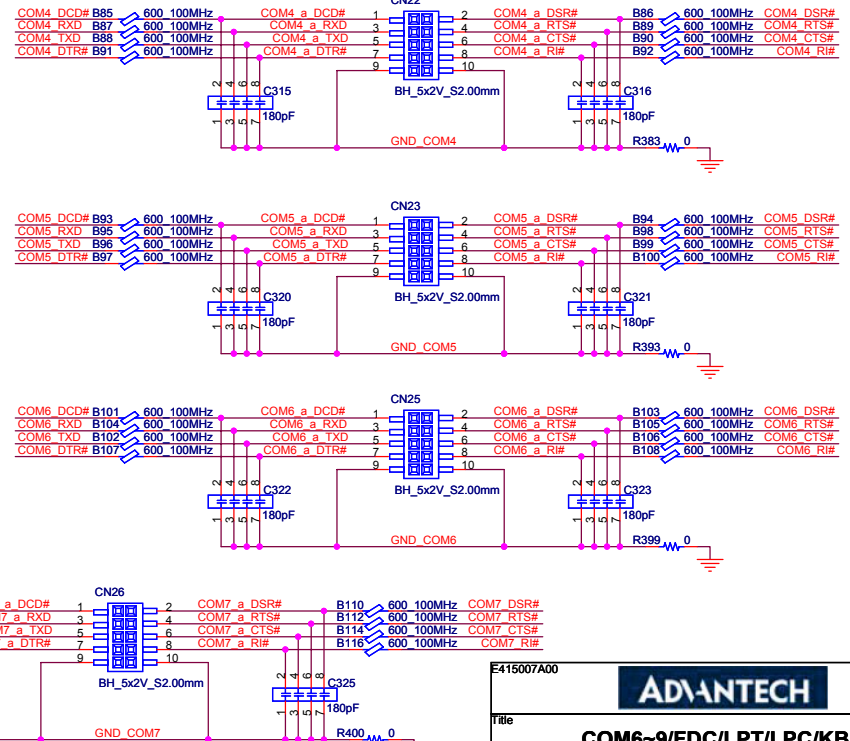
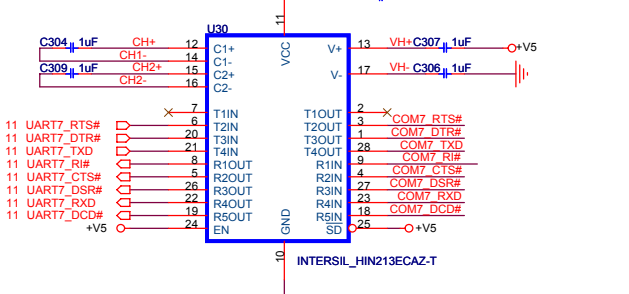
RS232-7

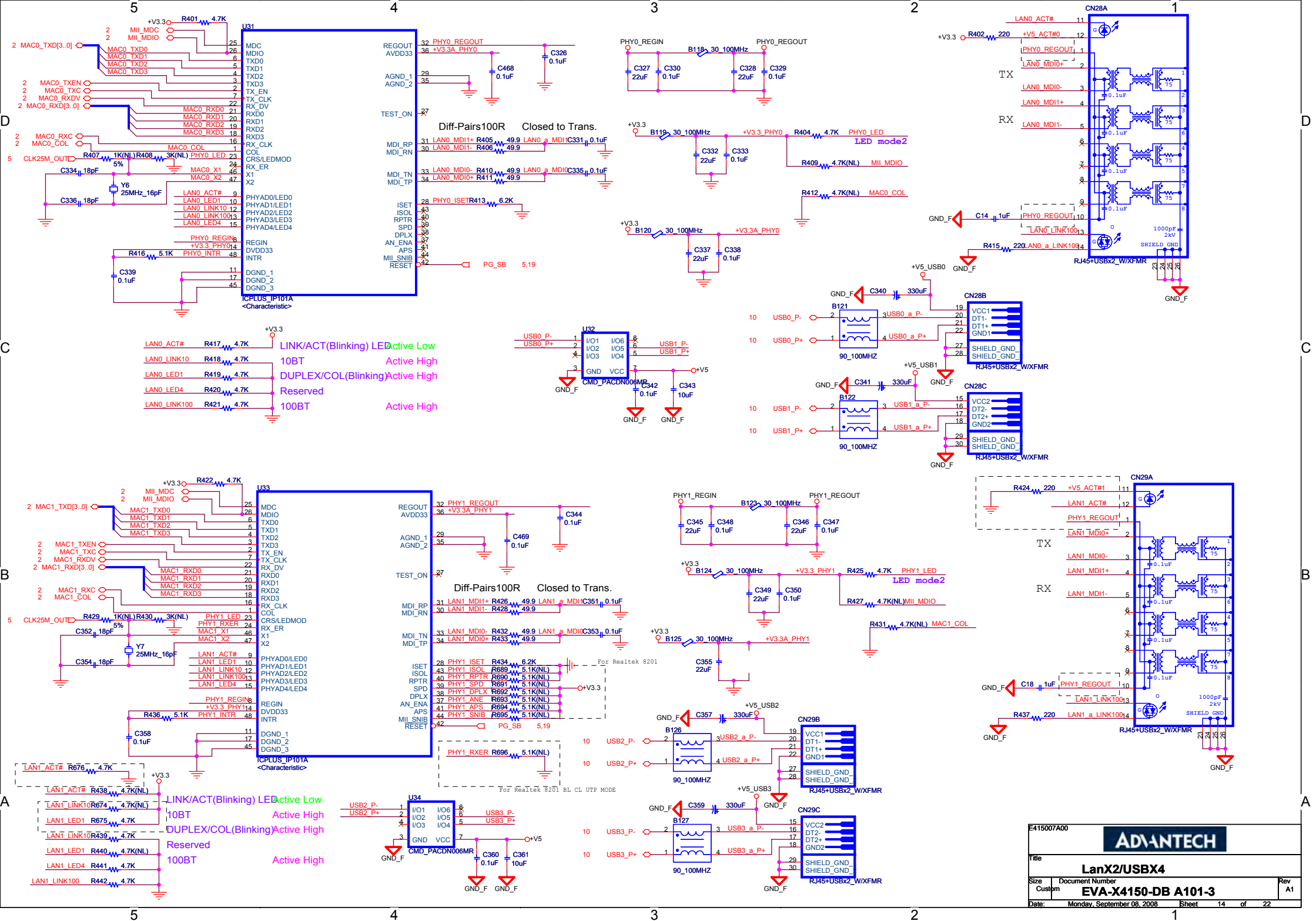


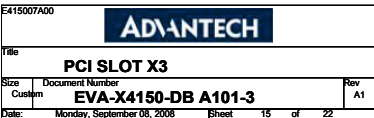
RS232-8

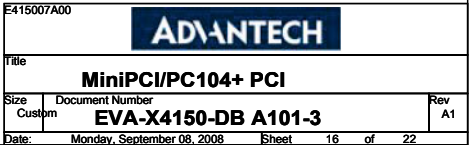


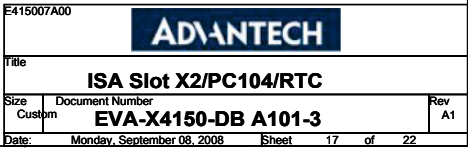
RS232-9

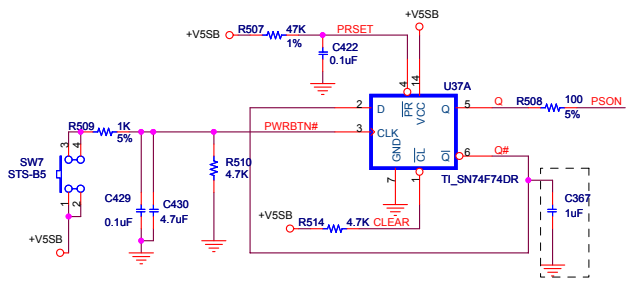
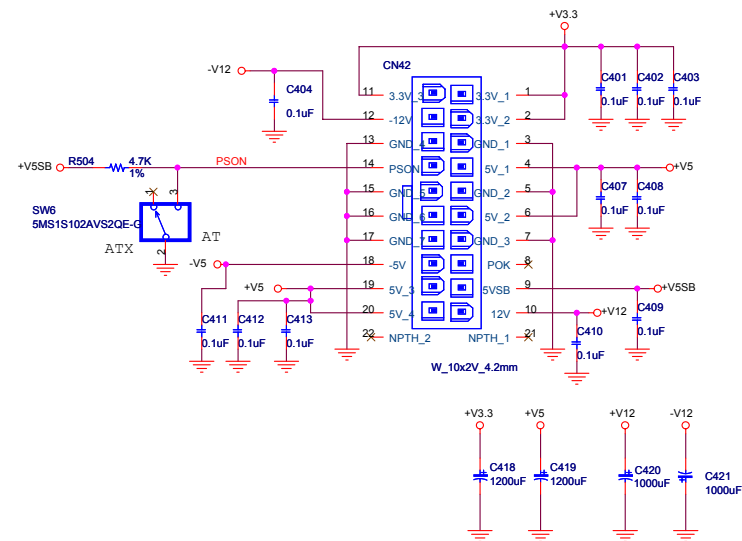
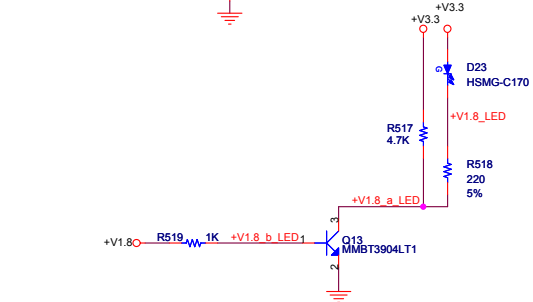
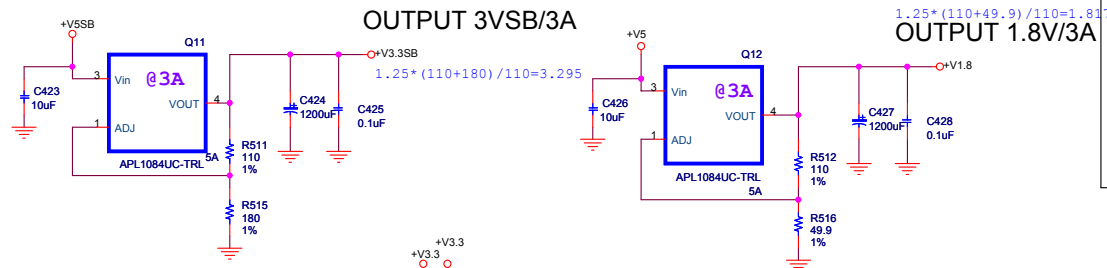
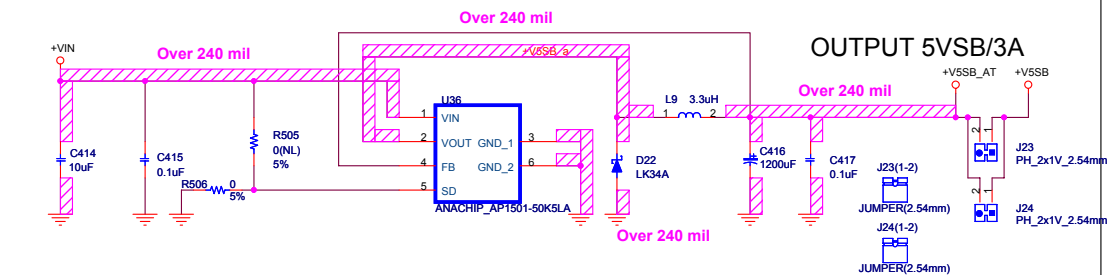
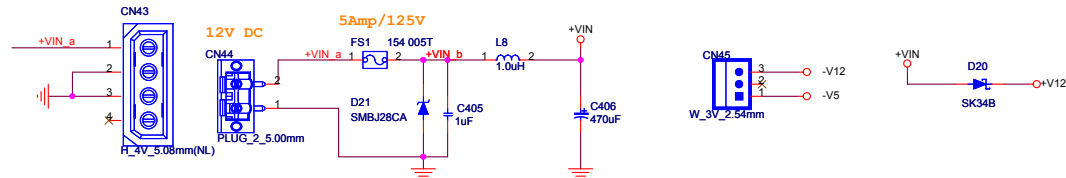






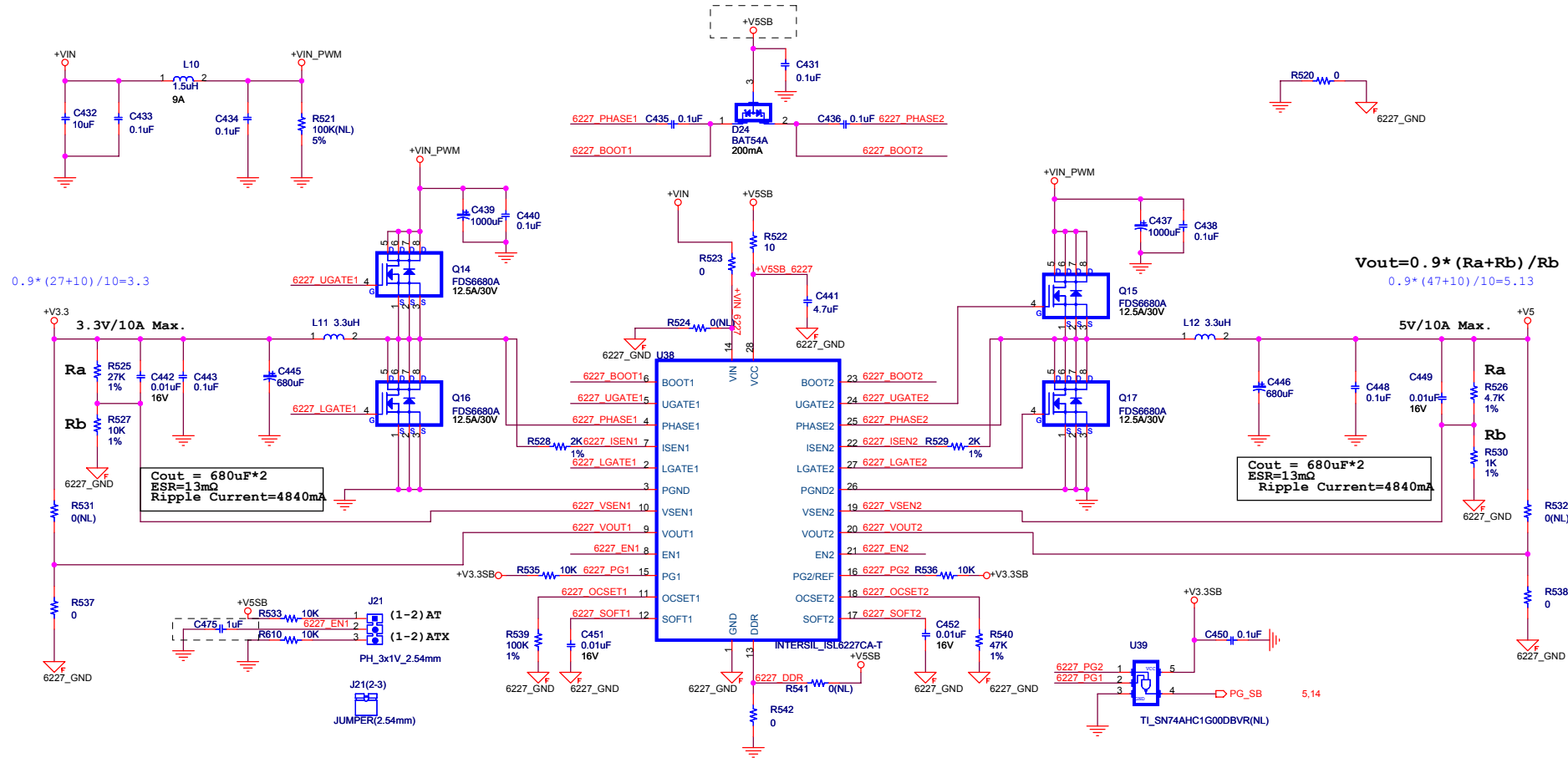






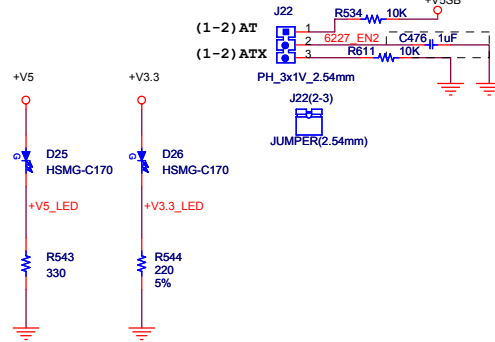
FUNCTION TABLE

INPUTS	OUTPUTS
PRE CLR CLK D Q Q	
L H X X H L	
H L X X L H	
H H ^ H H L	
H H ^ L L H	
H H L X Q Q 0 *	

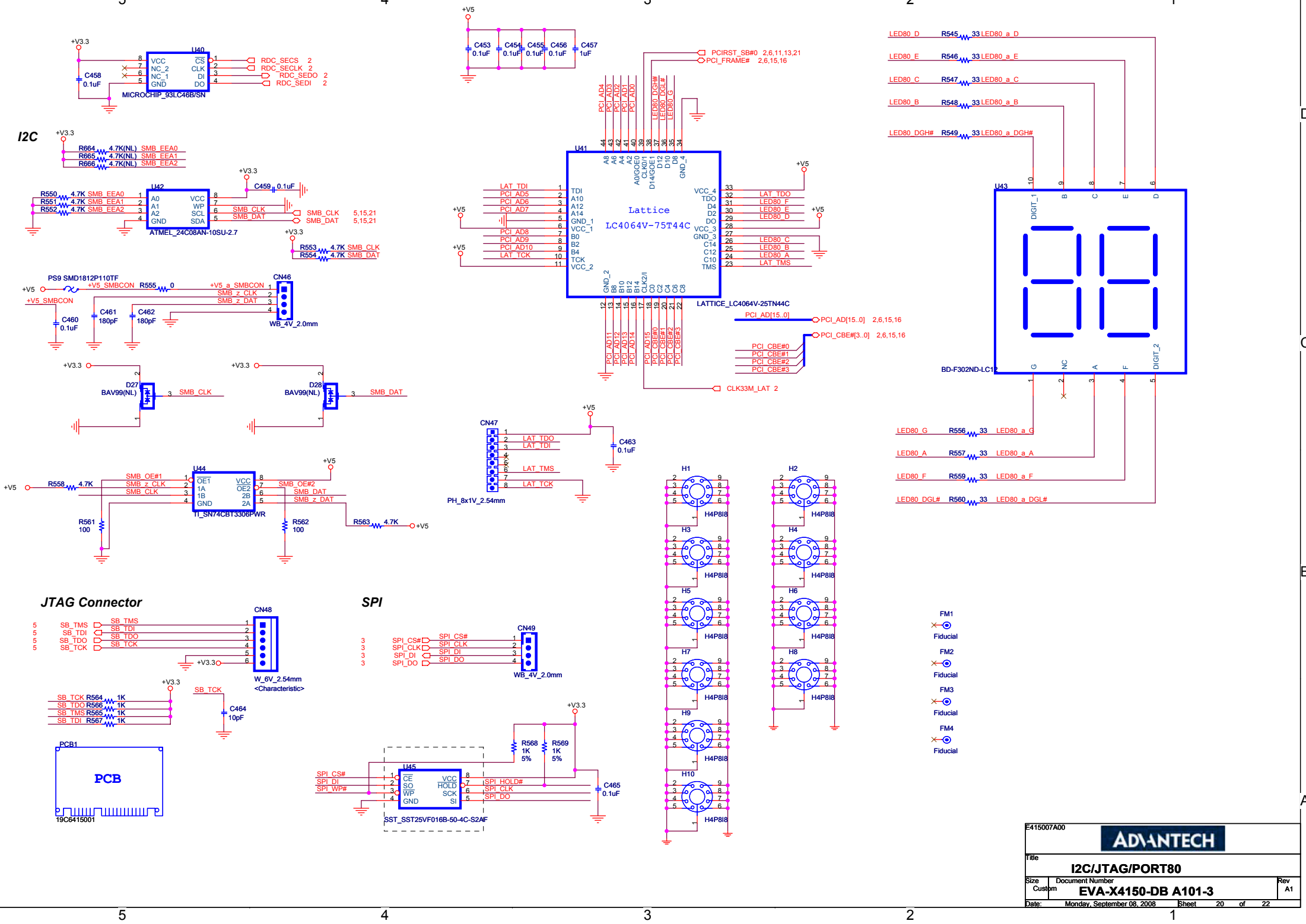


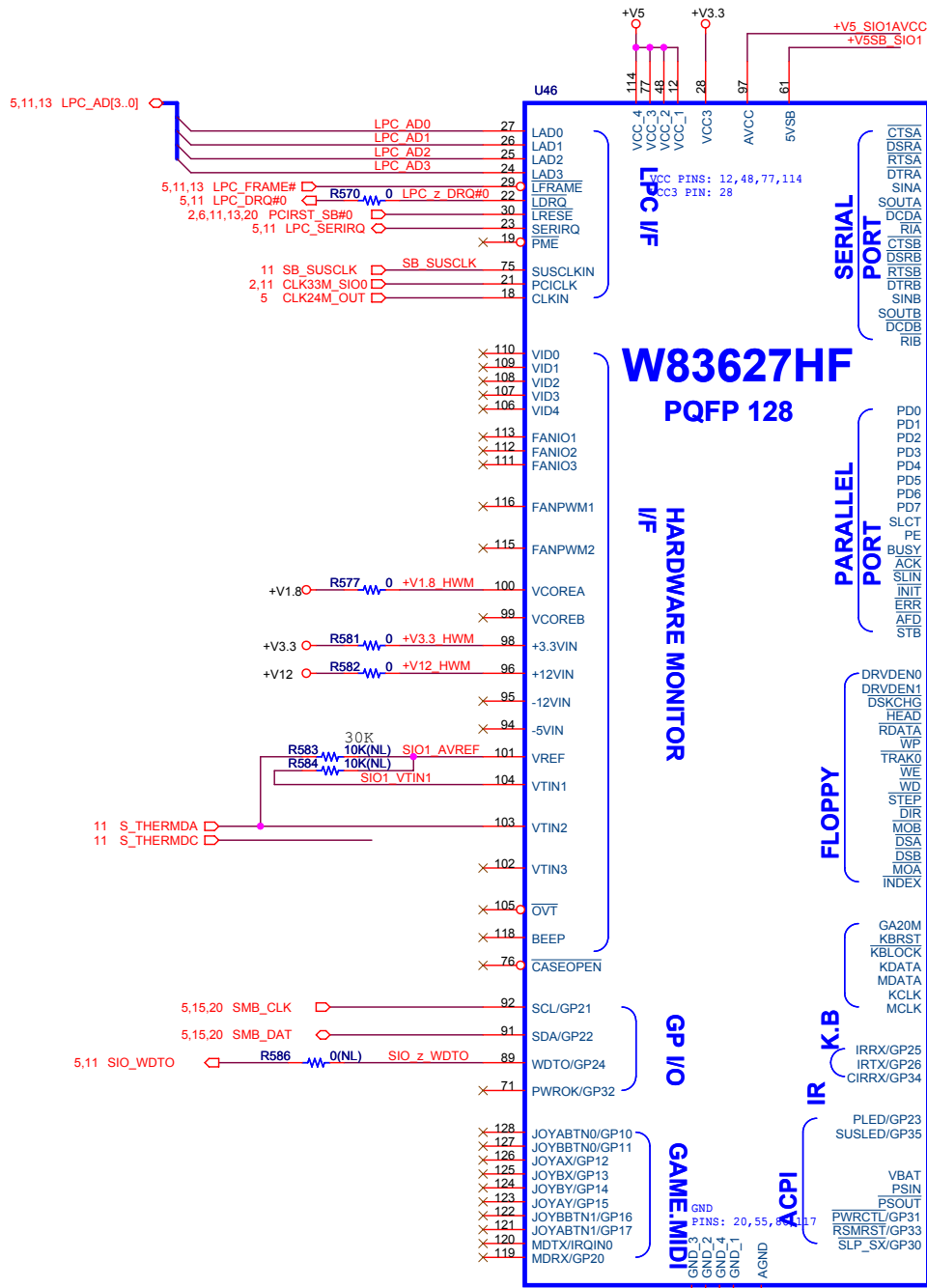
	Rset (R13,14)	Rcs (R6,11)	OCF Tripped point Tj @ 25 degree	OCF Tripped point Tj @ 85 degree
Initial	100K Ohm	2K Ohm	7.2A	5.8A
	120K Ohm	2K Ohm	5.9A	4.7A
	110K Ohm	1K Ohm	10A	*

	DDR	EN1	EN2	VIN	CH1/CH2 Phase	PG2/REF (Pin16)
Initial	0	1	1	0V<==>5V	180 degree	PG2
	1	1	1	4.2V<VIN<5V	90 degree	REF.
	1	1	1	VIN<4.2V	0 degree	REF.



VOT1/VOUT2	Inductor Current	Operation Mode
GND	Any value	Forced PWM
Connects to output voltage	<=IHYS	HYS
	>IHYS	PWM Initial





I/O Port: 2E
CLKIN: 48MHz

W83627HF

PQFP 128

HARDWARE MONITOR

GP I/O

GAME.MIDI

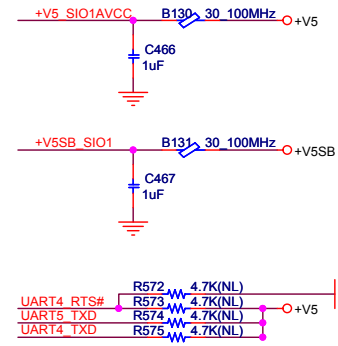
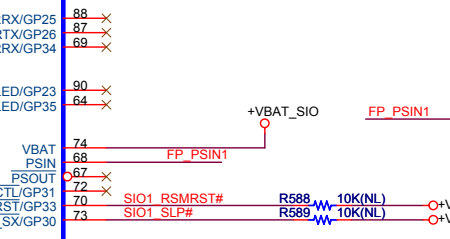
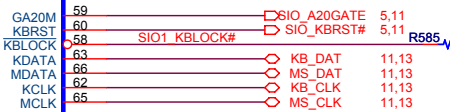
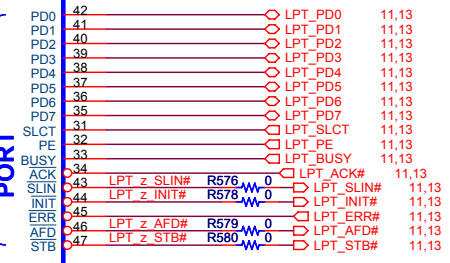
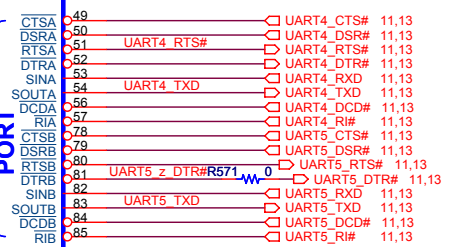
SERIAL PORT

PARALLEL PORT

FLOPPY

IR K.B

ACPI



UART_RTS1#	I/O Port
★ 0	2E
1	4E

UART_TX2	CLK IN
0	24MHz
★ 1	48MHz

UART_TX1	KBC
0	Disable
★ 1	Enable

External pull-ups must be placed on the nKBRST and A20M pins.

WINBOND_W83627HG-AW(NL)

ADVANTECH

Title

Size

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Monday, September 08, 2008

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Rev

A1

LPC SIO(W83627HF)

EVA-X4150-DB A101-3

EVA-X4150-DB Revision History

EVA-X4150-DB A101-1 Date: 2007/10/02 PCB:19C6415000

First Version schematic.

EVA-X4150-DB A101-2 Date: 2008/02/12 PCB:19C6415001

PG03: CF net IDE1_Ds are changed to IDE1_a_Ds.
PG04: Add R286 and R290.
PG05: B9,B4,B10 are changed to 0 ohm.
PG05: c81,C85 are changed to 22P.
PG05: Add C124.
PG05: Remove J12,J13 and add R683~R688.
PG06: Remove OSC2 and Q3.
PG08: Modify LVDS Transmitter U12 connection.
PG09: Remove Audio.
PG11: Remove OSC4 and change R350~R353 to 470ohms.
PG12: Modify Q10 connection and connect TXD_EN0 to U20.
PG12: Change CN15 COM1 conn connection.
PG14: CN28, CN29 pin 10 connected to power.
PG14: Add R689~R696 in PHY1 to reserve Realtek PHY using.
PG14: Change LAN2 PHY ID address.
PG16: Connect D24 to +V5SB.
PG19: Add R523, but R524 is not loading.
PG19: Change R528,R529 to 2K ohms.
PG19: Change R539 to 100K ohms and R540 to 47K ohms..
PG19: Add C475, C476.
PG19: Change SPI Flash U45 to 16Mb.

EVA-X4150-DB A101-3 Date: 2008/09/08 PCB:19C6415001

Modify Notes.