Radio and Television Interference

This equipment has been tested and found to comply with CISPR EN55022 Class A and EN61000-6-1 requirements and also with the limits for a Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference, in which case the user will be required to correct the interference at his own expense.

Changes or modifications to this equipment not expressly approved by Data Translation could void your authority to operate the equipment under Part 15 of the FCC Rules.

Note: This product was verified to meet FCC requirements under test conditions that included use of shielded cables and connectors between system components. It is important that you use shielded cables and connectors to reduce the possibility of causing interference to radio, television, and other electronic devices.

Canadian Department of Communications Statement

This digital apparatus does not exceed the Class A limits for radio noise emissions from digital apparatus set out in the Radio Interference Regulations of the Canadian Department of Communications.

Le présent appareil numérique n’émet pas de bruits radioélectriques dépassant les limites applicables aux appareils numériques de la class A prescrites dans le Règlement sur le brouillage radioélectrique édicté par le Ministère des Communications du Canada.
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About this Manual

The first part of this manual describes how to install and set up your DT9816 module and software, and verify that your module is working properly.

The second part of this manual describes the features of the DT9816 module and device driver, and how to program the DT9816 module using the DT-Open Layers for .NET Class Library™ software. Troubleshooting information is also provided.

Note: For information on checking system requirements, installing the software, and viewing the documentation, refer to the README file on the OMNI CD.

For more information on the class library, refer to the DT-Open Layers for .NET Class Library User's Manual. If you are using the DataAcq SDK or a software application to program your device, refer to the documentation for that software for more information.

Unless otherwise specified, all references to the DT9816 module refer to the DT9816, DT9816-A, and DT9816-S model numbers.

Intended Audience

This document is intended for engineers, scientists, technicians, or others responsible for using and/or programming the DT9816 module for analog input, digital I/O, or counter/timer operations in the Microsoft® Windows® XP, Windows Vista®, or Windows 7 operating system. It is assumed that you have some familiarity with data acquisition principles and that you understand your application.

How this Manual is Organized

This manual is organized as follows:

• Chapter 1, “Overview,” describes the major features of the DT9816 module, as well as the supported software and accessories for the module.

• Chapter 2, “Setting Up and Installing the Module,” describes how to install the module and how to configure the device driver.

• Chapter 3, “Wiring Signals to the Module,” describes how to wire signals to the module.

• Chapter 4, “Verifying the Operation of a Module,” describes how to verify the operation of the module with the Quick DataAcq application.

• Chapter 5, “Principles of Operation,” describes all of the features of the DT9816 module and how to use them in your application.

• Chapter 6, “Supported Device Driver Capabilities,” lists the supported subsystems and the associated capabilities accessible using the device driver for the DT9816 module.
• **Chapter 7, “Troubleshooting,”** provides information that you can use to resolve problems with a DT9816 module, should they occur.

• **Appendix A, “Specifications,”** lists the specifications of the DT9816 module.

• **Appendix B, “Screw Terminal Assignments,”** shows the screw terminal assignments of the DT9816 module.

• An index completes this manual.

### Conventions Used in this Manual

The following conventions are used in this manual:

• Notes provide useful information or information that requires special emphasis, cautions provide information to help you avoid losing data or damaging your equipment, and warnings provide information to help you avoid catastrophic damage to yourself or your equipment.

• Items that you select or type are shown in **bold**.

### Related Information

Refer to the following documents for more information on using the DT9816 module:

• *Benefits of the Universal Serial Bus for Data Acquisition.* This white paper describes why USB is an attractive alternative for data acquisition. It is available on the Data Translation web site (www.datatranslation.com).

• *Measure Foundry Manual* (UM-19298) and online help. These documents describe how to use DT Measure Foundry™ to build drag-and-drop test and measurement applications for Data Translation® data acquisition devices.

• *DT-Open Layers for .NET User’s Manual* (UM-22161). For programmers who are developing their own application programs using Visual C# or Visual Basic .NET, this manual describes how to use the DT-Open Layers for .NET Class Library to access the capabilities or Data Translation data acquisition devices.

• *DataAcq SDK User’s Manual* (UM-18326). For programmers who are developing their own application programs using the Microsoft C compiler, this manual describes how to use the DT-Open Layers DataAcq SDK™ to access the capabilities of Data Translation data acquisition devices.

• *DTx-EZ Getting Started Manual* (UM-15428). This manual describes how to use the ActiveX controls provided in DTx-EZ™ to access the capabilities of Data Translation data acquisition devices in Microsoft Visual Basic® or Visual C++®.

• *DAQ Adaptor for MATLAB* (UM-22024). This document describes how to use Data Translation’s DAQ Adaptor to provide an interface between the MATLAB Data Acquisition subsystem from The MathWorks and Data Translation’s DT-Open Layers architecture.

• *LV-Link Online Help.* This help file describes how to use LV-Link™ with the LabVIEW™ graphical programming language to access the capabilities of Data Translation data acquisition devices.
• Microsoft Windows XP, Windows Vista, or Windows 7 documentation.
• USB web site (http://www.usb.org).

Where To Get Help

Should you run into problems installing or using a DT9816 module, the Data Translation Technical Support Department is available to provide technical assistance. Refer to Chapter 7 for more information. If you are outside the United States or Canada, call your local distributor, whose number is listed on Data Translation’s web site (www.datatranslation.com).
Overview

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Key Hardware Features

The DT9816 module is part of the ECONseries of economy, multifunction mini-instruments. Three versions of the DT9816 module are available: the DT9816, the DT9816-A, and the DT9816-S. Table 1 summarizes the key features of each module.

Table 1: Key Features of the DT9816 Module

<table>
<thead>
<tr>
<th>Module</th>
<th>Analog Inputs</th>
<th>Analog Input Resolution</th>
<th>I/O Range</th>
<th>Analog Input Sample Rate</th>
<th>Digital I/O</th>
<th>C/T</th>
</tr>
</thead>
<tbody>
<tr>
<td>DT9816</td>
<td>6 SE</td>
<td>16-bit</td>
<td>±10 V or ±5 V</td>
<td>50 kS/s per channel</td>
<td>8 in/8 out</td>
<td>1</td>
</tr>
<tr>
<td>DT9816-A</td>
<td>6 SE</td>
<td>16-bit</td>
<td>±10 V or ±5 V</td>
<td>150 kS/s per channel</td>
<td>8 in/8 out</td>
<td>1</td>
</tr>
<tr>
<td>DT9816-S</td>
<td>6 SE</td>
<td>16-bit</td>
<td>±10 V or ±5 V</td>
<td>750 kS/s per channel</td>
<td>8 in/8 out</td>
<td>1</td>
</tr>
</tbody>
</table>

a. Actual maximum is 153.846 kS/s.

The key features are listed as follows:

- Six, independent, successive-approximation A/D converters with track-and-hold circuitry. Each converter uses a common clock and trigger for simultaneous sampling of all six analog input signals at up to 50 kHz per channel (DT9816), 150 kHz per channel (DT9816-A), or 750 kHz per channel (DT9816-S).

- Analog input subsystem provides 16-bit resolution and gains of 1 and 2 for effective full-scale input signal ranges of ±10 V and ±5 V.

- 8 digital inputs and 8 digital outputs

- 1, 16-bit counter/timer allows you to perform event counting, frequency measurement, and continuous pulse output (rate generation) operations

- Standard USB connector

- Shielded, rugged enclosure for noise immunity

- Low power device – draws less than 100 mA
Supported Software

The following software is available for use with the DT9816 module, and is provided on the OMNI CD:

- **Device Driver** – The DT9816 Device Driver allows you to use a DT9816 module with any of the supported software packages or utilities. Refer to page 25 for more information on configuring the device driver.

- **Quick DataAcq application** – The Quick DataAcq application provides a quick way to get up and running using a DT9816 module. Using this application, you can verify key features of the module, display data on the screen, and save data to disk. Refer to Chapter 4 for more information on using the Quick DataAcq application.

- **The quickDAQ application** – An evaluation version of this .NET application is included on the Data Acquisition OMNI CD. quickDAQ lets you acquire analog data from all devices supported by DT-Open Layers for .NET software at high speed, plot it during acquisition, analyze it, and/or save it to disk for later analysis.

- **Measure Foundry** – An evaluation version of this software is included or provided via a link on the OMNI CD. DT Measure Foundry is drag-and-drop test and measurement application builder designed to give you top performance with ease-of-use development. Order the full development version of this software package to develop your own application using real hardware.

- **DT-Open Layers for .NET Class Library** – Use this class library if you want to use Visual C# or Visual Basic for .NET to develop your own application software for a DT9816 module using Visual Studio 2003 or Visual Studio 2005; the class library complies with the DT-Open Layers standard.

- **DataAcq SDK** – Use the Data Acq SDK if you want to use Visual Studio 6.0 and Microsoft C or C++ to develop your own application software for a DT9816 module using Windows XP, Windows Vista, or Windows 7; the DataAcq SDK complies with the DT-Open Layers standard.

- **DTx-EZ** – Use this optional software package if you want to use ActiveX controls to access the capabilities of the DT9816 module using Microsoft Visual Basic or Visual C++; DTx-EZ complies with the DT-Open Layers standard.

- **DAQ Adaptor for MATLAB** – Data Translation’s DAQ Adaptor provides an interface between the MATLAB Data Acquisition (DAQ) subsystem from The MathWorks and Data Translation’s DT-Open Layers architecture.

- **LV-Link** – An evaluation version of LV-Link is provided on the OMNI CD. Use this software package if you want to use the LabVIEW graphical programming language to access the capabilities of the DT9816 module.
Getting Started Procedure

The flow diagram shown in Figure 1 illustrates the steps needed to get started using the DT9816 module. This diagram is repeated in each chapter; the shaded area in the diagram shows you where you are in the getting started procedure.

Set Up and Install the Module
(see Chapter 2 starting on page 19)

Wire Signals to the Module
(see Chapter 3 starting on page 27)

Verify the Operation of the Module
(see Chapter 4 starting on page 35)

Figure 1: Getting Started Flow Diagram
Part 1: Getting Started
Setting Up and Installing the Module

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Chapter 2

Set Up and Install the Module
(this chapter)

Wire Signals to the Module
(see Chapter 3 starting on page 27)

Verify the Operation of the Module
(see Chapter 4 starting on page 35)
Unpacking

Open the shipping box and verify that the following items are present:

- DT9816 module
- OMNI CD
- USB cable

If an item is missing or damaged, contact Data Translation. If you are in the United States, call the Customer Service Department at (508) 481-3700, ext. 1323. An application engineer will guide you through the appropriate steps for replacing missing or damaged items. If you are located outside the United States, call your local distributor (see Data Translation’s web site [www.datatranslation.com](http://www.datatranslation.com) for contact information).

---

**Note:** The DT9816 module is factory-calibrated and requires no further adjustment.
Attaching Modules to the Computer

This section describes how to attach a DT9816 module to the host computer.

Note: Most computers have several USB ports that allow direct connection to USB devices. If your application requires more DT9816 modules than you have USB ports for, you can expand the number of USB devices attached to a single USB port by using expansion hubs. For more information, refer to page 23.

You can unplug a module, and then plug it in again, if you wish, without causing damage. This process is called hot-swapping. Your application may take a few seconds to recognize a module once it is plugged back in.

Connecting Directly to the USB Ports

To connect DT9816 modules directly to the USB ports of your computer, do the following:

1. Attach one end of the USB cable to the USB port on the module.

2. Attach the other end of the USB cable to one of the USB ports on the host computer, as shown in Figure 2.

   The operating system automatically detects the USB module and starts the Found New Hardware wizard.

   ![Figure 2: Attaching the Module to the Host Computer](image)

3. For Windows Vista:
   a. Click Locate and install driver software (recommended).
      The popup message “Windows needs your permission to continue” appears.
   b. Click Continue.
      The Windows Security dialog box appears.
   c. Click Install this driver software anyway.
      The LED on the module turns green.
For Windows XP:

a. Click **Next** and/or **Finish** as required in the wizard.
   *Once the firmware is loaded, the wizard restarts to initiate the firmware to accept commands.*

b. Click **Next** and/or **Finish** again.
   *The LED on the module turns green.*

---

**Note:** Windows 7 finds the device automatically.

---

4. Repeat these steps to attach another DT9816 module to the host computer, if desired.

---

**Connecting to an Expansion Hub**

Expansion hubs are powered by their own external power supply. The practical number of DT9816 modules that you can connect to a single USB port depends on the throughput you want to achieve.

To connect multiple DT9816 modules to an expansion hub, do the following:

1. Attach one end of the USB cable to the module and the other end of the USB cable to an expansion hub.

2. Connect the power supply for the expansion hub to an external power supply.

3. Connect the expansion hub to the USB port on the host computer using another USB cable.
   *The operating system automatically detects the USB module and starts the Found New Hardware wizard.*

4. **For Windows Vista:**
   a. Click **Locate and install driver software (recommended).**
      *The popup message “Windows needs your permission to continue” appears.*
   b. Click **Continue.**
      *The Windows Security dialog box appears.*
   c. Click **Install this driver software anyway.**
      *The LED on the module turns green.*

   **For Windows XP:**
   a. Click **Next** and/or **Finish** as required in the wizard.
      *Once the firmware is loaded, the wizard restarts to initiate the firmware to accept commands.*
   b. Click **Next** and/or **Finish** again.
      *The LED on the module turns green.*

---

**Note:** Windows 7 finds the device automatically.
5. Repeat these steps until you have attached the number of expansion hubs and modules that you require. Refer to Figure 3. 

*The operating system automatically detects the USB devices as they are installed.*
Changing the Name of a Module (Optional)

To change the name of a DT9816 module, configure the device driver as follows:

1. From the Windows Start menu, select Settings | Control Panel.
2. From the Control Panel, double-click Open Layers Control Panel. The Data Acquisition Control Panel dialog box appears.
3. If you want to rename the module, click the DT9816 module that you want to rename, and then click Edit Name.
4. Enter a new name for the module, and then click OK. The name is used to identify the module in all subsequent applications.
5. When you are finished configuring the module, click Close.
6. Repeat steps 3 to 5 for the other modules that you want to configure.
7. Close the Data Acquisition Control Panel dialog box.

Continue with the instructions on wiring in Chapter 3 starting on page 27.
Wiring Signals to the Module

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Connecting Digital I/O Signals .................................................... 31
Connecting Counter/Timer Signals ............................................. 32
Preparing to Wire Signals

This section provides recommendations and information about wiring signals to a DT9816 module.

Wiring Recommendations

Keep the following recommendations in mind when wiring signals to a DT9816 module:

- Follow standard ESD procedures when wiring signals to the module.
- Use individually shielded twisted-pair wire (size 16 to 26 AWG) in highly noisy electrical environments.
- Separate power and signal lines by using physically different wiring paths or conduits.
- To avoid noise, do not locate the box and cabling next to sources that produce high electromagnetic fields, such as large electric motors, power lines, solenoids, and electric arcs, unless the signals are enclosed in a mumetal shield.
- Prevent electrostatic discharge to the I/O while the box is operational.
- Connect all unused analog input channels to analog ground.

Wiring Locations

You wire signals to the DT9816 module using the screw terminals on the module. Table 2 lists the screw terminal assignments.

**Table 2: DT9816 Screw Terminal Assignments**

<table>
<thead>
<tr>
<th>Terminal</th>
<th>Signal</th>
<th>Terminal</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>20</td>
<td>USB +5 V Out</td>
<td>40</td>
<td>Ext Trigger</td>
</tr>
<tr>
<td>19</td>
<td>Ground</td>
<td>39</td>
<td>Ext Clock</td>
</tr>
<tr>
<td>18</td>
<td>Counter 0 In</td>
<td>38</td>
<td>Ground</td>
</tr>
<tr>
<td>17</td>
<td>Counter 0 Out</td>
<td>37</td>
<td>Digital Output 7</td>
</tr>
<tr>
<td>16</td>
<td>Counter 0 Gate</td>
<td>36</td>
<td>Digital Output 6</td>
</tr>
<tr>
<td>15</td>
<td>Ground</td>
<td>35</td>
<td>Digital Output 5</td>
</tr>
<tr>
<td>14</td>
<td>Reserved</td>
<td>34</td>
<td>Digital Output 4</td>
</tr>
<tr>
<td>13</td>
<td>Reserved</td>
<td>33</td>
<td>Digital Output 3</td>
</tr>
<tr>
<td>12</td>
<td>Reserved</td>
<td>32</td>
<td>Digital Output 2</td>
</tr>
<tr>
<td>11</td>
<td>Reserved</td>
<td>31</td>
<td>Digital Output 1</td>
</tr>
<tr>
<td>10</td>
<td>2.5 V Reference</td>
<td>30</td>
<td>Digital Output 0</td>
</tr>
<tr>
<td>9</td>
<td>Analog Ground</td>
<td>29</td>
<td>Ground</td>
</tr>
<tr>
<td>8</td>
<td>Reserved</td>
<td>28</td>
<td>Digital Input 7</td>
</tr>
</tbody>
</table>
### Connecting Single-Ended Analog Input Signals

The DT9816 supports 6 single-ended analog input channels. Figure 4 shows how to connect single-ended voltage input signals (channels 0 and 1, in this case) to the screw terminals of a DT9816 module.

<table>
<thead>
<tr>
<th>Terminal</th>
<th>Signal</th>
<th>Terminal</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>7</td>
<td>Reserved</td>
<td>27</td>
<td>Digital Input 6</td>
</tr>
<tr>
<td>6</td>
<td>Analog Input CH5</td>
<td>26</td>
<td>Digital Input 5</td>
</tr>
<tr>
<td>5</td>
<td>Analog Input CH4</td>
<td>25</td>
<td>Digital Input 4</td>
</tr>
<tr>
<td>4</td>
<td>Analog Input CH3</td>
<td>24</td>
<td>Digital Input 3</td>
</tr>
<tr>
<td>3</td>
<td>Analog Input CH2</td>
<td>23</td>
<td>Digital Input 2</td>
</tr>
<tr>
<td>2</td>
<td>Analog Input CH1</td>
<td>22</td>
<td>Digital Input 1</td>
</tr>
<tr>
<td>1</td>
<td>Analog Input CH0</td>
<td>21</td>
<td>Digital Input 0</td>
</tr>
</tbody>
</table>

#### Table 2: DT9816 Screw Terminal Assignments (cont.)

**Note:** When using high sampling rates on the DT9816-S, it is recommended that you use an input impedance of 100 \( \Omega \) or less.

---

Figure 4: Connecting Single-Ended Inputs
Connecting Digital I/O Signals

Figure 5 shows how to connect digital input signals (lines 0 and 1, in this case) to the screw terminals of a DT9816 module.

![Figure 5: Connecting Digital Inputs](image)

Figure 6 shows how to connect digital output signals (line 0, in this case) to the screw terminals of a DT9816 module.

![Figure 6: Connecting Digital Outputs](image)
Connecting Counter/Timer Signals

The DT9816 module provides one counter/timer that you can use for the following operations:

- Event counting
- Frequency measurement
- Continuous pulse output (rate generation)

This section describes how to connect counter/timer signals for these operation modes. Refer to page 55 for more information about using the counter/timers.

Connecting Signals for Event Counting

Figure 7 shows how to connect counter/timer signals to the screw terminals on the DT9816 module to perform an event counting operation using an external gate.

In this example, the counter counts the number of rising edges that occur on the Counter 0 In signal when the Counter 0 Gate signal is in the active state (as specified by software). Refer to page 57 for more information.

![Figure 7: Connecting Counter/Timer Signals for an Event Counting Operation Using an External Gate](image)

Figure 8 shows how to connect counter/timer signals to the screw terminals on the DT9816 module to perform an event counting operation without using a gate (also called a software gate). The counter counts the number of rising edges that occur on the Counter 0 In signal.
Connecting Signals for Frequency Measurement

One way to measure frequency is to connect a pulse of a known duration to the Counter 0 Gate signal, as shown in Figure 9. In this case, the frequency of the Counter 0 In signal is the number of counts divided by the period of the signal connected to the Counter 0 Gate input.

Figure 8: Connecting Counter/Timer Signals for an Event Counting Operation Without Using a Gate

Figure 9: Connecting Counter/Timer Signals for a Frequency Measurement Operation Using an External Pulse
Connecting Signals for Rate Generation

Figure 10 shows how to connect counter/timer signals to the screw terminals of a DT9816 module to perform a rate generation (continuous pulse output) operation.

Figure 10: Connecting Counter/Timer Signals for a Rate Generation Operation
Verifying the Operation of a Module

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Set Up and Install the Module
(see Chapter 2 starting on page 19)

Wire Signals to the Module
(see Chapter 3 starting on page 27)

Verify the Operation of the Module
(this chapter)
Running the Quick DataAcq Application

The Quick DataAcq application is installed automatically when you install the driver software.

To run the Quick DataAcq application, do the following:

1. If you have not already done so, power up your computer and any attached peripherals.
2. Click Start from the Task Bar.
3. Browse to Programs | Data Translation, Inc | DT-Open Layers for Win32 | QuickDataAcq. The main menu appears.

Note: The Quick DataAcq application allows you to verify basic operations on the board; however, it may not support all of the board’s features.

For information on each of the features provided, use the online help for the Quick DataAcq application by pressing F1 from any view or selecting the Help menu. If the system has trouble finding the help file, navigate to C:\Program Files\Data Translation\Win32\dtdataacq.hlp, where C: is the letter of your hard disk drive.
Testing Single-Value Analog Input

To verify that the module can read a single analog input value, do the following:

1. Connect a voltage source, such as a function generator, to analog input channel 0 (single-ended mode) on the DT9816 module. Refer to page 30 for an example of how to connect a single-ended analog input.

2. In the Quick DataAcq application, choose Single Analog Input from the Acquisition menu.

3. Select the appropriate DT9816 module from the Board list box.

4. In the Channel list box, select analog input channel 0.

5. In the Range list box, select the range for the channel (±10 V).


7. Click Get to acquire a single value from analog input channel 0.

   The application displays the value on the screen in both text and graphical form.
Testing Continuous Analog Input

To verify that the module can perform a continuous analog input operation, do the following:

1. Connect known voltage sources, such as the outputs of a function generator, to analog input channels 0 and 1 on the DT9816 module (using the single-ended configuration). Refer to page 30 for an example of how to connect a single-ended analog input.

2. In the Quick DataAcq application, choose Scope from the Acquisition menu.

3. Select the appropriate DT9816 module from the Board list box.

4. In the Sec/Div list box, select the number of seconds per division (.1 to .00001) for the display.

5. In the Channel list box, select analog input channel 1, and then click Add to add the channel to the channel list.
   Note that, by default, channel 0 is included in the channel list.

6. Click Config from the Toolbar.

7. In the Config dialog, select ChannelType, and then select Single Ended.

8. In the Config dialog, select Range, and then select Bipolar.

9. From the Scope view, double-click the input range of the channel to change the input range of the module.
   The display changes to reflect the selected range for all the analog input channels on the module.

10. In the Trigger box, select Auto to acquire data continuously from the specified channels or Manual to acquire a burst of data from the specified channels.

11. Click Start from the Toolbar to start the continuous analog input operation.
    The application displays the values acquired from each channel in a unique color on the oscilloscope view.

12. Click Stop from the Toolbar to stop the operation.
Testing Single-Value Digital Input

To verify that the module can read a single digital input value, do the following:

1. Connect a digital input to digital input line 0 of port A on the DT9816 module. Refer to page 31 for an example of how to connect a digital input.

2. In the Quick DataAcq application, choose Digital Input from the Acquisition menu.

3. Select the appropriate DT9816 module from the Board list box.

4. Select digital input port A by clicking Port A.

5. Click Get.

The application displays the value of each digital input line in port A on the screen in both text and graphical form.
Testing Single-Value Digital Output

To verify that the module can output a single digital output value, do the following:

1. Connect a digital output to digital output line 0 of port B on the DT9816 module. Refer to page 31 for an example of how to connect a digital output.

2. In the Quick DataAcq application, select Digital Output from the Control menu.

3. Select the appropriate DT9816 module from the Board list box.

4. Select digital output port B by clicking Port B.

5. Click the appropriate bits to select the type of signal to write from the digital output lines. If the bit is selected, a high-level signal is output from the digital output line; if the bit is not selected, a low-level signal is output from the digital output line. Optionally, you can enter an output value in the Hex text box.

6. Click Send.

The application outputs and displays the value of each digital output line of digital port B on the screen in both text and graphical form.
Testing Frequency Measurement

To verify that the module can perform a frequency measurement operation, do the following:

1. Wire an external clock source to counter/timer 0 on the DT9816 module. Refer to page 33 for an example of how to connect signals to a counter/timer for a frequency measurement operation.

   **Note:** The Quick DataAcq application works only with counter/timer 0.

2. In the Quick DataAcq application, choose **Frequency Counter** from the **Acquisition** menu.

3. Select the appropriate DT9816 module from the **Board** list box.

4. In the **Count Duration** text box, enter the number of seconds during which events will be counted.

5. Click **Start** to start the frequency measurement operation. *The operation automatically stops after the number of seconds you specified has elapsed, and the application displays the frequency on the screen.*

If you want to stop the frequency measurement operation when it is in progress, click **Stop**.
Testing Pulse Output

To verify that the module can perform a pulse output operation, do the following:

1. Connect a scope to counter/timer 0 on the DT9816 module. Refer to page 34 for an example of how to connect a scope (a pulse output) to counter/timer 0.

**Note:** The Quick DataAcq application works only with counter/timer 0.

2. In the Quick DataAcq application, choose Pulse Generator from the Control menu.
3. Select the appropriate DT9816 module from the Board list box.
4. Select Continuous to output a continuous pulse stream.
5. Select High-to-low to output a falling-edge pulse (the low portion of the total pulse output period is the active portion of the signal).
6. Click Start to generate the pulse(s).
   *The application displays the results both in text and graphical form.*
7. Click Stop to stop a continuous pulse output operation.
Part 2: Using Your Module
Principles of Operation

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Digital I/O Features ................................................................. 54
Counter/Timer Features ............................................................. 55
Figure 11 shows a block diagram of the DT9816 module.

**Figure 11: Block Diagram of the DT9816 Module**
Analog Input Features

This section describes the following features of analog input (A/D) operations on the DT9816 module:

- Input resolution, described below
- Analog input channels, described below
- Input ranges and gains, described on page 50
- Input sample clock sources, described on page 50
- Analog input conversion modes, described on page 51
- Input triggers, described on page 52
- Data format and transfer, described on page 52
- Error conditions, described on page 53

Input Resolution

The resolution of the A/D subsystem on the DT9816 module is 16-bits. This resolution is fixed; it cannot be programmed in software.

Analog Input Channels

The DT9816 provides six single-ended simultaneous analog input channels. You can acquire data from a single analog input channel or from a group of analog input channels on the module.

Note: To maintain simultaneous operation, all analog input connections must have the same lead lengths.

The following subsections describe how to specify the channels.

Specifying a Single Analog Input Channel

The simplest way to acquire data from a single analog input channel is to specify the channel for a single-value analog input operation using software; refer to page 51 for more information about single-value operations.

You can also specify a single channel using the analog input channel list, described in the next section.
**Specifying One or More Analog Input Channels**

You can read data from one or more analog input channels using an analog input channel list. Group the channels in the list sequentially (starting either with 0 or with any other analog input channel) in ascending order. You cannot specify the same channel more than once in the list.

Using software, specify the channels you want to sample. You can enter up to 6 entries in the channel list for this module. Refer to page 51 for more information about the supported conversion modes.

**Input Ranges and Gains**

The DT9816 provides an input range of ±10 V. Use software to specify the range as ±10 V with a gain of 1, or ±10 V with a gain of 2 for an effective input range of ±5 V.

You can specify the gain in a single-value operation, or specify the gain for each entry in the channel list.

*Note:* This is the range for the entire analog input subsystem, not the range per channel.

**Input Sample Clock Sources**

You can pace an analog input operation on a DT9816 module using a software clock source. Using software, specify the clock source as internal, then specify the clock frequency at which to pace the operation. The supported frequencies depends on the model you selected, as shown in Table 3.

<table>
<thead>
<tr>
<th>Table 3: Supported Clock Frequency</th>
</tr>
</thead>
<tbody>
<tr>
<td>Model</td>
</tr>
<tr>
<td>---------</td>
</tr>
<tr>
<td>DT9816</td>
</tr>
<tr>
<td>DT9816-A</td>
</tr>
<tr>
<td>DT9816-S</td>
</tr>
</tbody>
</table>

According to sampling theory (Nyquist Theorem), specify a frequency that is at least twice as fast as the input’s highest frequency component. For example, to accurately sample a 2 kHz signal, specify a sampling frequency of at least 4 kHz. Doing so avoids an error condition called *aliasing*, in which high frequency input components erroneously appear as lower frequencies after sampling.
Analog Input Conversion Modes

The DT9816 supports the following conversion modes:

- Single-value operations
- Continuous scan operations

The following subsections describe the conversion modes in more detail.

**Single-Value Operations**

Single-value operations are the simplest to use. Using software, you specify the range, gain, and analog input channel. The module acquires the data from the specified channel and returns the data immediately. For a single-value operation, you cannot specify a clock source, trigger source, scan mode, or buffer.

Single-value operations stop automatically when finished; you cannot stop a single-value operation.

**Continuous Scan Mode**

Use continuous scan mode if you want to accurately control the period between successive simultaneous conversions of all channels in a channel list.

When it receives a software trigger, the module cycles through the channel list, acquiring and converting the data for each entry in the list (this process is defined as the scan). The module then wraps to the start of the channel list and repeats the process continuously until either all the allocated buffers on the subsystem queue are filled or until you stop the operation. Refer to page 52 for more information about buffers.

The conversion rate is determined by the frequency of the internal sample clock; refer to page 50 for more information about the internal sample clock. The sample rate, which is the rate at which a single entry in the channel list is sampled, is the same as the conversion rate due to the simultaneous nature of the module.

To select continuous scan mode, use software to specify the data flow as Continuous.
Figure 12 illustrates continuous scan mode using a channel list with three entries: channel 0, channel 1, and channel 2. In this example, analog input data is acquired simultaneously on all channels on each clock pulse of the input sample clock. Data is acquired continuously.

![Figure 12: Continuous Scan Mode](image)

**Input Triggers**

A trigger is an event that occurs based on a specified set of conditions. Acquisition starts when the module detects the initial trigger event and stops when either no more buffers are available or you stop the operation.

The DT9816 module supports the following trigger sources:

- **Software trigger** — A software trigger event occurs when you start the analog input operation (the computer issues a write to the module to begin conversions). Using software, specify the trigger source as a software trigger.

- **External digital (TTL) trigger** — The external trigger is initiated by a falling-edge transition on the A/D external TTL trigger input. Using software, specify the trigger source as an external, negative digital (TTL) trigger.

**Data Format and Transfer**

DT9816 modules use binary data encoding, where 0000 represents negative full-scale, and FFFFh represents positive full-scale. Use software to specify the data encoding as binary. The ADC outputs FFFFh for above-range signals, and 0000 for below-range signals.

Before you begin acquiring data, you must allocate buffers to hold the data. A buffer done event is returned whenever a buffer is filled. This allows you to move and/or process the data as needed.

We recommend that you allocate a minimum of two buffers for a continuous analog input operation. Data is written to multiple allocated input buffers continuously; when no more empty buffers are available, the operation stops. The data is gap-free.
Error Conditions

An overrun condition is reported if the A/D sample clock rate is too fast. This error is reported if a new A/D sample clock pulse occurs while the ADC is busy performing a conversion from the previous A/D sample clock pulse. The host computer can clear this error. To avoid this error, use a slower sampling rate or increase the buffer size and/or number of buffers.
Digital I/O Features

This section describes the following features of digital I/O operations on the DT9816:

- Digital I/O lines
- Resolution
- Operation modes

Digital I/O Lines

The DT9816 module includes 8 digital input and 8 digital output lines.

Using software, you can specify the digital I/O line that you want to read or write in a single-value digital I/O operation. Refer to page 54 for more information about single-value operations.

A digital line is high if its value is 1; a digital line is low if its value is 0. On power up or reset, a low value (0) is output from each of the digital output lines.

Resolution

The DT9816 provides 16 digital lines that are dedicated, 8 each, to the DIN and DOUT subsystems. By default, these lines are organized as two 8-bit ports. Because these are dedicated lines, you cannot combine the digital ports into one port to change resolution.

Operation Modes

The DT9816 supports single-value digital I/O operations only. For a single-value operation, use software to specify the digital I/O port (the gain is ignored). The DT9816 then reads data from or writes data to the digital lines associated with that port.

Single-value operations stop automatically when finished; you cannot stop a single-value operation.
Counter/Timer Features

This section describes the following features of counter/timer (C/T) operations on the DT9816 module:

- C/T channel, described below
- C/T clock sources, described on page 56
- Gate types, described on page 56
- Pulse types and duty cycles, described on page 56
- C/T operation modes, described on page 57

C/T Channel

The DT9816 module provides one 16-bit counter/timer. The counter accepts a clock input signal and gate input signal and outputs a pulse (pulse output signal), as shown in Figure 13.

![Figure 13: Counter/Timer Channel](image-url)
C/T Clock Sources

The following clock sources are available for the counter/timer:

- **Internal clock** – Through software, specify the clock source as internal, and specify the frequency at which to pace the counter/timer operation. The frequency of the internal C/T clock can range from 60 Hz to 6 MHz.

- **External clock** – An external clock is useful when you want to pace counter/timer operations at rates not available with the internal clock or if you want to pace at uneven intervals.

Connect an external clock with a maximum recommended frequency of 6 MHz to the Counter 0 In signal on the DT9816 module. Using software, specify the C/T clock source as external, and specify a clock divider between 2 and 65534 to determine the actual frequency at which to pace the counter/timer operation. For example, if you connect a 6 MHz external C/T clock and use a clock divider of 2, the resulting C/T output frequency is 3 MHz. Counter/timer operations start on the falling edge of the Counter 0 In signal.

Gate Types

The edge or level of the Counter 0 Gate signal determines when a counter/timer operation is enabled. Using software, you can specify one of the following gate types:

- **None** – A software command enables any counter/timer operation immediately after execution.

- **Logic-high level external gate input** – Enables a counter/timer operation when Counter 0 Gate is high, and disables a counter/timer operation when Counter 0 Gate is low. Note that this gate type is used for event counting and rate generation modes; refer to page 57 for more information about these modes.

Pulse Duty Cycles

Counter/timer output signals from the DT9816 module are high-to-low going signals. The low portion of the total pulse output period is the active portion of the counter/timer clock output signal.

The duty cycle (or pulse width) indicates the percentage of the total pulse output period that is active. In rate generation mode, the duty cycle is fixed at 50% for the DT9816 module. Figure 14 illustrates a high-to-low going output pulse with a duty cycle of 50%.
Counter/Timer Operation Modes

The DT9816 module supports the following counter/timer operation modes:

- Event counting
- Frequency measurement
- Rate generation

**Event Counting**

Use event counting mode if you want to count the number of falling edges that occur on Counter 0 In when the gate is active (high-level). Refer to page 56 for information about specifying the active gate type.

You can count a maximum of 65,536 events before the counter rolls over to 0 and starts counting again.

For event counting operations, use software to specify the counter/timer mode as count, the C/T clock source as external, and the active gate type as high-level.

Make sure that the signals are wired appropriately. Refer to page 32 for an example of connecting an event counting application.

**Frequency Measurement**

Connect a pulse of a known duration to the Counter 0 Gate signal. Specify the active gate in software (high level). When the operation starts, read the number of counts that occurred when the gate was active.

You can determine the frequency of the clock input signal using the following equation:

\[
\text{Frequency Measurement} = \frac{\text{Number of Events}}{\text{Measurement Period}}
\]
Make sure that the signals are wired appropriately. Refer to page 33 for an example of connecting a frequency measurement application.

**Rate Generation**

Use rate generation mode to generate a continuous pulse output signal from Counter 0 Out; this mode is sometimes referred to as continuous pulse output or pulse train output.

The pulse output operation is enabled whenever the Counter 0 Gate signal is active (high level or software gate). While the pulse output operation is enabled, the counter outputs a high-to-low going pulse with a pulse width of 50% continuously. As soon as the operation is disabled, rate generation stops.

The frequency of the output is determined by the C/T clock source (either internal or external) and, for an external clock source, the clock divider used. You can generate an output signal from Counter 0 Out with a frequency of 60 Hz to 6 MHz.

To specify rate generation mode, use software to specify the counter/timer mode as rate, the C/T clock source as either internal or external, the clock divider (2 to 65534) if external, and the active gate type (low-level, high-level, or software gate). Refer to page 56 for more information about gate types.

Make sure that the signals are wired appropriately. Refer to page 34 for an example of connecting a rate generation application.
Supported Device Driver Capabilities

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Ranges ..................................................................................................... 64
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Triggers .................................................................................................... 66
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The DT9816 Device Driver provides support for the analog input (A/D), digital input (DIN), digital output (DOUT), and counter/timer (C/T) subsystems. For information on how to configure the device driver, refer to page 25.

<table>
<thead>
<tr>
<th>Table 4: DT9816 Subsystems</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>DT9816</strong></td>
</tr>
<tr>
<td>Total Subsystems on Module</td>
</tr>
</tbody>
</table>

The tables in this chapter summarize the features available for use with the DT-Open Layers for .NET Class Library and the DT9816 modules. The DT-Open Layers for .NET Class Library provides properties that return support information for specified subsystem capabilities.

The first row in each table lists the subsystem types. The first column in each table lists all possible subsystem capabilities. A description of each capability is followed by the property used to describe that capability in the DT-Open Layers for .NET Class Library.

**Note:** Blank fields represent unsupported options.

For more information, refer to the description of these properties in the DT-Open Layers for .NET Class Library online help or DT-Open Layers for .NET Class Library User’s Manual.
# Data Flow and Operation Options

Table 5: DT9816 Data Flow and Operation Options

<table>
<thead>
<tr>
<th>DT9816</th>
<th>A/D</th>
<th>D/A</th>
<th>DIN</th>
<th>DOUT</th>
<th>C/T</th>
<th>QUAD</th>
</tr>
</thead>
<tbody>
<tr>
<td>Single-Value Operation Support</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Simultaneous Single-Value Output Operations</td>
<td>SupportsSingleValue</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Continuous Operation Support</td>
<td>Yes</td>
<td></td>
<td>Yes</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Continuous Operation until Trigger</td>
<td>SupportsContinuous</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Continuous Operation before &amp; after Trigger</td>
<td>SupportsContinuousPreTrigger</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Waveform Operations Using FIFO Only</td>
<td>SupportsWaveformModeOnly</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Simultaneous Start List Support</td>
<td>SupportsSimultaneousStart</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Supports Programmable Synchronization Modes</td>
<td>SupportsSynchronization</td>
<td></td>
<td></td>
<td></td>
<td></td>
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</tr>
<tr>
<td>Synchronization Modes</td>
<td>SynchronizationMode</td>
<td></td>
<td></td>
<td></td>
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</tr>
<tr>
<td>Interrupt Support</td>
<td>SupportsInterruptOnChange</td>
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<td></td>
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<tr>
<td>Output FIFO Size</td>
<td>FifoSize</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Auto-Calibrate Support</td>
<td>SupportsAutoCalibrate</td>
<td></td>
<td></td>
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</tr>
</tbody>
</table>
Buffering

Table 6: DT9816 Buffering Options

<table>
<thead>
<tr>
<th>DT9816</th>
<th>A/D</th>
<th>D/A</th>
<th>DIN</th>
<th>DOUT</th>
<th>C/T</th>
<th>QUAD</th>
</tr>
</thead>
<tbody>
<tr>
<td>Buffer Support</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
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</tr>
<tr>
<td>SupportsBuffering</td>
<td>Yes</td>
<td></td>
<td></td>
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<tr>
<td>Single Buffer Wrap Mode Support</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SupportsWrapSingle</td>
<td></td>
<td></td>
<td></td>
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</tr>
<tr>
<td>Inprocess Buffer Flush Support</td>
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<td></td>
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</tr>
<tr>
<td>SupportsInProcessFlush</td>
<td>Yes</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Triggered Scan Mode

Table 7: DT9816 Triggered Scan Mode Options

<table>
<thead>
<tr>
<th>DT9816</th>
<th>A/D</th>
<th>D/A</th>
<th>DIN</th>
<th>DOUT</th>
<th>C/T</th>
<th>QUAD</th>
</tr>
</thead>
<tbody>
<tr>
<td>Triggered Scan Support</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
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</tr>
<tr>
<td>SupportsTriggeredScan</td>
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</tr>
<tr>
<td>Maximum Number of CGL Scans per Trigger</td>
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</tr>
<tr>
<td>MaxMultiScanCount</td>
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<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>Maximum Retrigger Frequency</td>
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<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MaxRetriggerFreq</td>
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<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>Minimum Retrigger Frequency</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MinRetriggerFreq</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
</tbody>
</table>

Data Encoding

Table 8: DT9816 Data Encoding Options

<table>
<thead>
<tr>
<th>DT9816</th>
<th>A/D</th>
<th>D/A</th>
<th>DIN</th>
<th>DOUT</th>
<th>C/T</th>
<th>QUAD</th>
</tr>
</thead>
<tbody>
<tr>
<td>Binary Encoding Support</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SupportsBinaryEncoding</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Twos Complement Support</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SupportsTwosCompEncoding</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Returns Floating-Point Values</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ReturnsFloats</td>
<td></td>
<td></td>
<td></td>
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<td></td>
</tr>
</tbody>
</table>
### Channels

**Table 9: DT9816 Channel Options**

<table>
<thead>
<tr>
<th></th>
<th>DT9816</th>
<th>A/D</th>
<th>D/A</th>
<th>DIN</th>
<th>DOUT</th>
<th>C/T</th>
<th>QUAD</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of Channels</td>
<td>6&lt;sup&gt;a&lt;/sup&gt;</td>
<td></td>
<td></td>
<td>1</td>
<td>1</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>SE Support</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SupportsSingleEnded</td>
<td>Yes</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SE Channels</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MaxSingleEndedChannels</td>
<td>6</td>
<td></td>
<td>0</td>
<td>0</td>
<td>0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>DI Support</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SupportsDifferential</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
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<td>Channel-List Inhibit</td>
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<td>SupportsChannelListInhibit</td>
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</table>

a. The DT9816 provides analog input channels 0 to 5.

b. You cannot specify the same channel more than once in the list. Place channels in your channel list in ascending order. All channels are sampled simultaneously with data returned in ascending channel order; if your channel does not match, you will have unexpected results.

### Gain

**Table 10: DT9816 Gain Options**

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Ranges

Table 11: DT9816 Range Options

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Resolution

Table 12: DT9816 Resolution Options

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Thermocouple and RTD Support

Table 13: DT9816 Thermocouple and RTD Support Options

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<th>Thermocouple Support</th>
<th>RTD Support</th>
<th>Resistance Support</th>
<th>Voltage Converted to Temperature in Hardware</th>
<th>Supported Thermocouple Types</th>
<th>Supported RTD Types</th>
<th>Supports CJC Source Internally in Hardware</th>
<th>Supports CJC Channel</th>
<th>Available CJC Channels</th>
<th>Supports Interleaved CJC Values in Data Stream</th>
<th>Supports Programmable Filters</th>
<th>Programmable Filter Types</th>
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<td>RTDType</td>
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<td>SupportsCjcSourceChannel</td>
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<td>SupportsTemperatureFilters</td>
<td>TemperatureFilterType</td>
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IEPE Support

Table 14: DT9816 IEPE Support Options

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<th>Software Programmable AC Coupling</th>
<th>Software Programmable DC Coupling</th>
<th>Software Programmable External Excitation Current Source</th>
<th>Software Programmable Internal Excitation Current Source</th>
<th>Available Excitation Current Source Values</th>
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<td>SupportsDCCoupling</td>
<td>SupportsExternalExcitationCurrentSrc</td>
<td>SupportsInternalExcitationCurrentSrc</td>
<td>SupportedExcitationCurrentValues</td>
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Triggers

Table 15: DT9816 Trigger Options

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<td>External Positive TTL Trigger Support</td>
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<td>SupportsPosExternalTTLTrigger</td>
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<tr>
<td>External Negative TTL Trigger Support</td>
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<td>External Positive TTL Trigger Support for Single-Value Operations</td>
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Clocks

Table 16: DT9816 Clock Options

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<tr>
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<td>SupportsInternalClock</td>
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<tr>
<td>External Clock Support</td>
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<td>Minimum Frequency</td>
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<tr>
<td>MinFrequency</td>
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<td>0</td>
<td>60 Hz</td>
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a. The module supports only even number values for the clock divider. Odd values are rounded down.
b. The DT9816 supports an actual maximum of 50 kHz, the DT9816-A supports an actual maximum of 153.846 kHz, and the DT9816-S supports an actual maximum of 750 kHz.
c. For the DT9816 and DT9816-A, the actual minimum is 61 Hz. For the DT9816-S, the minimum frequency is 183.1 Hz.
**Counter/Timers**

Table 17: DT9816 Counter/Timer Options

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<td>SupportsCount</td>
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<td>Generate Rate Mode Support</td>
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<td>SupportsRateGenerate</td>
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Table 17: DT9816 Counter/Timer Options (cont.)

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<td>Interrupt-Driven Operations</td>
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<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

a. The pulse width (duty cycle) if fixed at 50% when rate generation mode is used.
**General Checklist**

Should you experience problems using a DT9816 module, do the following:

1. Read all the documentation provided for your product. Make sure that you have added any “Read This First” information to your manual and that you have used this information.

2. Check the OMNI CD for any README files and ensure that you have used the latest installation and configuration information available.

3. Check that your system meets the requirements stated in the README file on the OMNI CD.

4. Check that you have installed your hardware properly using the instructions in Chapter 2.

5. Check that you have configured the device driver properly using the instructions in Chapter 2.

6. Check that you have wired your signals properly using the instructions in Chapter 3.

7. Search the DT Knowledgebase in the Support section of the Data Translation web site (at www.datatranslation.com) for an answer to your problem.

8. Visit the product’s page on the Data Translation web site for the latest tips, white papers, product documentation, and software fixes.

If you still experience problems, try using the information in Table 18 to isolate and solve the problem. If you cannot identify the problem, refer to page 70.

---

**Table 18: Troubleshooting Problems**

<table>
<thead>
<tr>
<th>Symptom</th>
<th>Possible Cause</th>
<th>Possible Solution</th>
</tr>
</thead>
<tbody>
<tr>
<td>Module is not recognized</td>
<td>You plugged the module into your computer before installing the device driver.</td>
<td>From the Control Panel &gt; System &gt; Hardware &gt; Device Manager, uninstall any unknown devices (showing a yellow question mark). Then, run the setup program on your OMNI CD to install the USB device drivers, and reconnect your USB module to the computer.</td>
</tr>
<tr>
<td>Module does not respond.</td>
<td>The module configuration is incorrect.</td>
<td>Check the configuration of your device driver; see the instructions in Chapter 2.</td>
</tr>
<tr>
<td></td>
<td>The module is damaged.</td>
<td>Contact Data Translation for technical support; refer to page 72.</td>
</tr>
<tr>
<td>Intermittent operation.</td>
<td>Loose connections or vibrations exist.</td>
<td>Check your wiring and tighten any loose connections or cushion vibration sources; see the instructions in Chapter 3.</td>
</tr>
<tr>
<td></td>
<td>The module is overheating.</td>
<td>Check environmental and ambient temperature; consult the module’s specifications on page 80 of this manual and the documentation provided by your computer manufacturer for more information.</td>
</tr>
<tr>
<td></td>
<td>Electrical noise exists.</td>
<td>Check your wiring and either provide better shielding or reroute unshielded wiring; see the instructions in Chapter 3.</td>
</tr>
</tbody>
</table>
### Table 18: Troubleshooting Problems (cont.)

<table>
<thead>
<tr>
<th>Symptom</th>
<th>Possible Cause</th>
<th>Possible Solution</th>
</tr>
</thead>
<tbody>
<tr>
<td>Device failure error reported.</td>
<td>The module cannot communicate with the Microsoft bus driver or a problem with the bus driver exists.</td>
<td>Check your cabling and wiring and tighten any loose connections; see the instructions in Chapter 3.</td>
</tr>
<tr>
<td></td>
<td>The module was removed while an operation was being performed.</td>
<td>Ensure that your module is properly connected; see the instructions in Chapter 2.</td>
</tr>
<tr>
<td>Data appears to be invalid.</td>
<td>An open connection exists.</td>
<td>Check your wiring and fix any open connections; see the instructions in Chapter 3.</td>
</tr>
<tr>
<td></td>
<td>A transducer is not connected to the channel being read.</td>
<td>Check the transducer connections; see the instructions in Chapter 3.</td>
</tr>
<tr>
<td></td>
<td>The transducer is set up for differential inputs.</td>
<td>Check your wiring and ensure that your transducer is set up for single-ended inputs; see the instructions in Chapter 3.</td>
</tr>
<tr>
<td></td>
<td>The DT9816 module is out of calibration.</td>
<td>The DT9816 module is calibrated at the factory and should not require recalibration. If you want to readjust the calibration of the analog input or analog output circuitry, refer to the instructions on the Data Translation web site (<a href="http://www.datatranslation.com">www.datatranslation.com</a>).</td>
</tr>
<tr>
<td>Computer does not boot.</td>
<td>The power supply of the computer is too small to handle all the system resources.</td>
<td>Check the power requirements of your system resources and, if needed, get a larger power supply; consult the module’s specifications on page 80 of this manual.</td>
</tr>
<tr>
<td>USB 2.0 is not recognized.</td>
<td>Your operating system does not have the appropriate Service Pack installed.</td>
<td>Ensure that you load the appropriate Windows Service Pack (version 2 for Windows XP). If you are unsure of whether you are using USB 2.0 or USB 1.1, run the Open Layers Control Panel applet, described in Chapter 2.</td>
</tr>
<tr>
<td></td>
<td>Standby mode is enabled on your PC.</td>
<td>For some PCs, you may need to disable standby mode on your system for proper USB 2.0 operation. Consult Microsoft for more information.</td>
</tr>
</tbody>
</table>
Technical Support

If you have difficulty using a DT9816 module, Data Translation’s Technical Support Department is available to provide technical assistance.

To request technical support, go to our web site at http://www.datatranslation.com and click on the Support link.

When requesting technical support, be prepared to provide the following information:

- Your product serial number
- The hardware/software product you need help on
- The version of the OMNI CD you are using
- Your contract number, if applicable

If you are located outside the USA, contact your local distributor; see our web site (www.datatranslation.com) for the name and telephone number of your nearest distributor.
If Your Module Needs Factory Service

If your module must be returned to Data Translation, do the following:

1. Record the module’s serial number, then contact the Customer Service Department at (508) 481-3700, ext. 1323 (if you are in the USA) and obtain a Return Material Authorization (RMA).

   If you are located outside the USA, call your local distributor for authorization and shipping instructions. The name and telephone number of your nearest distributor are listed on Data Translation’s web site. All return shipments to Data Translation must be marked with the correct RMA number to ensure proper processing.

2. Using the original packing materials, if available, package the module as follows:

   - Wrap the module in an electrically conductive plastic material. Handle with ground protection. A static discharge can destroy components on the module.
   - Place in a secure shipping container.

3. Return the module to the following address, making sure the RMA number is visible on the outside of the box.

   Customer Service Dept.
   Data Translation, Inc.
   100 Locke Drive
   Marlboro, MA 01752-1192
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Digital I/O Specifications ............................................................ 78
Counter/Timer Specifications ......................................................... 79
Power, Physical, and Environmental Specifications ....................... 80
Regulatory Specifications ............................................................ 81
**Analog Input Specifications**

Table 19 lists the specifications for the A/D subsystem on the DT9816 module.

<table>
<thead>
<tr>
<th>Feature</th>
<th>DT9816 Specifications</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of analog input channels</td>
<td>6 single-ended</td>
</tr>
<tr>
<td>Number of gains</td>
<td>2 (1, 2)</td>
</tr>
<tr>
<td>Resolution</td>
<td>16-bit</td>
</tr>
<tr>
<td>Data encoding</td>
<td>offset binary</td>
</tr>
<tr>
<td>System accuracy, to % of FSR (Averaged over 50 readings)</td>
<td>±0.08% typical @ gain of 1</td>
</tr>
<tr>
<td>Range</td>
<td>±5 V, ±10 V</td>
</tr>
<tr>
<td>Nonlinearity</td>
<td>0.015%</td>
</tr>
<tr>
<td>Differential nonlinearity</td>
<td>0.003%</td>
</tr>
<tr>
<td>Inherent quantizing error</td>
<td>±1/2 LSB</td>
</tr>
<tr>
<td>Drift</td>
<td></td>
</tr>
<tr>
<td>Zero:</td>
<td>±25 μV/°C</td>
</tr>
<tr>
<td>Gain:</td>
<td>±50 ppm/°C</td>
</tr>
<tr>
<td>Differential linearity:</td>
<td>monotonic to 14 bits</td>
</tr>
<tr>
<td>Input impedance&lt;sup&gt;a&lt;/sup&gt;</td>
<td></td>
</tr>
<tr>
<td>Off channel:</td>
<td>—</td>
</tr>
<tr>
<td>On channel:</td>
<td>10 MΩ, 10 pf</td>
</tr>
<tr>
<td>Input bias current</td>
<td>±10 nA</td>
</tr>
<tr>
<td>Maximum input voltage (without damage)</td>
<td></td>
</tr>
<tr>
<td>Power on:</td>
<td>±35 V</td>
</tr>
<tr>
<td>Power off:</td>
<td>±20 V</td>
</tr>
<tr>
<td>A/D conversion time</td>
<td></td>
</tr>
<tr>
<td>DT9816:</td>
<td>8 μs</td>
</tr>
<tr>
<td>DT9816-A:</td>
<td>4 μs</td>
</tr>
<tr>
<td>DT9816-S:</td>
<td>950 ns</td>
</tr>
<tr>
<td>Channel acquisition time (±1/2 LSB)</td>
<td>1 μs</td>
</tr>
<tr>
<td>Sample-and-hold</td>
<td></td>
</tr>
<tr>
<td>Aperture uncertainty:</td>
<td>1 ns</td>
</tr>
<tr>
<td>Aperture delay:</td>
<td>35 ns</td>
</tr>
<tr>
<td>Aperture match:</td>
<td>5 ns</td>
</tr>
<tr>
<td>Gain match:</td>
<td>0.05%</td>
</tr>
<tr>
<td>Zero match:</td>
<td>±3.0 mV</td>
</tr>
<tr>
<td>Maximum throughput</td>
<td></td>
</tr>
<tr>
<td>DT9816:</td>
<td>50 kHz per channel</td>
</tr>
<tr>
<td>DT9816-A:</td>
<td>150 kHz per channel</td>
</tr>
<tr>
<td>DT9816-S:</td>
<td>750 kHz per channel</td>
</tr>
</tbody>
</table>
### Table 19: A/D Subsystem Specifications (cont.)

<table>
<thead>
<tr>
<th>Feature</th>
<th>DT9816 Specifications</th>
</tr>
</thead>
<tbody>
<tr>
<td>Signal bandwidth (to –3 dB point)</td>
<td></td>
</tr>
<tr>
<td>DT9816:</td>
<td>4 MHz typical</td>
</tr>
<tr>
<td>DT9816-A:</td>
<td>4 MHz typical</td>
</tr>
<tr>
<td>DT9816-S:</td>
<td>40 MHz typical</td>
</tr>
<tr>
<td>ESD protection (per spec)</td>
<td></td>
</tr>
<tr>
<td>Arc:</td>
<td>8 kV</td>
</tr>
<tr>
<td>Contact:</td>
<td>4 kV</td>
</tr>
<tr>
<td>Reference</td>
<td>2.5 V</td>
</tr>
<tr>
<td>Monotonicity</td>
<td>Yes</td>
</tr>
</tbody>
</table>

a. Very high input impedance minimizes any source error. When using high sampling rates on the DT9816-S, it is recommended that you use an input impedance of 100 Ω or less.
Digital I/O Specifications

Table 20 lists the specifications for the digital input (DIN) and digital output (DOUT) subsystems on the DT9816 module.

<table>
<thead>
<tr>
<th>Feature</th>
<th>DT9816 Specifications</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of digital I/O lines</td>
<td>16 (8 each; dedicated)</td>
</tr>
<tr>
<td>Number of ports</td>
<td>2, 8-bit</td>
</tr>
<tr>
<td>Input termination</td>
<td>Series 1 kΩ Series 33.2 Ω</td>
</tr>
<tr>
<td>Logic family</td>
<td>TTL</td>
</tr>
<tr>
<td>Logic sense</td>
<td>Positive true</td>
</tr>
<tr>
<td>Inputs</td>
<td></td>
</tr>
<tr>
<td>Input type: Level sensitive</td>
<td></td>
</tr>
<tr>
<td>Input logic load: 1 TTL Load</td>
<td></td>
</tr>
<tr>
<td>High input voltage: 2.4 V min</td>
<td></td>
</tr>
<tr>
<td>Low input voltage: 0.8 V max</td>
<td></td>
</tr>
<tr>
<td>Low input current: -0.4 mA max</td>
<td></td>
</tr>
<tr>
<td>Outputs</td>
<td></td>
</tr>
<tr>
<td>High output: 2.8 V min</td>
<td></td>
</tr>
<tr>
<td>Low output: 0.6 V max</td>
<td></td>
</tr>
<tr>
<td>High output current (source): 4.5 mA</td>
<td></td>
</tr>
<tr>
<td>Low output current (sink): 10 mA</td>
<td></td>
</tr>
<tr>
<td>Software I/O selectable</td>
<td>Yes</td>
</tr>
<tr>
<td>ESD protection (per spec)</td>
<td></td>
</tr>
<tr>
<td>Arc: 8 kV</td>
<td></td>
</tr>
<tr>
<td>Contact: 4 kV</td>
<td></td>
</tr>
</tbody>
</table>
Counter/Timer Specifications

Table 21 lists the specifications for the C/T subsystem on the DT9816 module.

Table 21: C/T Subsystem Specifications

<table>
<thead>
<tr>
<th>Feature</th>
<th>Specifications</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of counter/timers</td>
<td>1</td>
</tr>
<tr>
<td>Counter/timer modes</td>
<td>Event counting, frequency measurement, rate generation</td>
</tr>
<tr>
<td>Resolution</td>
<td>16-bit</td>
</tr>
<tr>
<td>Minimum pulse width:</td>
<td>25 ns</td>
</tr>
<tr>
<td>(minimum amount of time it takes a C/T to recognize an input pulse)</td>
<td></td>
</tr>
<tr>
<td>Logic family</td>
<td>TTL</td>
</tr>
<tr>
<td>Inputs</td>
<td>Level sensitive</td>
</tr>
<tr>
<td>Input logic load:</td>
<td>1 TTL Load</td>
</tr>
<tr>
<td>High input voltage:</td>
<td>2.4 V min</td>
</tr>
<tr>
<td>Low input voltage:</td>
<td>0.8 V max</td>
</tr>
<tr>
<td>Low input current:</td>
<td>-0.4 mA max</td>
</tr>
<tr>
<td>Outputs</td>
<td>2.8 V min</td>
</tr>
<tr>
<td>High output:</td>
<td>0.6 V max</td>
</tr>
<tr>
<td>Low output:</td>
<td>2 mA</td>
</tr>
<tr>
<td>High output current (source):</td>
<td>12 mA</td>
</tr>
<tr>
<td>Low output current (sink):</td>
<td></td>
</tr>
<tr>
<td>ESD protection (per spec)</td>
<td>8 kV</td>
</tr>
<tr>
<td>Arc:</td>
<td>4 kV</td>
</tr>
<tr>
<td>Contact:</td>
<td></td>
</tr>
<tr>
<td>Internal clock frequency</td>
<td>60 Hz to 6 MHz</td>
</tr>
<tr>
<td>External clock divider</td>
<td>2 to 65534</td>
</tr>
</tbody>
</table>
**Power, Physical, and Environmental Specifications**

Table 22 lists the power, physical, and environmental specifications for the DT9816 module.

### Table 22: Power, Physical, and Environmental Specifications

<table>
<thead>
<tr>
<th>Feature</th>
<th>DT9816 Specifications</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power, +5 V Enumeration Operation</td>
<td>&lt; 100 mA</td>
</tr>
<tr>
<td></td>
<td>&lt; 250 mA</td>
</tr>
<tr>
<td>Physical</td>
<td></td>
</tr>
<tr>
<td>Dimensions (board):</td>
<td>100 mm (L) x 100 mm (W) x 15.5 mm (H)</td>
</tr>
<tr>
<td>Dimensions (box with screw terminals and feet):</td>
<td>107.7 mm (L) x 100 mm (W) x 33.5 mm (H)</td>
</tr>
<tr>
<td>Weight (board):</td>
<td>65.8 g</td>
</tr>
<tr>
<td>Weight (box with screw terminals and feet):</td>
<td>138.8 g</td>
</tr>
<tr>
<td>Environmental</td>
<td></td>
</tr>
<tr>
<td>Operating temperature range:</td>
<td>0 to 55° C</td>
</tr>
<tr>
<td>Storage temperature range:</td>
<td>-40 to 85° C</td>
</tr>
<tr>
<td>Relative humidity:</td>
<td>to 95% non-condensing</td>
</tr>
</tbody>
</table>
Regulatory Specifications

Table 23 lists the regulatory specifications for the DT9816 module.

Table 23: Regulatory Specifications

<table>
<thead>
<tr>
<th>Feature</th>
<th>Specifications</th>
</tr>
</thead>
<tbody>
<tr>
<td>Immunity</td>
<td>EN61000-6-1:2001</td>
</tr>
<tr>
<td>RoHS (EU Directive 2002/95/EG)</td>
<td>Compliant (as of July 1st, 2006)</td>
</tr>
</tbody>
</table>
Screw Terminal Assignments
Table 24 lists the screw terminal assignments for the DT9816 module.

### Table 24: DT9816 Screw Terminal Assignments

<table>
<thead>
<tr>
<th>Screw Terminal</th>
<th>Signal</th>
<th>Screw Terminal</th>
<th>Signal</th>
</tr>
</thead>
<tbody>
<tr>
<td>20</td>
<td>USB +5 V Out</td>
<td>40</td>
<td>Ext Trigger</td>
</tr>
<tr>
<td>19</td>
<td>Ground</td>
<td>39</td>
<td>Ext Clock</td>
</tr>
<tr>
<td>18</td>
<td>Counter 0 In</td>
<td>38</td>
<td>Ground</td>
</tr>
<tr>
<td>17</td>
<td>Counter 0 Out</td>
<td>37</td>
<td>Digital Output 7</td>
</tr>
<tr>
<td>16</td>
<td>Counter 0 Gate</td>
<td>36</td>
<td>Digital Output 6</td>
</tr>
<tr>
<td>15</td>
<td>Ground</td>
<td>35</td>
<td>Digital Output 5</td>
</tr>
<tr>
<td>14</td>
<td>Reserved</td>
<td>34</td>
<td>Digital Output 4</td>
</tr>
<tr>
<td>13</td>
<td>Reserved</td>
<td>33</td>
<td>Digital Output 3</td>
</tr>
<tr>
<td>12</td>
<td>Reserved</td>
<td>32</td>
<td>Digital Output 2</td>
</tr>
<tr>
<td>11</td>
<td>Reserved</td>
<td>31</td>
<td>Digital Output 1</td>
</tr>
<tr>
<td>10</td>
<td>2.5 V Reference</td>
<td>30</td>
<td>Digital Output 0</td>
</tr>
<tr>
<td>9</td>
<td>Analog Ground</td>
<td>29</td>
<td>Ground</td>
</tr>
<tr>
<td>8</td>
<td>Reserved</td>
<td>28</td>
<td>Digital Input 7</td>
</tr>
<tr>
<td>7</td>
<td>Reserved</td>
<td>27</td>
<td>Digital Input 6</td>
</tr>
<tr>
<td>6</td>
<td>Analog Input CH5</td>
<td>26</td>
<td>Digital Input 5</td>
</tr>
<tr>
<td>5</td>
<td>Analog Input CH4</td>
<td>25</td>
<td>Digital Input 4</td>
</tr>
<tr>
<td>4</td>
<td>Analog Input CH3</td>
<td>24</td>
<td>Digital Input 3</td>
</tr>
<tr>
<td>3</td>
<td>Analog Input CH2</td>
<td>23</td>
<td>Digital Input 2</td>
</tr>
<tr>
<td>2</td>
<td>Analog Input CH1</td>
<td>22</td>
<td>Digital Input 1</td>
</tr>
<tr>
<td>1</td>
<td>Analog Input CH0</td>
<td>21</td>
<td>Digital Input 0</td>
</tr>
</tbody>
</table>
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