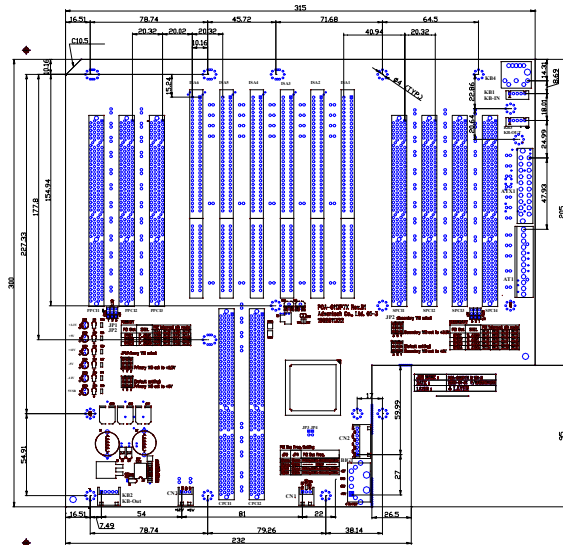


# PCA-6113P7X

Description: 13 slot backplane 1 CPU/ 7 PCI/ 5 ISA Rev. B01-3

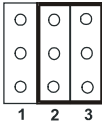


## 1. Connectors

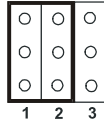
Connector	Description	CN1		AT1		KB-1/KB-2/KB-3	
ISA1~3, 6	16 Bit ISA Bus connector	PIN	Name	PIN	Name	PIN	Name
PPCI1 ~ PPCB	64 Bit PCI Bus connector (Primary)	1	PS-ON	1	NC	1	KBCLK
SPCI1 ~ SPC4	64 Bit PCI Bus connector (Secondary)	2	GND	2	+5V	2	KBDATA
ISA4 ~ ISA5	PICMG connector	3	+5V	3	+12V	3	NC
KB1	KB-In, from CPU card K/B connector	CN2		4	-12V	4	KBGND
KB2~KB3	KB-Out, 5 pin external K/B connector	PIN	Name	5	GND	5	KBVCC
KB4	KB-Out, 5 pin AT external K/B connector	1	5VSB	6	GND	ATX1	
BIG1	Big 4 Pin Power connector	2	GND	7	GND	1	+3.3V
AT1	AT Power connector	3	GND	8	GND	2	+3.3V
ATX1	ATX Power connector	4	-5V	9	GND	3	GND
CN1	PS-ON Function, to CPU card for ATX power signal, 3 pin connector	5	+5V	10	-5V	4	+5V
CN2	8 pin Alarm Board Power connector	6	+3.3V	11	+5V	5	GND
CN3	3 pin +5V and +12V Power connector	7	-12V	12	+5V	6	+5V
JP1	Primary VIO Select (select 3.3V/5V)	8	+12V	BIG1		7	GND
JP2	Secondary VIO Select (select 3.3V/5V)	CN3		PIN	Name	8	NC
JP3	Primary PCI Bus frequency	PIN	Name	1	+12V	9	5VSB
JP4	Secondary PCI Bus frequency	1	+12V	2	GND	10	+12V
		2	GND	3	GND	11	+3.3V
		3	+5V	4	+5V	12	-12V
						13	GND
						14	PS-ON
						15	GND
						16	GND
						17	GND
						18	-5V
						19	+5V
						20	+5V

## 2. Jumper Setting

- JP1: Primary VIO Select

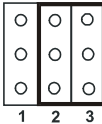


Primary VIO set to +5V

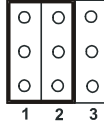


Primary VIO set to +3.3V

- JP2: Secondary Select



Secondary VIO set to +3.3V



Secondary VIO set to +5V

- JP3/JP4

PCI Bus Frequency Setting		
JP3	JP4	PCI Bus Frequency
Close	Close	Primary: 33MHz, Secondary: 33MHz
Close	Open	ERROR
Open	Close	Primary: 66MHz, Secondary: 33MHz
Open	Open	Primary: 66MHz, Secondary: 66MHz

## 3. PCI Routing table

Primary PCI Group					
PCI Slot	IDSEL	PCI interrupt Pin Route			
		INTA	INTB	INTC	INTD
PPC1	P_AD31	INTB	INTC	INTD	INTA
PPC2	P_AD30	INTC	INTD	INTA	INTB
PPC3	P_AD29	INTD	INTA	INTB	INTC

Secondary PCI Group					
PCI Slot	IDSEL	PCI interrupt Pin Route			
		INTA	INTB	INTC	INTD
SPC1	P_AD20	INTA	INTB	INTC	INTD
SPC2	P_AD21	INTB	INTC	INTD	INTA
SPC3	P_AD22	INTC	INTD	INTA	INTB
SPC4	P_AD23	INTD	INTA	INTB	INTC

#### **4. 64bit/66MHz add-on card**

A. The PCA-6113P7X backplane provides high performance PCI slots running at 64 bit and 66MHz, and can also operate under various speeds of 32bit/33MHz, 32bit/66MHz, 64bit/33MHz.

B. Once a 33 MHz card is installed in the bus group, the PCI bus speed will decelerate to 33 MHz. Depending on the speed requirement, it is highly recommended to allocate different add-on cards to different PCI groups. In order to obtain the best performance, please use 66MHz add-on cards in primary PCI group, and 33MHz add-on cards in secondary PCI group .

C. Shall any incompatibility occur when using add-on cards running at 66MHz PCI bus in PCA-6113P7X backplane, please adjust the PCI bus frequency to 33MHz. The PCI bus frequency can be adjusted with JP3 and JP4. Correct jumper setting is available in this manual.

#### **5. Detachable board**

Depending on the chassis usage, users may need to remove the detachable board along the dotted lines.