

ARK-3380

Embedded Box Computer

User Manual

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Product Warranty (2 year)

Advantech warrants to you, the original purchaser, that each of its products will be free from defects in materials and workmanship for two years from the date of purchase.

This warranty does not apply to any products which have been repaired or altered by persons other than repair personnel authorized by Advantech, or which have been subject to misuse, abuse, accident or improper installation. Advantech assumes no liability under the terms of this warranty as a consequence of such events.

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3. If your product is diagnosed as defective, obtain an RMA (return merchandize authorization) number from your dealer. This allows us to process your return more quickly.
4. Carefully pack the defective product, a fully-completed Repair and Replacement Order Card and a photocopy proof of purchase date (such as your sales receipt) in a shippable container. A product returned without proof of the purchase date is not eligible for warranty service.
5. Write the RMA number visibly on the outside of the package and ship it prepaid to your dealer.

Declaration of Conformity

CE

This product has passed the CE test for environmental specifications when shielded cables are used for external wiring. We recommend the use of shielded cables. This kind of cable is available from Advantech. Please contact your local supplier for ordering information.

FCC Class A

Note: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

Technical Support and Assistance

1. Visit the Advantech web site at **www.advantech.com/support** where you can find the latest information about the product.
2. Contact your distributor, sales representative, or Advantech's customer service center for technical support if you need additional assistance. Please have the following information ready before you call:
 - Product name and serial number
 - Description of your peripheral attachments
 - Description of your software (operating system, version, application software, etc.)
 - A complete description of the problem
 - The exact wording of any error messages

ARK-3380 Series Model

There are three sub-models in ARK-3380 series listed below:

| | |
|------------------|---|
| ARK-3380-1S0B1E: | ULV Celeron® M 1 GHz Embedded Box Computer, with DVI, LVDS, Audio, Fast Ethernet, S-Video, C-Video, 1 x RS-232, 1 x RS-232/422/485, 3 x USB2.0, 1 x 8 Bits DIO |
| ARK-3380-1S4B1E: | LV Pentium® M 1.4GHz, Embedded Box Computer, with DVI, LVDS, Audio, Fast Ethernet, S-Video, C-Video, 1 x RS-232, 1 x RS-232/422/485, 3 x USB2.0, 1 x 8 Bits DIO |

Table 1.1: ARK-3380 Model list

Packing list

Before installing your board, make sure that the following materials have been received:

- Warranty card
- 1 x ARK-3380 Unit
- 1 x DIN-Rail Mounting Kit (P/N:1997001110, 1997001120,1997001130,1997001140)
- 1 x PS2 Keyboard/Mouse Cable (P/N: 1700060202)
- 1 x Utility CD
- 1 x Flat Cable support RS-485/RS-422 mode for COM2 serial port (P/N: 1700001967)
- 1 x 2-P Phoenix to DC-Jack power cable (P/N: 1700001394)

If any of these items are missing or damaged, contact your distributor or sales representative immediately.

Safety Instructions

1. Please read these safety instructions carefully.
2. Please keep this User's Manual for later reference.
3. Please disconnect this equipment from AC outlet before cleaning. Don't use liquid or sprayed detergent for cleaning. Use moisture sheet or clothe for cleaning.
4. For pluggable equipment, the socket-outlet shall near the equipment and shall be easily accessible.
5. Please keep this equipment from humidity.
6. Lay this equipment on a reliable surface when install. A drop or fall could cause injury.
7. Do not leave this equipment in an uncontrolled environment; storage temperatures above 60°C may damage the equipment.
8. The openings on the enclosure are for air convection hence protecting the equipment from overheating. **DO NOT COVER THE OPENINGS.**
9. Make sure the voltage of the power source when connecting the equipment to the power outlet.
10. Place the power cord such a way that people cannot step on it. Do not place anything over the power cord. The power cord must be rated for the product and for the voltage and current marked on the product's electrical ratings label. The voltage and current rating of the cord should be greater than the voltage and current rating marked on the product.
11. All cautions and warnings on the equipment should be noted.
12. If the equipment is not used for long time, disconnect the equipment from mains to avoid being damaged by transient over-voltage.
13. Never pour any liquid into ventilation openings; this could cause fire or electrical shock.
14. Never open the equipment. For safety reasons, only qualified service personnel should open the equipment.
15. If one of the following situations arise, get the equipment checked by service personnel:
 - a. The Power cord or plug is damaged.
 - b. Liquid has penetrated the equipment.
 - c. The equipment has been exposed to moisture.

- d. The equipment has not worked well or you can not get it work according to user's manual.
- e. The equipment has been dropped and damaged.
- f. The equipment has obvious signs of breakage

CAUTION!

THIS COMPUTER IS PROVIDED WITH A BATTERY-POWERED REAL-TIME CLOCK CIRCUIT. THERE IS A DANGER OF EXPLOSION IF BATTERY IS INCORRECTLY REPLACED. REPLACE ONLY WITH SAME OR EQUIVLENT TYPE RECOMMENDED BY THE MANUFACTURE. DISCARD USED BATTERIES ACCORDING TO THE MANUFACTURER'S INSTRUCTIONS.

Safety Precaution - Static Electricity

Follow these simple precautions to protect yourself from harm and the products from damage.

1. To avoid electrical shock, always disconnect the power from your PC chassis before you work on it. Don't touch any components on the CPU card or other cards while the PC is on.
2. Disconnect power before making any configuration changes. The sudden rush of power as you connect a jumper or install a card may damage sensitive electronic components.

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Overview

This chapter gives background information on the ARK-3380. It shows you the ark-3380 overview and specifications.

Sections include:

- Sections include:
- Introduction
- Hardware Specifications
- Chassis Dimension

Chapter 1 Overview

1.1 Introduction

The ARK-3380 Fan-less Embedded Box Computer combines a rich display interface and other industrial features into a rugged, compact metal chassis for multimedia intensive applications. The fan-less operation provides noise protection to the platform when deployed in external environments. The ARK-3380 Embedded Box Computer is ideally suited for embedded PC applications. All electronics are protected in a compact sealed housing for convenient embedded and stand alone applications, where space and environment considerations are critical.

1.2 Features

1.2.1 Dual Display, Video Intensive capacity

- Rich display interface of DVI, LVDS and TVout; as well as AC97 Stereo audio designed for diversified applications such as Point of Information, Gaming, Digital display broadcasting system

1.2.2 Highly Robust Casting Construction

- Fan-less operation in Aluminum sealed construction
- A special cushioned design that absorbs vibration to ensure maximum reliability under harsh conditions

1.2.3 Highly Compact Size

- With its maximum mounting height of 69 mm, the ARK-3380 can be used under space critical installation conditions

1.2.4 Highly Scalable Performance with low power consumption

- Scalable Low Voltage and Ultra Low Voltage Pentium M class processor system to bring high computing performance with low power consumption

1.2.5 Optimized Integration

- Few Parts, Easy Integration, Easy Maintenance to reduce investment
- Systems are supplied ready to run
- Long life cycle support for product continuity
- Wide range of DC 12V~24V power source offers flexibility of power input for various automation environments.

1.3 Hardware Specification

1.3.1 Processor Core Logic System

CPU

- Intel® Ultra Low Voltage Celeron® M or Intel® Pentium® M Low Voltage Processor, μ FC-BGA 479 Package:
 - ULV Celeron® M 1 GHz (for Model of ARK-3380-1S0B1E)
 - LV Pentium® M 1.4 GHz (for Model of ARK-3380-1S4B1E)

System Chipset

- Intel® 852GM or Intel® 855GME Memory Controller Hub (GMCH) Chipset:
 - Intel® 852GM for model of ARK-3380-1S0B1E
 - Intel® 855GME for model of ARK-3380-1S4B1E
 - Intel® FW82801DB I/O Controller Hub 4 (ICH4) Chipset
400 MHz FSB

BIOS: 4Mbit Flash BIOS, supports Plug & Play

Power Management Support ATX, APM Rev. 1.2 and ACPI

System Memory

- One 200 pin SO-DIMM socket
- Supports DDR SDRAM Up to 1GB

1.3.2 Display

Chipset

Integrated graphics built-in Intel® 852GM GMCH, or Intel® 855GME GMCH, utilizing Intel® Extreme Graphics 2 technology

Display Memory

Dynamic video memory allocation up to 64 MB

Display Interface support

- CRT Interface
- 36-bit LVDS interface, optional support up to 48-bit
- DVI interface, compliant with DVI specification 1.0
- Supports both NTSC/PAL, S-Video and Composite Video output

1.3.3 Ethernet

Ethernet Controller: Intel® 82551QM Ethernet Controller

Speed: 10/100Mbps, IEEE 802.3u (100 BASE-T) protocol compatible

1.3.4 Audio

Audio Interface

- Audio controller on the ICH4 chip
- AC97 3D surround stereo sound, Dual 6 W Amplifier
- Supports Line_In, Speaker_Out, and Microphone_In

1.3.5 Other

Watchdog Timer: 255 levels timer interval, setup by software

Serial Port: One RS-232 port (COM1) and One RS-232/422/485 port (COM2)

Note: The default setting of COM2 is RS-232. The RS-422/485 mode of COM2 can be support via replacing the internal cable and adjust the jumper inside of system.

Keyboard/Mouse: One PS/2 Port to support PS/2 Mouse and PS/2 Keyboard

USB: Three USB 2.0 compliant universal Serial bus port

DIO: ARK-3380 provides one D-sub 8 Bits Female connectors, which offers Digital IO communication interface ports. If you want to use DIO, you can find the Pin assignment as following

1.3.6 Storage

- Supports a drive bay space for 2.5" HDD
- Supports a CompactFlash socket for Type I/II CompactFlash disk

1.3.7 Mechanical

Construction: Aluminum housing

Mounting: DIN-rail mounting, Desk/wall mounting

Dimension (W x H x D): 264.5 mm x 69.2 mm x 137.25 mm (10.41"x 2.72"x 4.4")

Weight: Net 2 KG

1.3.8 Power Supply

- Output Rating 46 W, ATX support
- Fuse Rating 7 A @ 125 V
- Input Voltage: 12 VDC ~ 24 VDC,
 - Typical:
12 VDC @ 4.5A,

16 VDC @ 3.4 A,
19 VDC @ 2.9 A,
24 VDC @ 2.3 A

- Output Voltage:
 - +5 VDC @ 7 A
 - +12 VDC @ 0.5 A
 - +5VSB @ 1 A

1.3.9 Environment Specifications

Operating Temperature

- When System is equipped with Industrial Grade Compact Flash Disk only: -20 to 60° C
- When System is equipped with 2.5-inch Hard Disk: 0 to 45° C

Relative humidity 95 % @ 40 ° C(non-condensing)

Vibration loading during operation

- When system is equipped with Compact Flash Disk only:
5Grms, IEC 68-2-64, random, 5~500Hz, 1 Oct./min, 1hr/axis.
- When system is equipped with 2.5-inch hard disk:
1Grms, IEC 68-2-64, random, 5~500Hz, 1 Oct./min, 1hr/axis.

Shock during operation

- When system is equipped with Compact Flash Disk only:
50Grms, IEC 68-2-27, half sine, 11 ms duration
- When system is equipped with Hard Disk:
20Grms, IEC 68-2-27, half sine, 11 ms duration

EMC Approved: CE, FCC Class A

Safety Approved: UL

1.4 Chassis Dimensions

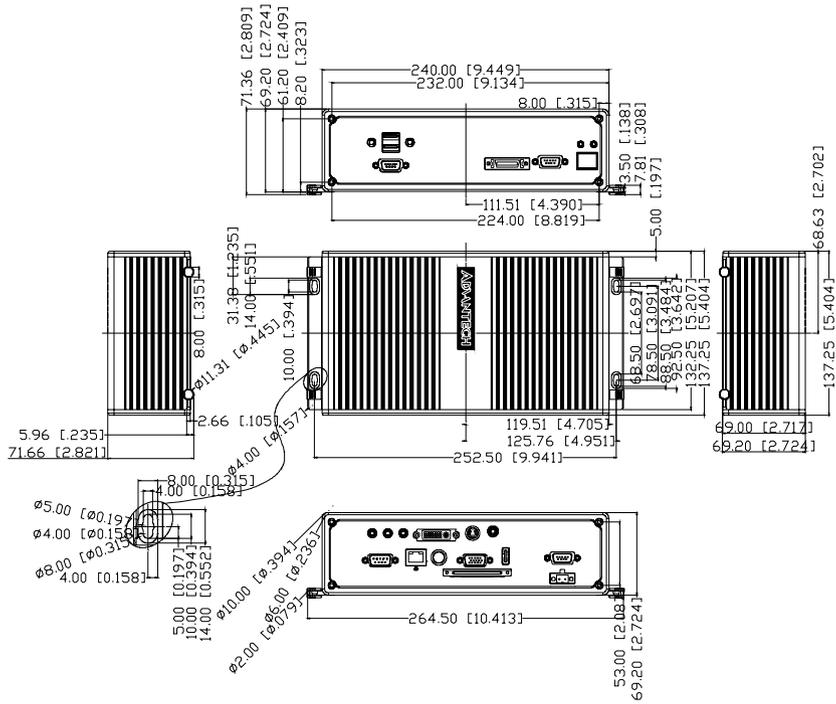


Figure 1.1: Chassis Dimensions

Hardware Functionality

This chapter shows how to set up the ARK-3380's hardware functions, including connecting peripherals, switches and indicators.

- Introduction of ARK-3380 External I/O connectors
- ARK-3380 front metal face plate external I/O conns
 - Power On/Off Button
 - LED Indicators
 - LVDS Connector
 - LCD Backlight On/Off control connector
- ARK-3380 rear metal face plate external I/O conns
 - COM1 connector
 - COM2 connector
 - Ethernet connector
 - Reset Button
 - PS2 Keyboard/Mouse connector
 - VGA connector
 - USB connector
 - Spk_Out, Mic_In, and Lin_In connectors
 - DVI connector
 - S-Video connector
 - Composite connector

Chapter 2 Hardware Functionality

2.1 Introduction of ARK-3380 External I/O Connectors

The following two figures show the external I/O connectors on ARK-3380. The following sections give you detail information about function of each I/O connector.

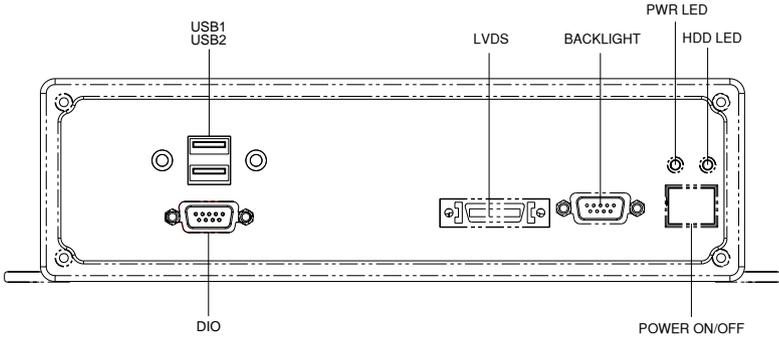


Figure 2.1: ARK-3380 front metal face plate external I/O connectors

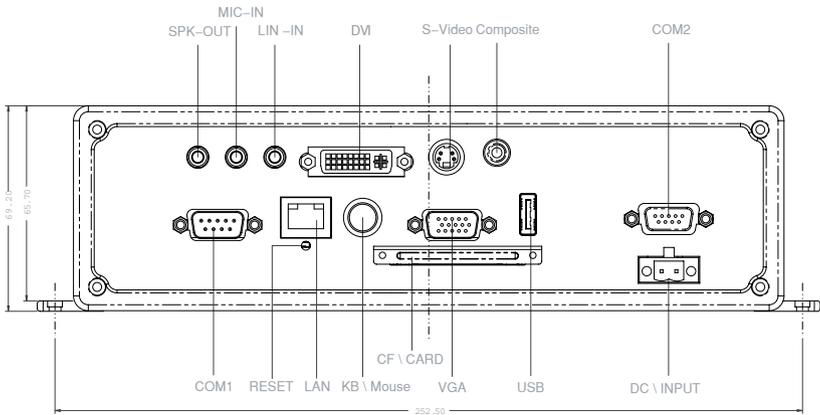


Figure 2.2: ARK-3380 rear metal face plate I/O connectors

2.2 ARK-3380 front metal face plate external I/O connectors

2.2.1 Power ON/OFF Button

The ARK-3380 comes with a ATX supported Power On/Off button, that Support dual function of Soft Power -On/Off (Instant off or Delay 4 Second), and Suspend.

2.2.2 LED Indicators

There are two LEDs on the ARK-3380 front metal face plate for indicating system status: PWR LED is for power status and Flash in Red color; and HDD LED is for hard disk and compact flash disk status, which Flash IN Green color.

2.2.3 LVDS Connector

The ARK-3380 comes with a D-Sub 26-pin connector that carries LVDS signal output, and can direct connect to LVDS LCD Display via external cable.

The system also provide a jumper of JP6 on internal PCM-9380 or PCM-9386 motherboard for selecting the LCD signal power of 5V or 3.3V, please refer to section 3.4 of Chapter 3 for the jumper table of JP6, and Chapter 6 of “Full Disassembly Procedure to set it. Up. The default setting of JP6 is 5V.

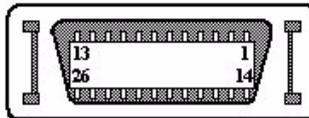


Figure 2.3: LVDS Connector

Table 2.1: LVDS Connector Pin Assignment

| Pin | Signal Name | Pin | Signal name |
|-----|-------------|-----|-------------|
| 1 | LVDS_CLKBP | 14 | LVDS_CLKBM |
| 2 | GND | 15 | LVDS_YAM0 |
| 3 | LVDS_YAP0 | 16 | LVDS_YAM1 |
| 4 | LVDS_YAP1 | 17 | LVDS_YAM2 |
| 5 | LVDS_YAP2 | 18 | LVDS_CLKAM |
| 6 | LVDS_CLKAP | 19 | GND |
| 7 | +3.3 or +5V | 20 | +3.3 or +5V |
| 8 | GND | 21 | LVDS_YAM3 |
| 9 | LVDS_YAP3 | 22 | LVDS_YBM0 |
| 10 | LVDS_YBP0 | 23 | LVDS_YBM1 |
| 11 | LVDS_YBP1 | 24 | LVDS_YBM2 |
| 12 | LVDS_YBP2 | 25 | LVDS_YBM3 |
| 13 | LVDS_YBP3 | 26 | GND |

2.2.4 LCD Backlight On/Off control Connector

The ARK-3380 comes with a D-Sub 9-pin connector which provides BKLTEN signal as well as +12V, +5V and Ground Pin signals that allow applicant to connect these signals to LCD Inverter to implement the LCD On/Off control.

- Provide BKLTEN signals that inverter Module require for controlling the on/off
- Provides 12V, 5V as the Inverter Power Source.

The additional VBR signal pin could be connected to LCD's Inverter that allow applicant to implement brightness adjustment through customer's software utility.

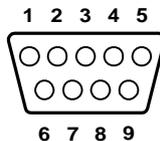


Figure 2.4: LCD Backlight connector

Table 2.2: LCD Backlight Connector Pin Assignment

| Pin | Signal name |
|-----|-------------|
| 1 | +12V |
| 2 | GND |
| 3 | BKLTEN |
| 4 | VBR |
| 5 | +5V |
| 6 | LVDS_DCLK |
| 7 | LVDS_DDAT |
| 8 | Reserved |
| 9 | Reserved |

2.2.5 DIO Connector

ARK-3380 provide one D-sub 9-pin Female connectors, which offers Digital IO communication interface ports. If you want to use DIO, you can find the Pin assignment as following.

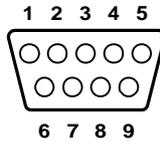


Figure 2.5: DIO Connector

Table 2.3: DIO connector

| DIO | |
|-----|-------------|
| Pin | Signal name |
| 1 | DIO0 |
| 2 | DIO1 |
| 3 | DIO2 |
| 4 | DIO3 |
| 5 | DIO4 |
| 6 | DIO5 |
| 7 | DIO6 |
| 8 | DIO7 |
| 9 | GND |

2.3 ARK-3380 rear metal face plate external I/O connectors

2.3.1 Power Input Connector

The ARK-3380 comes with a Phoenix connector that carries 12~24 VDC external power input.

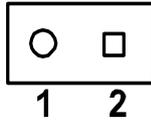


Figure 2.6: Power Input Connector

Table 2.4: Power connector pin assignments

| Pin | Signal Name |
|-----|-------------|
| 1 | GND |
| 2 | +12~24VDC |

2.3.2 COM1 Connector

The ARK-3380 provides a D-sub 9-pin connector, which offers one standard RS-232 serial communication interface port of COM1.

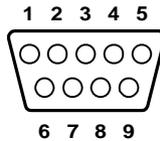


Figure 2.7: COM1 connector

Table 2.5: COM1 standard serial port pin assignments

| Pin | Signal Name |
|-----|-------------|
| 1 | DCD |
| 2 | RxD |
| 3 | TxD |
| 4 | DTR |
| 5 | GND |

| | |
|---|-----|
| 6 | DSR |
| 7 | RTS |
| 8 | CTS |
| 9 | RI |

2.3.3 COM2 Connector

The ARK-3380 provides a D-sub 9-pin connector, which offers one RS-232/422/485 serial communication interface port of COM2. The default setting of COM2 is RS-232. Please refer to section 4.1 “Jumper Settings” and section 7 “Full Disassembly Procedure” to set up RS-422 or RS-485.

The RS-422/485 mode of COM2 can be supported via replacing the internal COM 2 cable by using the new cable (Part Number 1700001967), and adjusting the jumper inside the system. The extra cable (pn.1700001967) has been stored in the accessory box of the product carton.

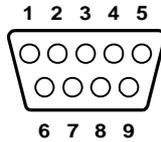


Figure 2.8: COM 2 connector

Table 2.6: COM2 standard serial port pin assignments

| | RS-232 | RS-422 | RS-485 |
|-----|-------------|-------------|-------------|
| Pin | Signal Name | Signal Name | Signal Name |
| 1 | DCD | Tx- | DATA- |
| 2 | RxD | Tx+ | DATA+ |
| 3 | TxD | Rx+ | NC |
| 4 | DTR | Rx- | NC |
| 5 | GND | GND | GND |
| 6 | DSR | NC | NC |
| 7 | RTS | NC | NC |
| 8 | CTS | NC | NC |
| 9 | RI | NC | NC |

Note: NC represents “No Connection”.

2.3.4 Audio Connector

The ARK-3380 offers AC97 stereo Audio output ports by three phone jack connectors of Speaker_Out, Mic_In and Line_In.

Table 2.7: Line_In connector

Line-In Connector

| | |
|-----------|---|
| Footprint | Phone Jack 3.5Ø 5P, 90 Degree, Female, BLUE color, SHIELDED |
|-----------|---|

Table 2.8: Speaker_Out connector

Speaker-Out Connector

| | |
|-----------|---|
| Footprint | Phone Jack 3.5Ø 5P, 90 Degree, Female, LIME color, SHIELDED |
|-----------|---|

Table 2.9: Mic_In connector

Mic_In Connector

| | |
|-----------|---|
| Footprint | Phone Jack 3.5Ø 5P, 90 Degree, Female, PINK color, SHIELDED |
|-----------|---|

2.3.5 DVI Connector

The ARK-3380 offers a Digital Visual Interface by a D-sub 24-pin female digital only connector, which supports high-speed, high-resolution digital displays.

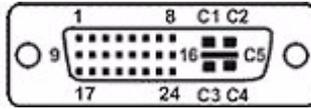


Table 2.10: DVI connector pin assignments

| Pin | Signal name |
|-----|-------------|
| 1 | TMDS_C2# |
| 2 | TMDS_C2 |
| 3 | GND |
| 6 | MDVI_CLK |
| 7 | MDVI_DATA |
| 9 | TMDS_C1# |
| 10 | TMDS_C1 |
| 11 | GND |
| 14 | VCC_DVI |
| 15 | GND |
| 16 | HP_DET |
| 17 | TMDS_C0# |
| 18 | TMDS_C0 |
| 19 | GND |
| 22 | GND |
| 23 | TMDS_CK |
| 24 | TMDS_CK# |
| C5 | GND |

2.3.6 S-Video Connector

The ARK-3380 has a 4-pin mini-DIN female S-video connector for analog video output to a PAL or NTSC video monitor or VCR.

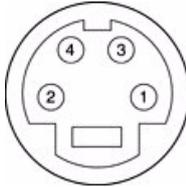


Figure 2.9: S-Video connector

2.3.7 Composite Video Connector

The ARK-3380 has a RCA-Jack connector for composite video output to a PAL or NTSC video monitor or VCR.



Figure 2.10: Composite-Video connector

2.3.8 Ethernet Connector (LAN)

The ARK-3380 is equipped with an Intel 82551QM Ethernet controller that is fully compliant with IEEE 802.3u 10/100Base-T CSMA/CD standards. The Ethernet port provides a standard RJ-45 jack connector with LED indicators on the front side to show its Active/Link status (Green LED) and Speed status (white LED).

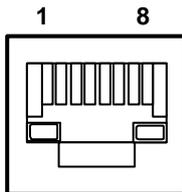


Figure 2.11: Ethernet connector

Table 2.11: RJ-45 Connector pin assignments

| Pin | 10/100BaseT Signal Name |
|-----|-------------------------|
| 1 | XMT+ |
| 2 | XMT- |
| 3 | RCV+ |
| 4 | NC |
| 5 | NC |
| 6 | RCV- |
| 7 | NC |
| 8 | NC |

2.3.9 Reset Button

Press the "Reset" button to activate the reset function.

2.3.10 PS2 Keyboard/Mouse Connector

The ARK-3380 provides a PS/2 keyboard/mouse connector. A 6-pin mini-DIN connector is located on the rear metal face plate of the ARK-3380. The ARK-3380 comes with an adapter to convert from the 6-pin mini-DIN connector to two 6-pin mini-DIN connectors for PS/2 keyboard and PS/2 mouse connection.

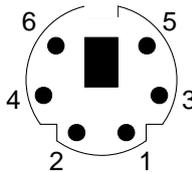


Figure 2.12: PS/2 connector

Table 2.12: PS/2 Keyboard/Mouse connector pin assignments

| Pin | Signal name |
|-----|-------------|
| 1. | PS2_KBDAT |
| 2. | PS2_MS DAT |
| 3. | GND |
| 4. | VCC |
| 5. | PS2_KBCLK |
| 6. | PS2_MSCLK |

2.3.11 VGA Connector

The ARK-3380 provides a high resolution VGA interface by a D-sub 15-pin connector to support a VGA CRT monitor. It supports VGA and VESA, up to 1600 x 1200 @85-Hz resolution and up to 32 MB shared memory.

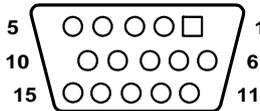


Figure 2.13: VGA connector

Table 2.13: VGA connector pin assignment

| Pin | Signal name |
|-----|-------------|
| 1 | Red |
| 2 | Green |
| 3 | Blue |
| 4 | NC |
| 5 | GND |
| 6 | GND |
| 7 | GND |
| 8 | GND |
| 9 | NC |
| 10 | GND |
| 11 | NC |

| | |
|----|--------|
| 12 | NC |
| 13 | H-SYNC |
| 14 | V-SYNC |
| 15 | NC |

2.3.12 USB Connector

The ARK-3380 provides three connectors, one in the rear panel and two in the front panel, of USB interface, which gives complete Plug & Play and hot swapping for up to 127 external devices. The USB interface complies with USB UHCI, Rev. 2.0 compliant. The USB interface can be disabled in the system BIOS setup. Please refer to Table 2.13 for its pin assignments.

The USB connector is used for connecting any device that conforms to the USB interface. Many recent digital devices conform to this standard. The USB interface supports Plug and Play, which enables you to connect or disconnect a device whenever you want, without turning off the computer.



Figure 2.14: USB connector

Table 2.14: USB Connector

| Pin | Signal name | Pin | Signal name |
|-----|-------------|-----|-------------|
| 1 | VCC | 2 | VCC |
| 3 | USB_P0- | 4 | USB_P1- |
| 5 | USB_P0+ | 6 | USB_P1+ |
| 7 | GND | 8 | GND |
| 9 | GND | 10 | NC |

Hardware Installation and Upgrade

This chapter introduces how to initialize the ARK-3380.

Sections include:

- Jumpers and Connectors
- Installing the DDR SDRAM Memory Module
- Inserting a Compact Flash Card
- Installing the 2.5" Hard Disk Drive (HDD)
- Connecting Power

Chapter 3 Hardware Installation and Upgrade

3.1 Jumpers and Connectors

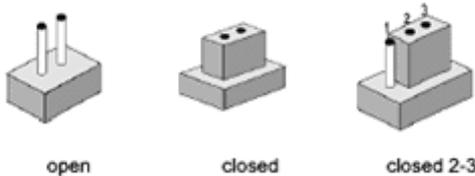
The ARK-3380 Embedded Box Computer consists of a PC-based computer that is housed in an aluminum top cover, a metal bottom case with accessed bottom cover and Front/ Rear Metal Face plates. Your HDD, SDRAM, are all readily accessible by removing the accessed bottom cover. Any maintenance or hardware upgrades can be easily completed after removing the top cover, and Front and Rear Metal Face plates. If you are a systems integrator and need to know how to completely disassemble the embedded box computer, you can find more useful information in Chapter 6.



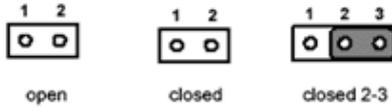
Warning! Do not remove any mechanical parts, such as the top cover, bottom cover and front with rear face plate until you have verified that no power is flowing within the Embedded Box Computer. Power must be switched off and the power cord must be unplugged. Every time you service the Embedded Box Computer, you should be aware of this.

3.2 Setting jumpers

You can configure your ARK-3380 to match the needs of your application by setting jumpers. A jumper is the simplest kind of electrical switch. It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To “close” a jumper, you connect the pins with the clip. To “open” a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2, and 3. In this case, you would connect either pins 1 and 2 or pins 2 and 3.



The jumper settings are schematically depicted in this manual as follows:



A pair of needle-nose pliers may be helpful when working with jumpers. If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any changes.

3.3 COM2 RS-232/422/485 Jumper setting (J3/J4/J5)

The COM2 port located on front metal face plate of ARK-3380 unit which can be configured to operate in RS-232, RS-422 or RS-485 mode by setting up the Jumper Pins of J3/J4/J5 located on internal motherboard of the ARK-3380 unit. Please also refer to Chapter 6 “Full Disassembly Procedure” of ARK-3380 Embedded Box Computer”.

Table 3.1: COM2 RS-232/422/485 Jumper Selection

| Function | Setting |
|----------|-------------------------------|
| RS-232 * | J3 (1-2 closed) (J4, J5 open) |
| RS-422 | J4 (1-2 closed) (J3, J5 open) |
| RS-485 | J5 (1-2 closed) (J3, J4 open) |

(*): means default setting of the jumper/function

The default setting of COM2 is RS-232. The RS-422/485 mode of COM2 can be support by replacing the internal cable and adjusting the jumper inside of system.

3.4 LCD Power Jumper Setting (J6)

The ARK-3380 series of embedded box computer provide a JP6 jumper located on internal PCM-9380 or PCM-9386 motherboard for selecting the LCD signal power of 5V or 3.3V. When you connect your LVDS LCD Panel display, you need to set up JP6 for your LCD power setting selection for your LVDS Panel display.

Table 3.2: LCD Power Setting (J6)

| Close pins | Function |
|------------|----------|
| 1-2 | +5V* |

| | |
|-----|-------|
| 2-3 | +3.3V |
|-----|-------|

(*): means default setting of the jumper/function

Please also refer to Chapter 6 of “Full Disassembly Procedure to set it. Up. The default setting of JP6 is 5V.

3.5 NTSC/PAL TV type Setting (J1)

The ARK-3380 series of embedded box computer provides a J1 jumper located on the internal MIO-253 module I/O board for selecting NTSC or PAL type of TV set. When you connect your TV set to the S-Video or Composite Video output connector, you need to set up the J1 jumper to select the correct TV mode.

Table 3.3: Type 3.3: NTSC/PAL TV selection(J1)

| Close pins | Function |
|------------|----------|
| 1-2 | PAL* |
| 2-3 | NTSC |

(*): means default setting of the jumper/function

Please also refer to Chapter 6 of “Full Disassembly Procedure to set it. Up. The default setting of J1 is PAL TV.

3.6 Installing the DDR SDRAM Memory Module

The ARK-3380 provides one 200-pin SODIMM (Small Outline Dual Inline Memory Module) socket and supports 2.5V DDR SDRAM. You can install from 64 MB to 1 GB of DDR SDRAM memory. The procedure of installing a DDR SDRAM SODIMM into the ARK-3380 is detailed below, please follow these steps carefully.

1. Remove the power cord.
2. Unscrew the four screws from the bottom cover of the ARK-3380.
3. Remove the bottom cover.
4. Plug a DDR SDRAM SODIMM with your OS or application program into a DDR SDRAM SODIMM on board.
5. Screw back the bottom cover with four screws.

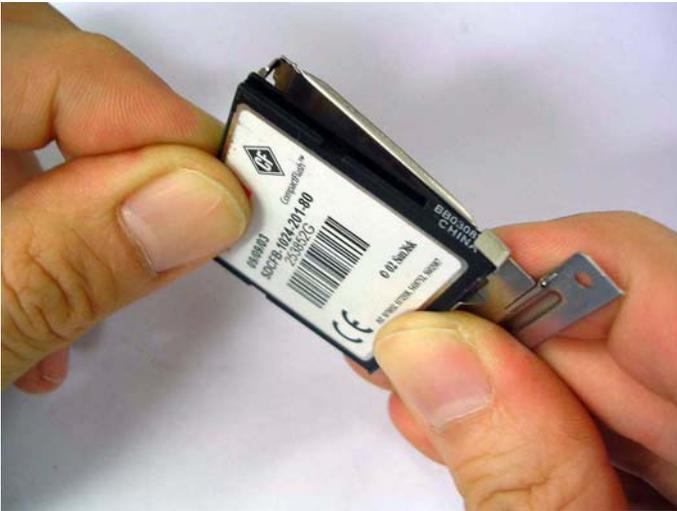
3.7 Inserting a Compact Flash Card

The procedure of installing a Compact Flash card into the ARK-3380 is detailed below, please follow these steps carefully.

1. Remove the power cord.
2. Unscrew the two screws from the CF Door located on rear face plate of the ARK-3380. Remove the embedded box computer.



3. Remove the CF carrier.
4. Placing a Compact Flash card with your OS or application program into the CF carrier.



5. Insert the CF Carrier back and assemble with 2 screws to the CF door located on rear face plate of the ARK-3380.



Note: The CompactFlash socket is allocated as Secondary IDE Master.

3.8 Installing the 2.5" Hard Disk Drive (HDD)

You can attach one Serial ATA (SATA) hard disk drive to the ARK-3380's internal controller. The following are instructions for installation:

1. Remove the power cord.
2. Unscrew the four screws from bottom cover of the ARK-3380.
3. Remove the bottom cover of the ARK-3380.
4. Connect the SATA flat cable to the connector to the hard disk.
5. Screw back the bottom cover with the four screws.

3.9 Connecting Power

Connect the ARK-3380 to a 12~24 VDC power source. The power source can either be from a power adapter or an in-house power source.

CHAPTER
4

Award BIOS Setup

Chapter 4 Award BIOS Setup

4.1 Introduction

The Award BIOS ROM has a built-in setup program that allows users to modify the basic system configuration. This information is stored in battery-backed memory (CMOS RAM) and it retains the setup information when the power is turned off.

4.1.1 CMOS RAM Auto-backup and Restore

The CMOS RAM is powered by a system board button cell battery. When BIOS setup is completed, the data in CMOS RAM is automatically backed up to Flash ROM. If operating in a harsh industrial environment causes a software error, BIOS will recheck the data in CMOS RAM and automatically restore the original data from Flash ROM to CMOS RAM for booting.

***Note:** If you intend to change the CMOS setting without restoring the previous backup, must be pressed within two seconds of the "CMOS checksum error..." display screen message appearing. Then enter the "Setup" screen to modify the data. If the "CMOS checksum error..." message appears again and again, please check to see if the system battery needs to be replaced.*

4.2 Entering Setup

Turn on the computer and check for the .patch code. If there is a number assigned to the patch code, it means that the BIOS supports your CPU. If there is no number assigned to the patch code, please contact an Advantech application engineer to obtain an up-to-date patch code file. This will ensure that your CPU system status is valid. After ensuring that a number is assigned to the patch code, press to enter Setup.

| CONTROL KEYS | |
|-----------------------|--|
| < ↑ >> ↓ >> ← >> → >> | Move to highlight item |
| <Enter> | Select Item |
| <Esc> | Main Menu - Quit without saving changes into CMOS |
| | Sub Menu - Exit current page and return to Main Menu |
| <Page Up/+> | Increase the numeric value or make changes |
| <Page Down/-> | Decrease the numeric value or make changes |
| <F1> | General help, for Setup Sub Menu |
| <F2> | Item Help |
| <F5> | Load Previous Values |
| <F7> | Load Optimized Defaults |
| <F10> | Save all CMOS changes |

4.2.1 Main Menu

During bootup, press to enter AwardBIOS CMOS Setup Utility; the Main Menu will appear on the screen. Use arrow keys to select among the items and press <Enter> to accept or enter a sub-menu.

- **Standard CMOS Features**

This setup page includes all the items in standard compatible BIOS.

- **Advanced BIOS Features**

This setup page includes all the Award advanced BIOS features items.

- **Advanced Chipset Features**

This setup page includes all the advanced chipset configuration items.

- **Integrated Peripherals**

This setup page includes all onboard peripheral devices.

- **Power Management Setup**

This setup page includes all the Power Management features items.

- **PnP/PCI Configurations**

This setup page includes PnP OS and PCI device configuration.

- **Frequency/Voltage Control**

This setup page includes CPU host clock control, frequency ratio and voltage.

- **Load Optimized Defaults**

This setup page loads system optimized values, for the best system performance configuration.

- **Set Password**

Establish, change, or disable password.

- **Save & Exit Setup**

Save CMOS value settings to CMOS and exit BIOS setup.

- **Exit Without Saving**

Abandon all CMOS value changes and exit BIOS setup.

4.2.2 Standard CMOS Features

- **Date**

The date format is <weekday>, <month>, <day>, <year>.

Weekday From Sun to Sat, determined and displayed by BIOS only

Month From Jan to Dec

Day From 1 to 31

Year From 1999 through 2098

- **Time**

The time format is <hour> : <minute> : <second>, based on 24-hour time.

- **IDE Primary Master/Slave**

2.5" HDD Auto-Detection Press "Enter" for automatic device detection.

- **IDE Secondary Master/Slave**

CF Card Auto-Detection Press "Enter" for automatic device detection.

- **Halt on**

This item determines whether the computer will stop if an error is detected during power up.

No Errors The system boot will not stop for any error.

All Errors Whenever the BIOS detects a non-fatal error the system will be stopped.

All, But Keyboard The system boot will not stop for a keyboard error; it will stop for all other errors. (Default value)

All, But Diskette The system boot will not stop for a disk error; it will stop for all other errors.

All, But Disk/Key The system boot will not stop for a keyboard or disk error; it will stop for al other errors.

- **Base Memory**

The POST of the BIOS will determine the amount of base (or conventional) memory installed in the system.

- **Extended Memory**

The POST of the BIOS will determine the amount of extended memory (above 1 MB in CPU's memory address map) installed in the system.

- **Total Memory**

This item displays the total system memory size.

4.2.3 Advanced BIOS Features

- **CPU Feature**

This item allows the user to adjust CPU features, CPU ratio, VID and Thermal and special features such as XD flag.

- **Virus Warning [Disabled]**

This item allows the user to choose the VIRUS Warning feature for IDE Hard Disk boot sector protection.

- **CPU L1 & L2 Cache [Enabled]**

This item allows the user to enable CPU L1 & L2 cache.

- **CPU L3 Cache [Enabled]**

This item allows the user to enable CPU L3 cache.

- **Quick Power On Self Test [Enabled]**

This field speeds up the Power-On Self Test (POST) routine by skipping retesting a second, third and fourth time. Default is enabled.

- **First / Second / Third Boot Device**

Assign bootup priorities.

Floppy Floppy drive gets this priority.

LS120 LS120 gets this priority.

Hard Disk Hard Disk gets this priority.

CDROM CDROM gets this priority.

ZIP ZIP drive gets this priority.

USB-FDD USB-FDD gets this priority.

USB-ZIP USB-ZIP gets this priority.

USB-CDROM USB-CDROM gets this priority.

USB-HDD USB-HDD gets this priority.

LAN LAN gets this priority.

Disabled Disable this boot function.

- **Boot Other Device [Enabled]**

Use this to add yet another device to the bootup queue. If First, Second, and Third Boot Devices are not present or fail, the system will boot from the next available device.

- **Boot Up NumLock Status [On]**

When ON, system boots to keypad NumLock activated.

- **Gate A20 Option [On]**

Options are On (default), and Off.

On means that the chipset controls GateA20, and gives fast GateA20 speed.

Off means that the keyboard controller controls GateA20. This gives normal GateA20 speed.

- **Typematic Rate Setting**

This item enables users to set the two typematic control items.

- Typematic Rate (Chars/Sec)

This item controls the speed at which system registers repeated key-strokes.

The eight settings are 6, 8, 10, 12, 15, 20, 24 and 30.

- Typematic Delay (Msec)

This item sets the time interval before automatic character repetition kicks in. The four delay options are 250, 500, 750 and 1000.

- **Security Option [Setup]**

System System will not boot and will not access Setup page unless the correct password is entered at the prompt.

Setup System will boot, but access to Setup is denied unless correct password is entered at the prompt. (Default value)

- **ACPI Mode [Enable]**

This item sets the operating in Advanced Configuration and Power Interface in Windows 2000-based computer for power saving Function.

4.2.4 Advanced Chipset Features

Note: The “Advanced Chipset Features” options control the configuration of the board’s chipset. This page is developed for the particular chipset, to control chipset register settings, and fine tune system performance. It is strongly recommended that only technical users make changes to the default settings.

- **DRAM Timing Selectable [By SPD]**

This option refers to the method by which the DRAM timing is selected.

The default is “By SPD”.

Manual This item provides DRAM clock/drive for The user selection.

By SPD This item provides DRAM clock/drive for SPD (Serial Presence Detect).

- **MGM Core Frequency [Auto Max 266 MHz]**

This field sets the frequency of the DRAM memory installed. The default setting is Auto Max 266MHz.

- **System BIOS Cacheable [Enabled]**

This item allows the system BIOS to be cached to allow faster execution and better performance.

- **Video BIOS Cacheable [Disabled]**

This item allows the video BIOS to be cached to allow faster execution and better performance.

- **Memory Hole [Disabled]**

This item reserves 15 MB-16 MB memory address space to ISA expansion cards that specifically require the setting. When enabled, memory from 15 MB-16 MB will be unavailable to the system because only the expansion cards can access memory in this area.

- **Delayed Transaction [Enabled]**

The chipset has an embedded 32-bit posted write buffer to support delay transaction cycles. Select Enabled to support compliance with PCI specification version 2.1.

- **Delay Prior to Thermal [16Min]**

This field activates the CPU thermal monitoring function after the system is given time to boot for a set number of minutes. Options are 16Min and 64 Min.

- **AGP Aperture Size [64]**

The field sets aperture size of the graphics. The aperture is a portion of the PCI memory address range dedicated for graphics memory address space. Host cycles that hit the aperture range are forwarded to the AGP without any translation. The default setting is 64 M.

- **On-Chip VGA [Enabled]**

This item is setting for start up video output from an add-on-card or onboard device.

- **On-Chip Frame Buffer Size [32 MB]**

The options available are: 1 MB, 4 MB, 8 MB, 16 MB, and 32 MB. 32 MB is the default setting.

- **Boot Display [VBIOS Default]**

The default setting is VBIOS Default. The options available include CRT, LFP, and LFP + CRT.

- **Panel Scaling [Auto]**

The default setting is Auto. The other options are On and Off.

- **Panel Type [800X600]**

Use this field to select the LCD Panel type. The options are:

640 x 480

800 x 600

1024 x 768

1280 x 1024

1600 x 1200

4.2.5 Integrated Peripherals

Note: These “Integrated Peripherals” options include configuration of the board’s chipset, with settings for IDE, ATA, SATA, USB, Super IO and sensor devices. This page is chipset dependent.

- **IDE Cable Detect [Enabled]**

Some UDMA cables use a hole in the ribbon cable as a cable detect mechanism to determine if a UDMA IDE or standard IDE cable is installed. The default setting is: Enabled.

- **DIO Group 1/2 Direction [Output / Input]**

This item allows users to set the OnChip DIO status.

- **OnChip IDE Device [Press Enter]**

This item enables users to set the OnChip IDE device status; it includes enabling IDE devices and setting PIO and DMA access mode, and in some new chipsets also supports SATA devices (Serial-ATA).

- **Onboard Device [Press Enter]**

This item enables users to set the Onboard device status, including enabling USB, AC97, MC97 and LAN devices.

- Onboard LAN Control (82551 Only) [Enabled]

Options are Enabled and Disabled. Select Disabled if the user does not want to use onboard LAN controller1.

- USB Control [Enabled]

Select Enabled if your system contains a Universal Serial Bus (USB) controller and you have USB peripherals. The choices: Enabled, Disabled.

- USB 2.0 Control [Enabled]

Select Enabled if your system includes Universal Serial Bus (USB) controller support USB 2.0 and you have USB peripherals. The choices: Enabled, Disabled.

- USB Keyboard/Mouse Support [Enabled]

Select Enabled if the user plans to use a USB keyboard. The choices: Enabled, Disabled.

- **Super IO Device [Press Enter]**

This item enables users to set the Super IO device status, including enable Floppy, COM, LPT, IR and control GPIO and Power fail status.

- Onboard Serial Port 1 [3F8/IRQ4]

This item allows the user to select I/O port address. Range is from 2E8 to 3F8.

- Onboard Serial Port 2 [2F8/IRQ3]

This item allows the user to select I/O port address. Range is from 2E8 to 3F8.

• **Watch Dog Timer Select [Disabled]**

This item allows the user to enable or disable the watch dog timer. If enabled, a timer value may be set in either minutes or seconds.

4.2.6 Power Management Setup

Note: Use “Power management Setup” options to configure the system for most effective energy utilization still consistent with intended computer use.

• **ACPI Function [Enabled]**

This item defines the ACPI (Advanced Configuration and Power Interface) feature that makes hardware status information available to the operating system, allowing PC and system devices to communicate to improve power management.

• **ACPI Suspend Type [S1 (POS)]**

This item allows the user to select sleep state when in suspend, but this system is limited to S1 Mode.

S1(POS) The suspend mode is equivalent to a software power down;

S3(STR) The system shuts down with the exception of a refresh current to the system memory.

S1(POS) &S3(STR) This item supports two modes, with software selection.

• **Power Management Option [User Define]**

This item allows the user to select system power saving mode.

Min Saving Minimum power management. Suspend Mode=1 hr.

Max Saving Maximum power management. Suspend Mode=1 min.

User Define Allows the user to set each mode individually. Suspend Mode= Disabled or 1 min ~1 hr.

- **Video Off Method [DPMS]**

This item allows the user to determine the manner in which the monitor is blanked.

V/H SYNC+Blank This option will cause system to turn off vertical and horizontal synchronization ports and write blanks to the video buffer.

Blank Screen This option only writes blanks to the video buffer.

DPMS Initial display power management signaling.

- **Video Off in suspend [Yes]**

This item allows the user to determine the manner in which the monitor is blanked.

No Screen is off when system goes into suspend mode.

Yes Screen is never turned off.

- **Suspend Type [Stop Grant]**

This option controls the suspend state. Stop Grant only halts power to the processor. It is set as default to facilitate quick awakening performance.

PwrOn Suspend saves more power during suspend, and is recommended when greater power saving is desired.

Stop Grant When in this state, the CPU has internal clock running and may respond to interrupts and other signals. The CPU may transition from Stop Grant state to Normal state, AutoHalt mode and Sleep mode.

PwrOn Suspend When in this state, the CPU remains in low-power status with power still on.

- **Modem use IRQ [3]**

This item allows the user to determine which IRQ the MODEM can use.

- **Suspend Mode [Disabled]**

This item allows the user to enable or disable suspend mode. When enabled, the user can set the delay before suspend.

- **HDD Power Down [Disabled]**

This item allows the user to enable or disable HDD power down. When enabled, the user can set the delay before HDD power down.

- **Soft-Off by PWR-BTTN [Instant-Off]**

This item allows the user to adjust the function of the power button.

Instant-Off Press power button then Power off instantly.

Delay 4 Sec Press power button 4 sec. to Power off.

- **Wakeup By PCI Card [Press Enter]**

This item allows the user to select wake up by PCI Device.

- **Power On By Ring [Disabled]**

This item allows the user to enable system resume by modem ring. System default is set to “Disabled”.

- **Resume By Alarm [Disabled]**

This item allows the user to enable and key in Date/time to power on system.

Disabled Disable this function.

Enabled Enable alarm function to power on system

Date (of month) Alarm 1-31

Resume Time (HH:MM:SS) Alarm(0-23) : (0-59) : 0-59)

- **PWRON After PWR-Fail [Off]**

This item allows the user to select power fail function. The functions depend on chipset design.

4.2.7 PnP/PCI Configurations

Note: This “PnP/PCI Configurations” screen is for setting up the IRQ and DMA (both PnP and PCI) bus assignments.

- **Reset Configuration Data [Disabled]**

This item allows the user to clear any PnP configuration data stored in the BIOS.

- **Resources Controlled By [Auto (ESCD)]**

- IRQ Resources

This item allows the user to assign interrupt types to IRQ-3, 4, 5, 7, 9, 10, 11, 12, 14, and 15, respectively.

- PCI / VGA Palette Snoop [Disabled]

The item is designed to solve problems caused by some non-standard VGA cards. A built-in VGA system does not need this function.

4.2.8 Frequency/Voltage Control

Note: This “Frequency/Voltage Control” option controls the CPU Host and PCI frequency, this page is CPU and Chipset dependent; some items will show up when a processor which supports those items is installed.

- **Auto Detect PCI Clk [Enabled]**

This item enables or disables automatic PCI clock detection.

- **Spread Spectrum [Disabled]**

This item enables or disables spread spectrum modulation.

- **CPU Host/3V66/PCI Clock [Default]**

This item enables users to set the CPU Host, AGP and PCI clock, either by automatic detection or manually.

4.2.9 Load Optimized Defaults

NOTE: Load Optimized Defaults loads the default system values directly from ROM. If the stored record created by the Setup program should ever become corrupted (and therefore unusable), these defaults will load automatically when the ARK-339X Series system is turned on.

4.2.10 Set Password

Note: To enable this feature, first go to the Advanced BIOS Features menu, choose the Security Option, and select either Setup or System, depending on which aspect you want password protected. Setup requires a password only to enter Setup. System requires the password either to enter Setup or to boot the system. A password may be at most 8 characters long.

To Establish Password

1. Choose the Set Password option from the CMOS Setup Utility main menu and press <Enter>.
2. When you see “Enter Password”, enter the desired password and press <Enter>.
3. At the “Confirm Password” prompt, retype the desired password, then press <Enter>.
4. Select Save to CMOS and EXIT, type <Y>, then <Enter>.

To Change Password

1. Choose the Set Password option from the CMOS Setup Utility main menu and press <Enter>.
2. When you see “Enter Password”, enter the existing password and press <Enter>.
3. You will see “Confirm Password”. Type it again, and press <Enter>.
4. Select Set Password again, and at the “Enter Password” prompt, enter the new password and press <Enter>.
5. At the “Confirm Password” prompt, retype the new password, and press <Enter>.
6. Select Save to CMOS and EXIT, type <Y>, then <Enter>.

To Disable Password

1. Choose the Set Password option from the CMOS Setup Utility main menu and press <Enter>.
2. When you see “Enter Password”, enter the existing password and press <Enter>.
3. You will see “Confirm Password”. Type it again, and press <Enter>.
4. Select Set Password again, and at the “Enter Password” prompt, do not enter anything; just press <Enter>.
5. At the “Confirm Password” prompt, again, do not type in anything; just press <Enter>.
6. Select Save to CMOS and EXIT, type <Y>, then <Enter>.

4.2.11 Save & Exit Setup

Note:

Typing “Y” quits the BIOS Setup Utility and saves user-set values to CMOS.

Typing “N” returns to BIOS Setup Utility.

4.2.12 Quit Without Saving

Note:

Typing “Y” quits the BIOS Setup Utility without saving to CMOS.

Typing “N” returns to BIOS Setup Utility.

PCI SVGA/LCD Setup

This chapter details the software configuration information. It shows you how to configure the card to match your application requirements. The AWARD System BIOS is covered in Chapter 4.

Sections include:

- Installation of SVGA drivers
 - for Windows 98/ME
 - for Windows 2000

Chapter 5 PCI SVGA/LCD Setup

5.1 Introduction

The board has an onboard Intel 855GME chipset for its AGP/SVGA controller. It supports LVDS LCD displays and conventional analog CRT monitors with 64 MB frame buffer shared with system memory. The VGA controller can drive CRT displays with resolutions up to 1600 x 1200 @ 85 Hz and 2048 x 536 @ 75 Hz and support 2 channel LVDS display mode up to UXGA panel resolution with frequency range from 25 MHz to 112 MHz

5.1.1 CMOS setting for panel type

The ARK-3380 system BIOS and custom drivers are located in a 512 Kbyte, Flash ROM device, designated U29 of system motherboard of ARK-3380. A single Flash chip holds the system BIOS, VGA BIOS and network Boot ROM image. The display can be configured via CMOS settings. This method Choice of “Boot display” selection items of Advanced Chipset Features sections of Award BIOS Setup.

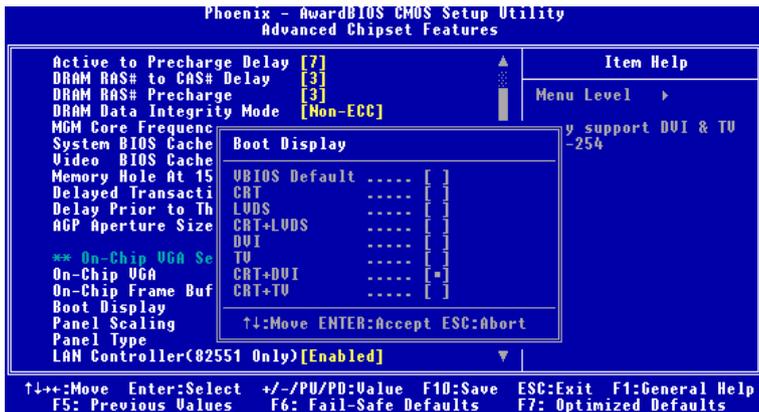


Figure 5.1: Advanced Chipset features screen

5.1.2 Display type

The ARK-3389 can be set in one of four configurations: on a CRT, on a LVDS based of flat panel display, on a DVI based of digital flat panel display, on a TV display, or on both dual independent displays. The system is initially set to “Auto”.

5.1.3 Dual Independent Display

The ARK-3389 uses an Intel 855GME or Intel 852GM controller that is capable of providing multiple views and simultaneous display with mixed video and graphics on a flat panel and CRT. To set up dual display under Windows 2000/XP follow these steps:

1. Select Start, “Control panel”, “Setting”, “Advanced”, “Graphics Properties”, “Device”.

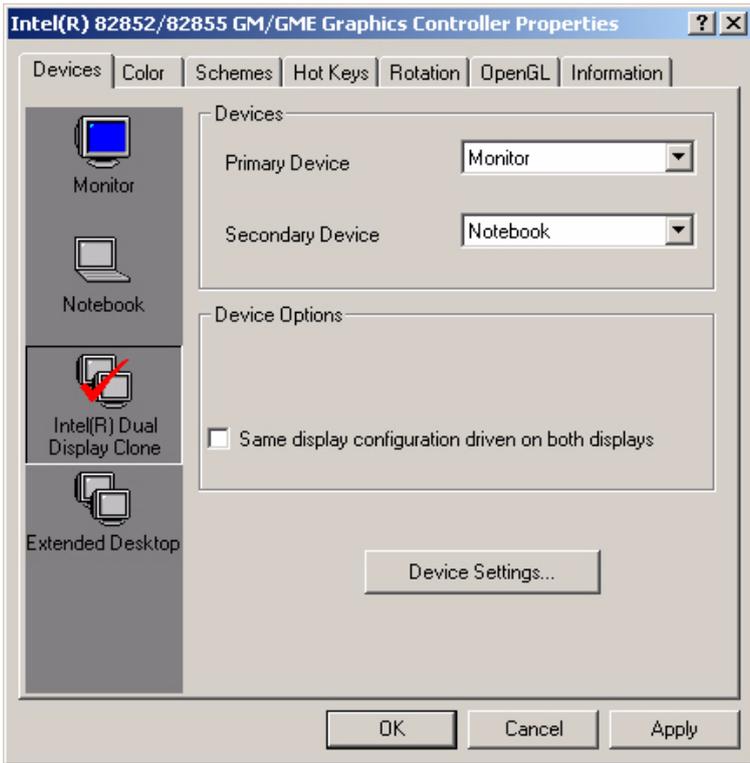


Figure 5.2: Intel® 82852/82855 GM/GME Graphics Controller Properties - Devices

2. Select “1” for current display, or “2” for second display.

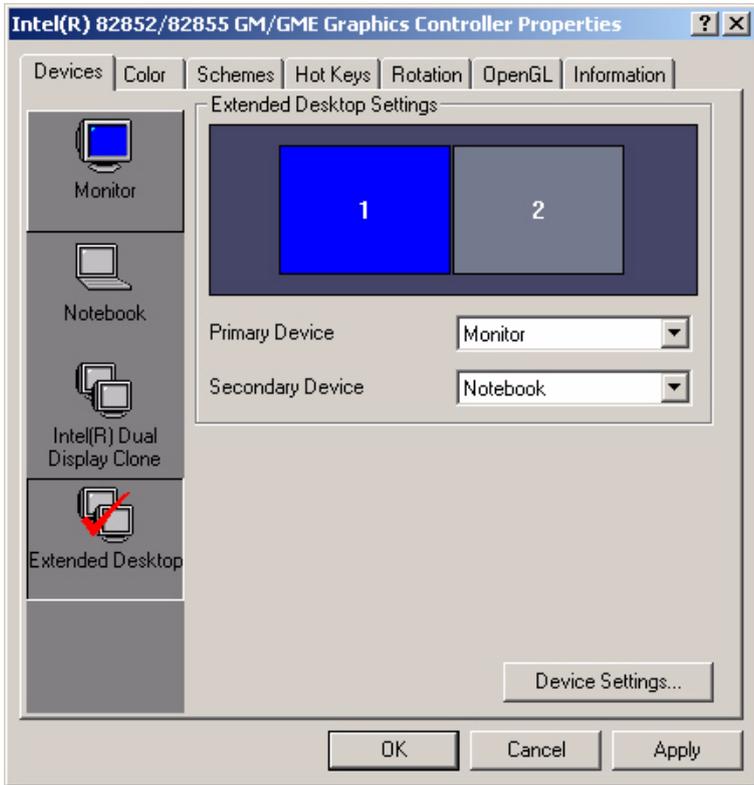


Figure 5.3: Intel® 82852/82855 GM/GME Graphics Controller Properties – Extended Desktop Settings

3. Enable “Extend my Windows desktop onto this monitor”.
4. Click “OK”.

5.2 Installation of the SVGA Driver

Complete the following steps to install the SVGA driver. Follow the procedures in the flow chart that apply to the operating system that you are using within your ARK-3380.

5.2.1 Installation of Windows 98/Me

1. Find Win98/Me VGA driver from CD at the directory of ARK-3380 CD, VGA\Win9x_ME\Graphics\Setup

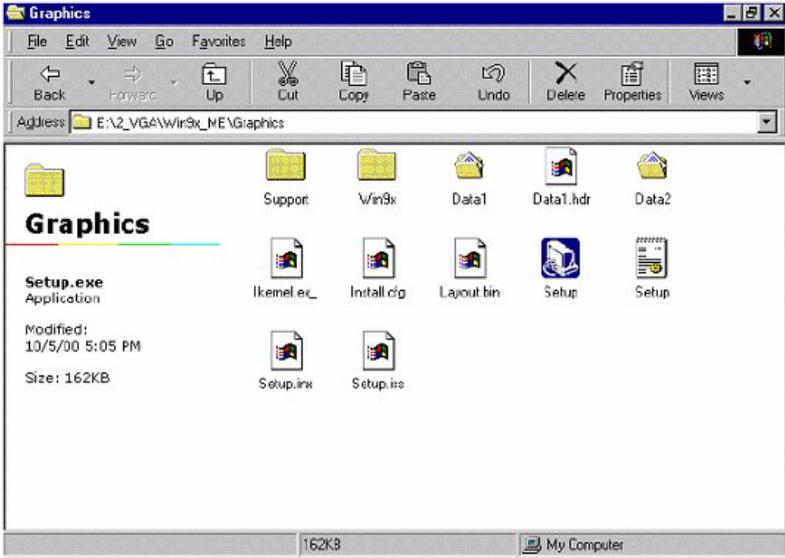


Figure 5.4: Directory “Graphics”

Notes: 1. The windows illustrations in this chapter are intended as examples only. Please follow the listed steps, and pay attention to the instructions which appear on your screen. 2. For convenience, the CD-ROM drive is designated as "D" throughout this chapter.

5.2.2 Installation of Windows 2000/XP

1. You can find Win2000/XP VGA driver from CD at the directory of ARK-3380 CD-ROM, \VGA\win2k_xp1332.

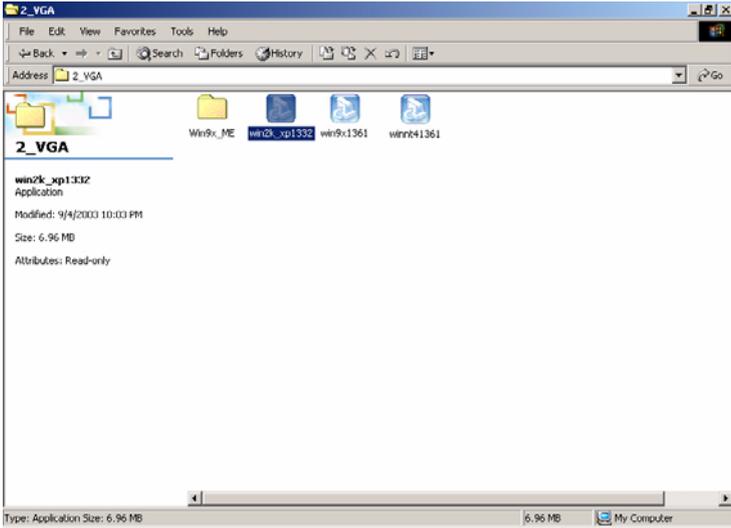


Figure 5.5: CD Directory “2.VGA”

2. Double click "setup" and "next" into setup wizard.

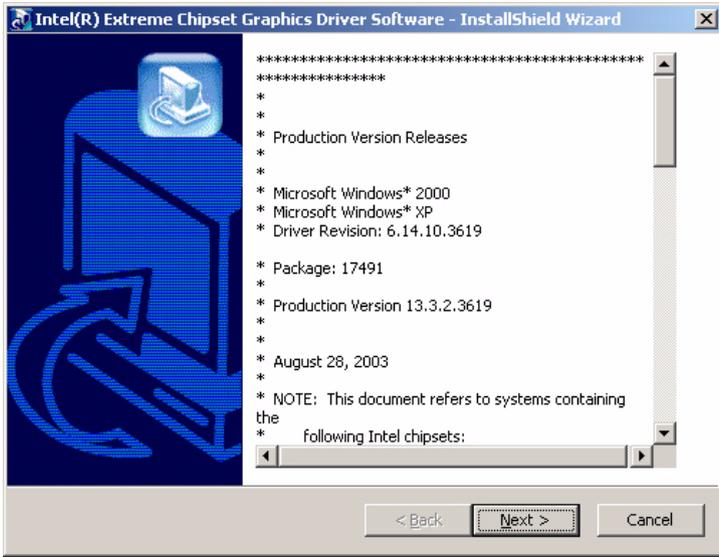


Figure 5.6: Intel® Extreme Chipset Graphics Driver Software Install Wizard

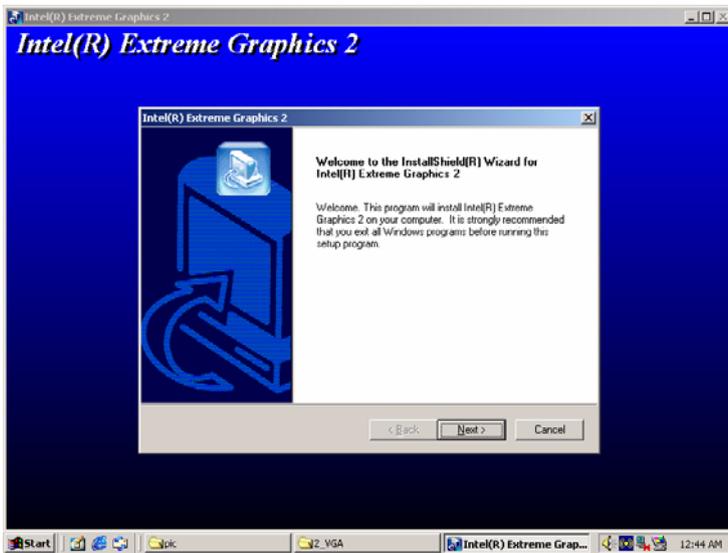


Figure 5.7: Intel® Extreme Graphics Driver Setup

- Restart computer when installation finished.

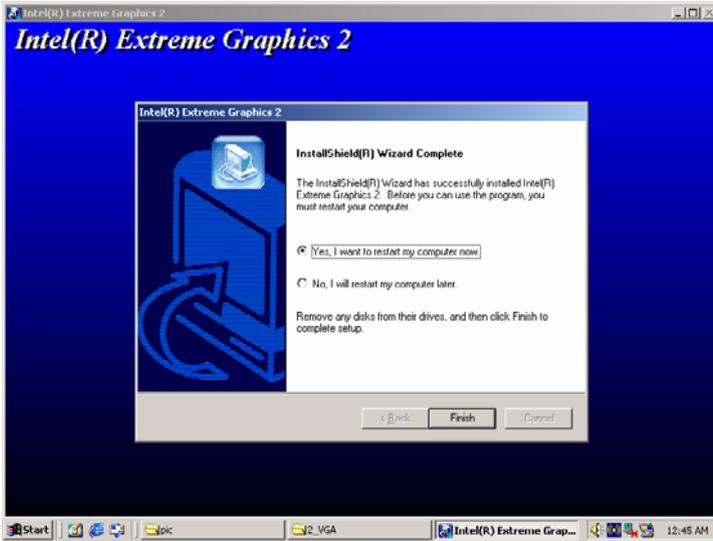


Figure 5.8: InstallShield® Wizard Complete

CHAPTER 6

Full Disassembly Procedure

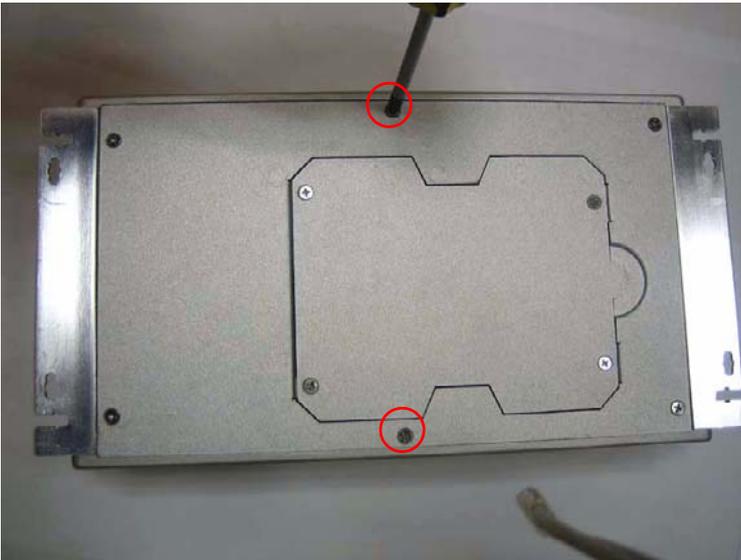
This chapter details the system disassembling procedure for setting up the jumpers and for maintenance.

Chapter 6 Full Disassembly Procedure

6.1 Introduction

If you want to completely disassemble the ARK-3380 embedded box computer, follow the step-by-step procedures below. Users should be aware that Advantech Co., Ltd. takes no responsibility whatsoever for any problems or damage caused by the user disassembly of the ARK-3380 embedded box computer. Make sure the power cord of the ARK-3380 embedded box computer is unplugged before you start disassembly. The following procedures do not include the detailed disassembly procedures for the HDD, Compact Flash Disk and SRAM; all of which can be found in Chapter 3.

1. Unscrew the 2 screws on the bottom side.



2. Unscrew the screws of the frame bracket on the front side of system.



3. Remove the front frame bracket by carefully pulling and lifting the bracket in order to unlock it.
4. The Front Frame Bracket removed.



5. Unscrew the 2 screws which are located on the Front Metal Face plate.

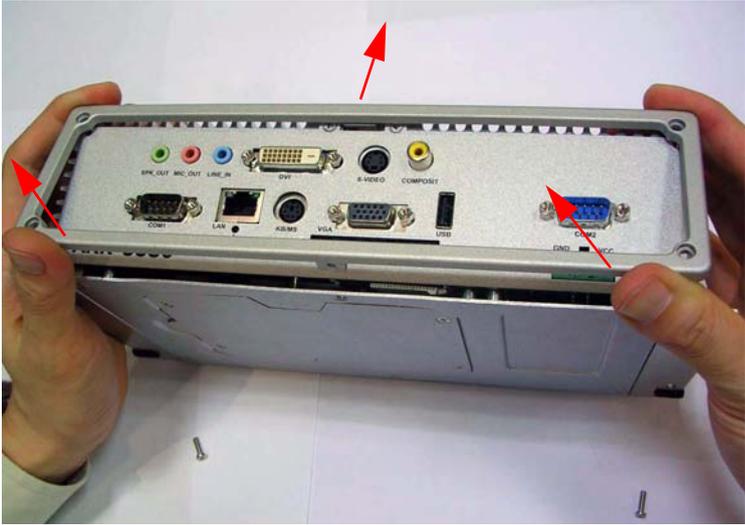


6. The Front Metal Face Plate removed.

Warning: Do not use too much pressure when removing the front metal face plate as the power button cable is still attached and could be damaged.

7. Unscrew the 4 screws of the frame bracket on the rear side of system.

- Remove the rear frame bracket by carefully pulling and lifting the bracket in order to unlock it.



- The rear frame bracket removed.



10. Unscrew the 2 screws which are located on the Rear Metal Face plate.
11. Disassembly the 2 screws which fixed the “COM2 port” on the Rear Metal Face Plate.

Warning: Do not use too much pressure when removing the front metal face plate as the COM2 connector cable is still attached and could be damaged.

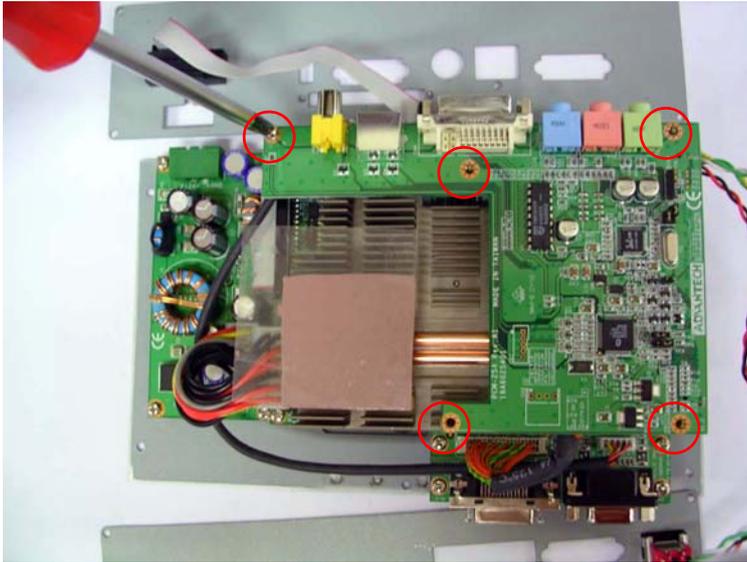
12. The rear Metal Face Plate removed.



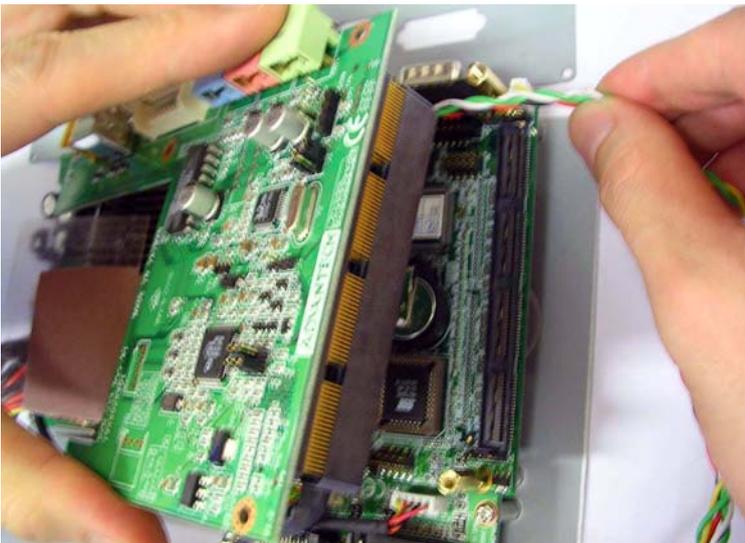
13. The system after the top cover has been removed.



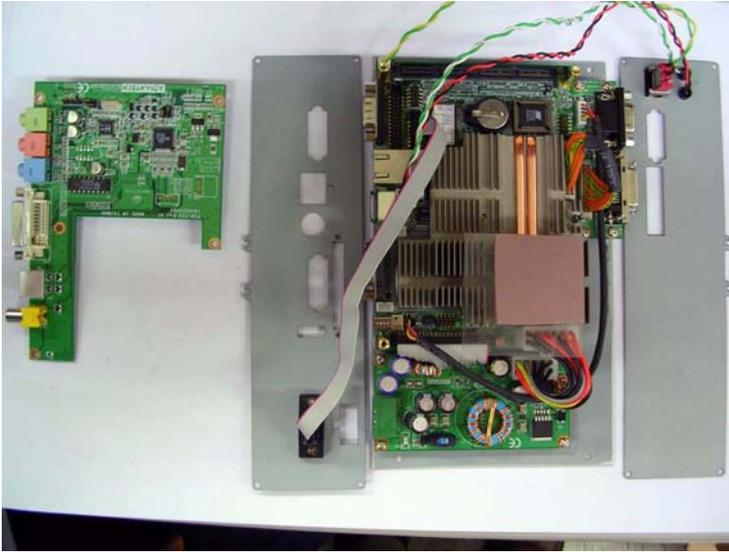
14. Unscrews the 5 screws which fixed the MIO-6254 Module I/O board to the system board.



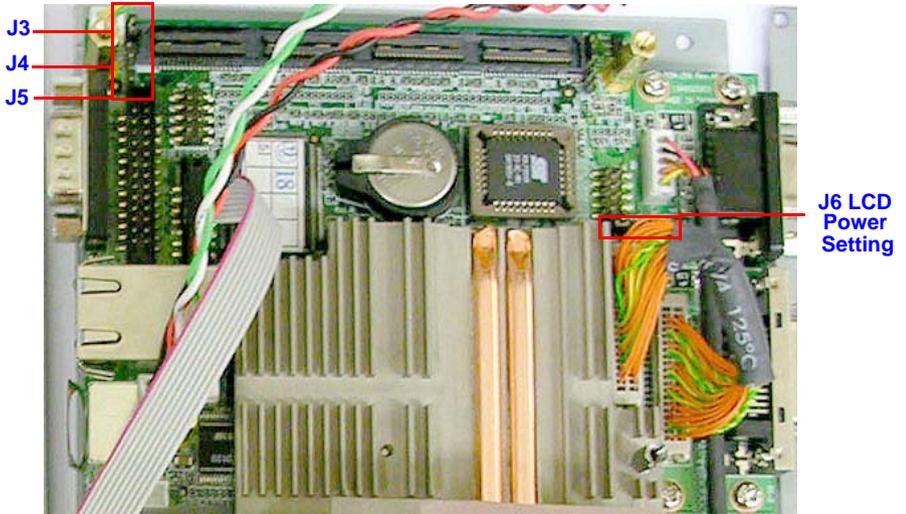
15. Remove MIO-6254 Module I/O board from MIO Socket of system board.



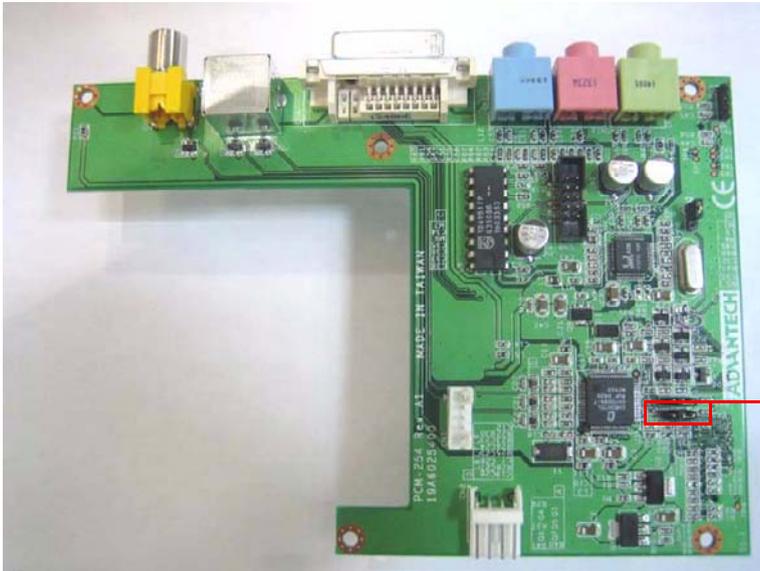
16. The MIO-6254 Module I/O board removed from system.



17. Jumper Location on PCM-9380/PCM-9386 Motherboard.



18. Jumper Location on MIO-6254 module I/O board.



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