

Revision History:

SOM-DB4400 Rev. A1 01-1 2001/06/18

1. PCB Version: A1 01-1 P/N:1906440000

SOM-DB4400 Rev. A1 02-1 2001/09/12

1. PCB Version: A1 02-1 P/N:1906440001
 2. PG13 : CN30.3 change NC/BACKON# connect to BACKON#_LCDG7_TX-14
 3. PG13 :Add. U28.5 SHFCLK1 for 36Bits LCD
 4. PG14 :Add. R186,R187 for 36Bits LCD
 5. PG14 :Add. R188 for NonBuffer LCD Clock
 6. PG25 :Add. C44 and C45
- BOM: 9696440011, 9696440001

SOM-DB4400 Rev. A2 01-1 2003/06/18

1. PCB Ver. A2 01-1 P/N:1906440002 BOM: 9696440012
2. PG02: Modify 3.3V Power Conflict (Delete SOM1A A12/A16/A24)
3. PG02: Add USB+-2 & USB+-3 Signals (SOM1A-A58,A62,A66,A72)
5. PG05: Add RSMRST#(D11),SMBALRT#(D26),BATLOW#(D41),GPE1#(D42),GPE2#(D89),EXTSMI(D21)
6. PG05: Add CBLID_P#(D90)
7. PG06: Add J15(PWR-ON),J16(RST-SW) Button SW And FAN Connector(CN35)
8. PG16: Add CN36 USB 3,4 PinHeader Connector
9. PG05: Add J14(SMB),J17(IIC) For verification

SOM-DB4400 Rev. A2 02-1 2003/10/14

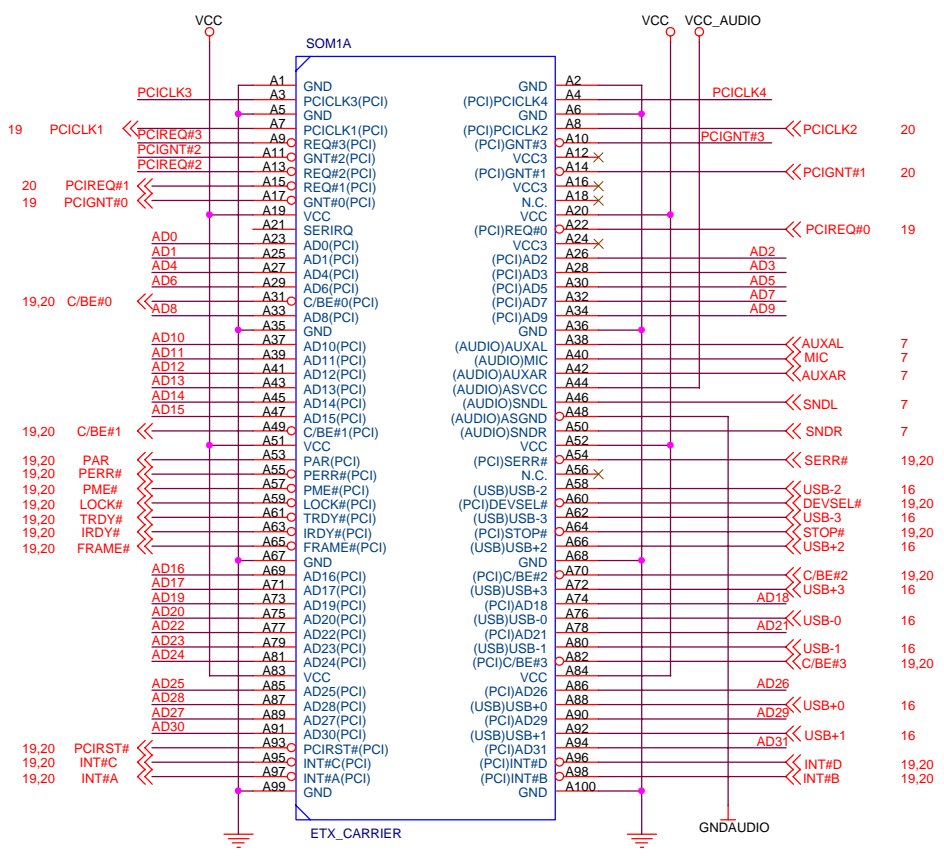
1. PCB Ver. A2 02-1 P/N:1906440003
 2. PG02: Modify Audio_VCC and Audio_GND from System Module.
 3. PG05: Modify J14(SMB),J17(IIC) from 3.3V to VCC.5V
 4. PG02~05: Add the ETX 400Pin Test Point
 5. Modify TVGND at a wide trace to fix noise issue
 6. Modify J15, J16 and (PG13) VR1 PCBFootprint.
 7. PG05: Add optional R481,R482 for ETX2.0 & 2.6
 8. PG06: Add optional Audio Power Q7 (78L05), C273, C274, L134, L133)
- BOM: 9696440013 W/By Pass, 9696440021 Full Function

SOM-DB4400 Rev. A2 03-1 2004/09/8

1. PCB Version: A2 03-1 P/N:19A6440000
2. PG20 : Fix CN17.A59(+5V) short to GND
3. PG26 : Fix PCI SERR# connect to 2nd SIO LTP ERR# signal
4. PG20 : Add J18 for PCI1 IDSEL
5. PG9 : Add J19 for CF master/slave select
6. PG16 : Add CK1,CK2 for USB2.0

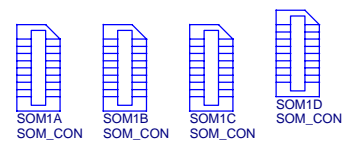
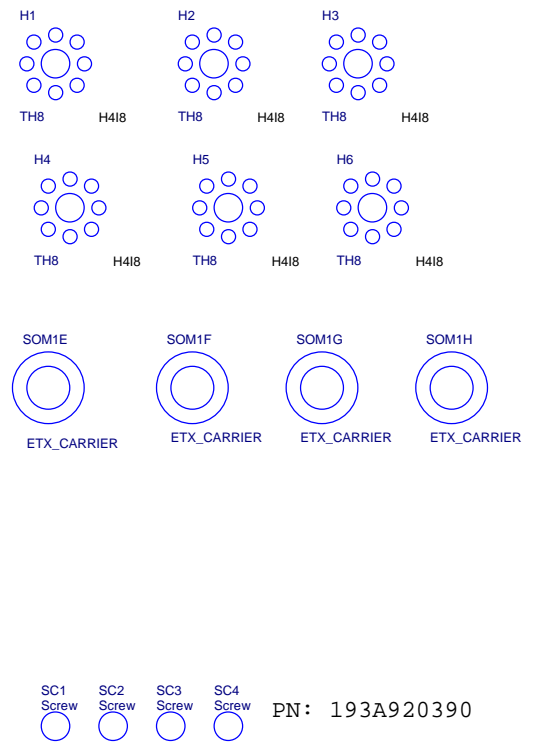
ADVANTECH

Title		
Revision History		
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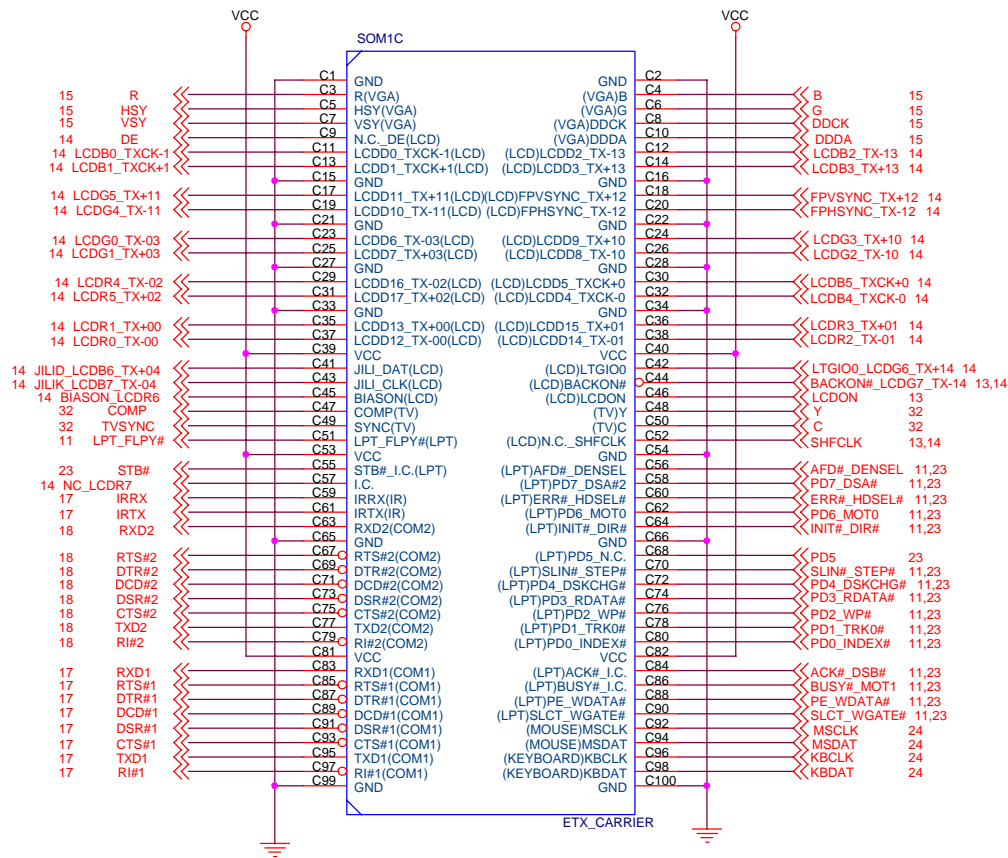
- PCICLK4 << PCICLK4 20
- PCIREQ#3 << PCIREQ#3 20
- PCIINT#3 << PCIINT#3 20
- PCICLK3 << PCICLK3 19
- PCIINT#2 << PCIINT#2 19
- PCIREQ#2 << PCIREQ#2 19

19,20 AD[0..31] << AD[0..31]

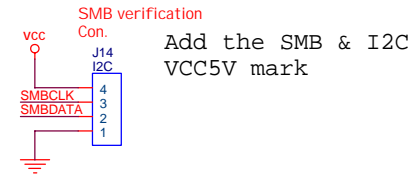
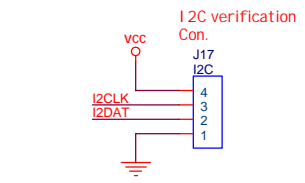
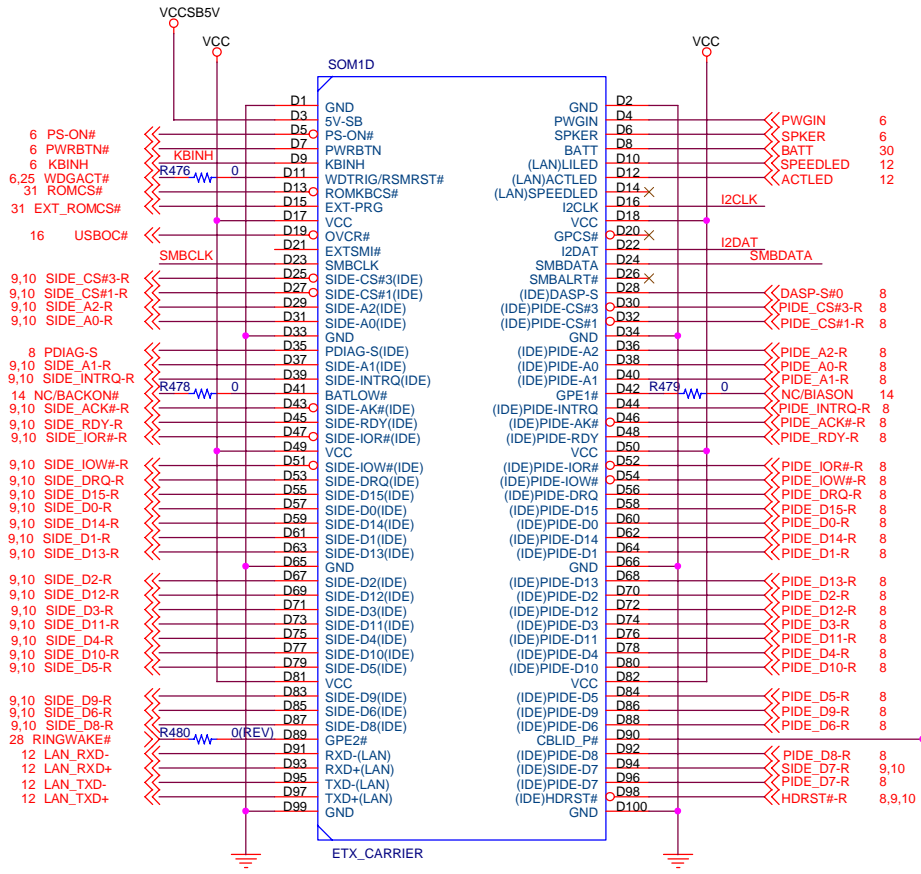


PCB1
1906440003

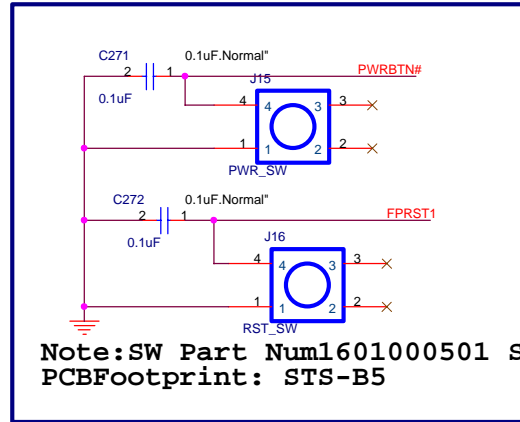
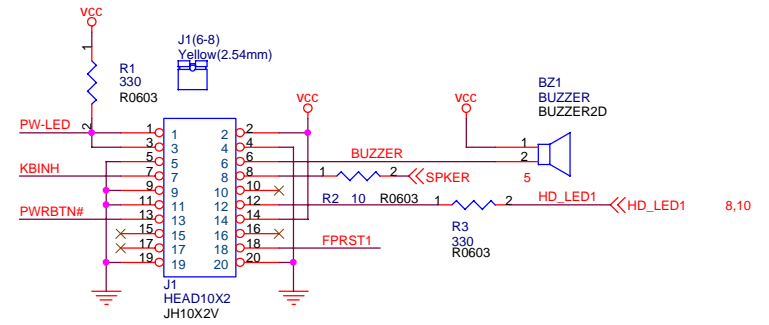
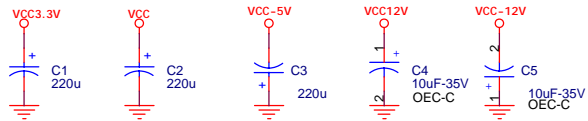
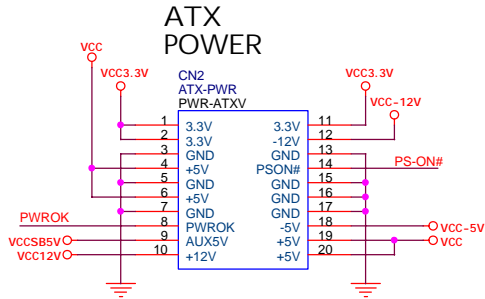
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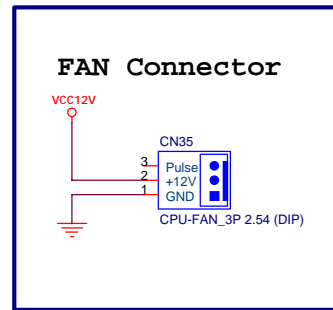
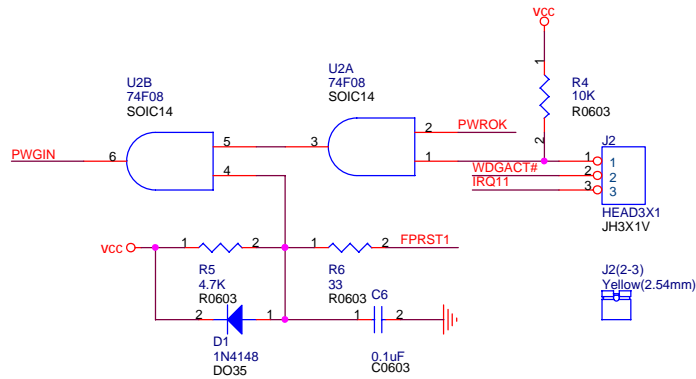
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Size	Document Number	Rev
	SOM-DB4400 A2 03-1	A2
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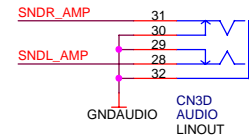
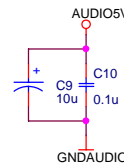
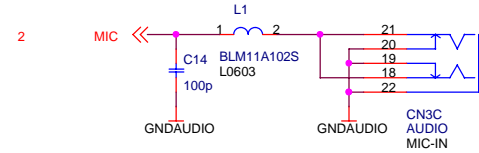
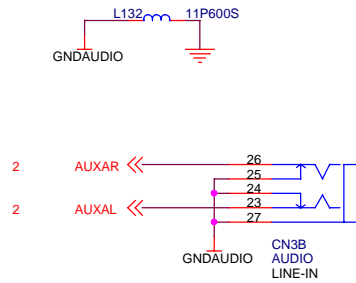
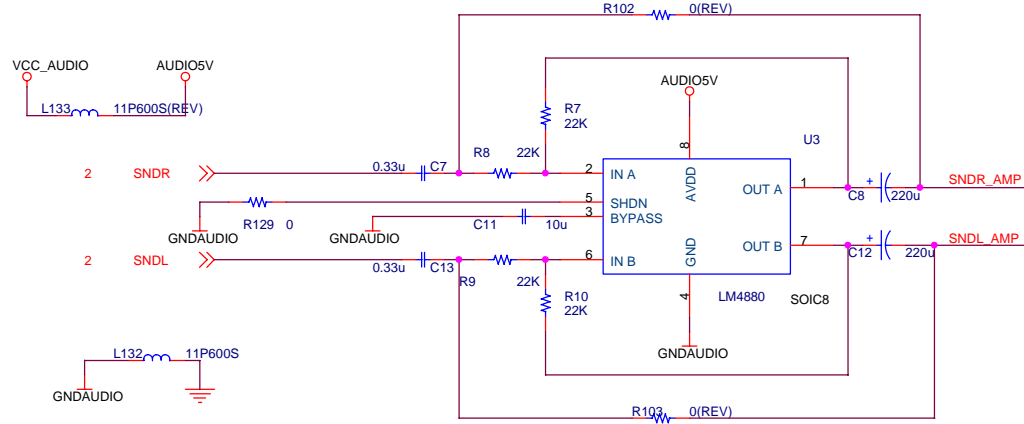
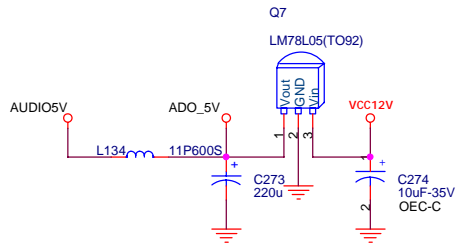
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SOM Carrier Connector X4		
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- LAYOUT NOTE:**
1. Add " + " at BZ1 on the PCB
 2. Add " FAN " at CN35
 3. Add " PWR_SW " at J15, " RST_SW " at J16



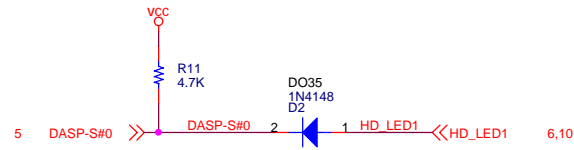
ADVANTECH		
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ATX Power Connector&FAN		
Size	Document Number	Rev
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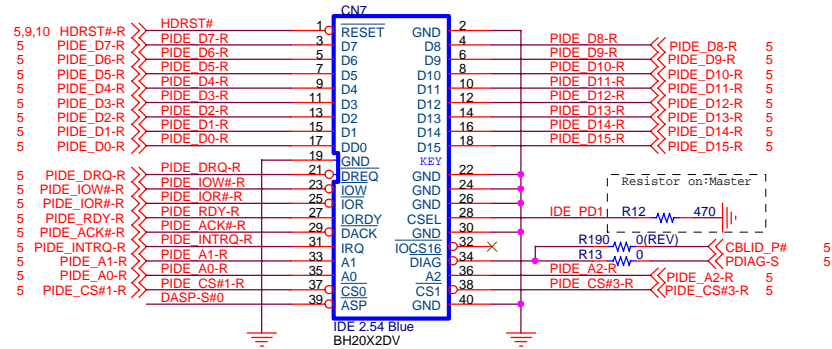
GNDAUDIO connect to GND via Hole

PN: 1652512202 AUDIO CONNECTOR 3IN1
 PN: 1654430100 Include GAME PORT
 PN: 1652505203 Pink, 1652505204 Blue, 1652505205 Green

ADVANTECH		
Title		
AC97 Composite Audio Connector		
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PRIMARY IDE Master/Slave

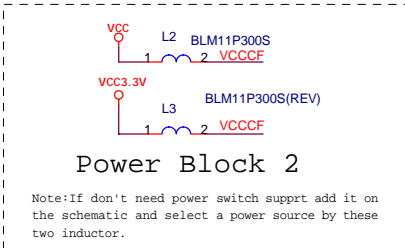
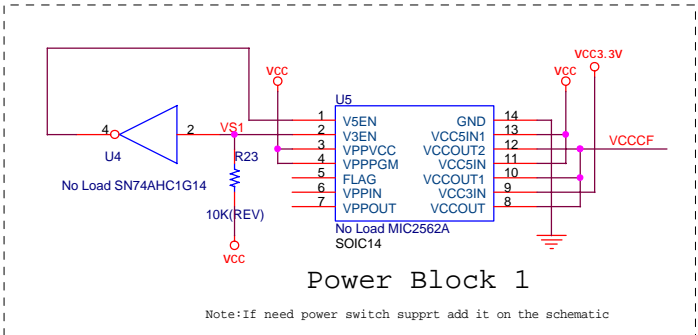
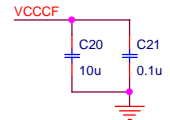
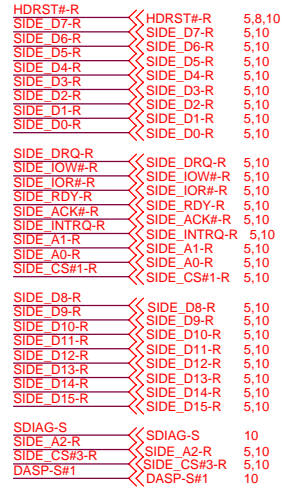
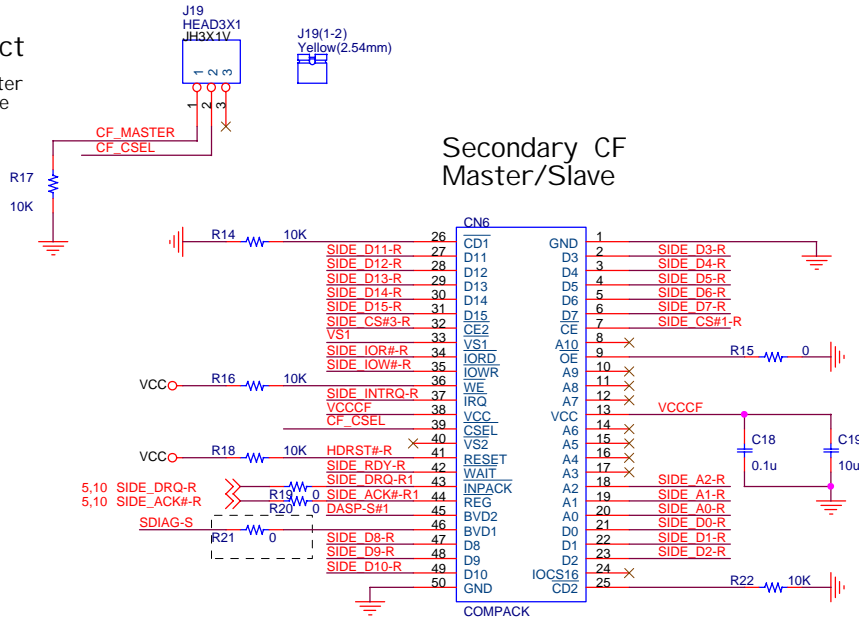


Note: PDIAG-S is for the on board master/slave handshaking signal. If need this function please add this resistor.

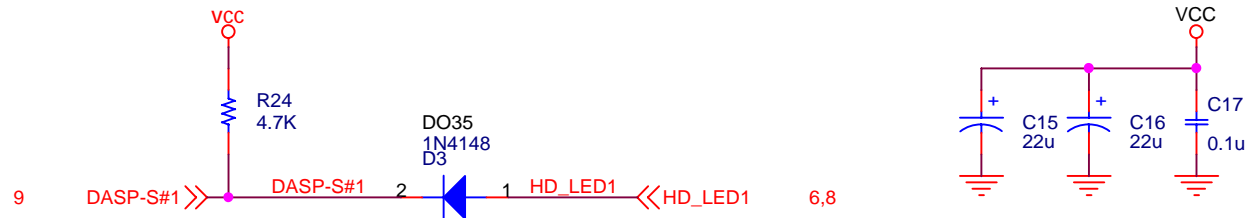
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Primary IDE 40 Pin Master/Slave		
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CF select

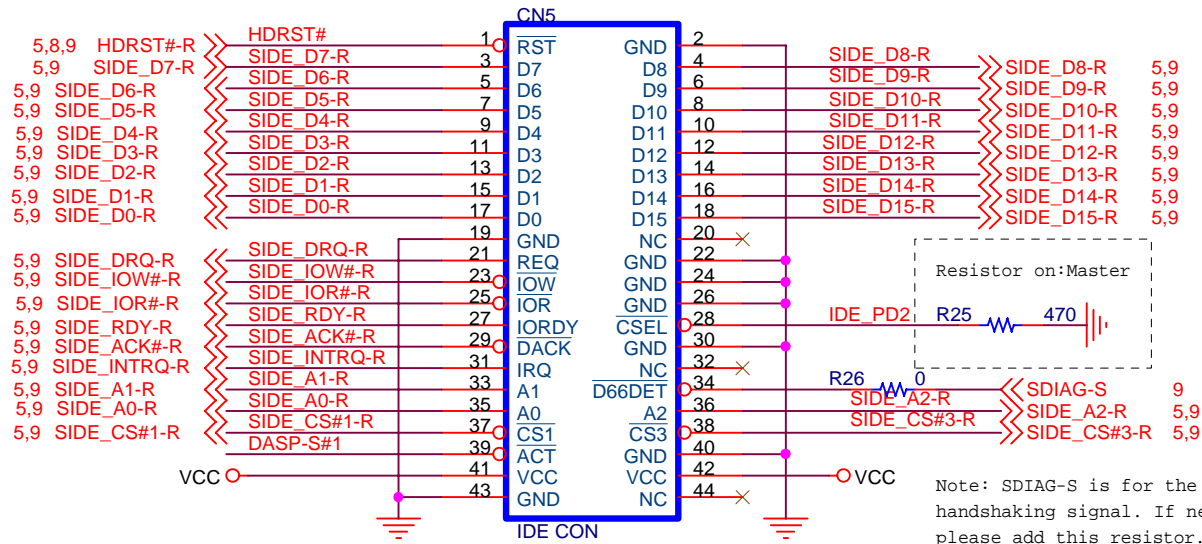
- 1-2 master
- 2-3 slave



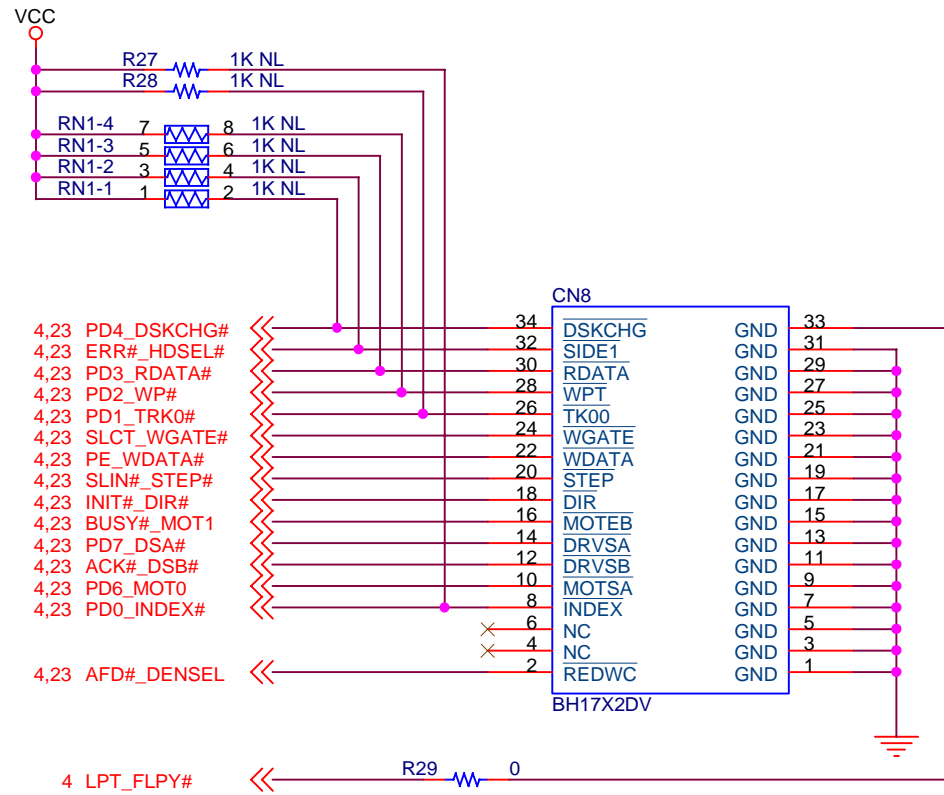
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Title Secondary IDE CF		
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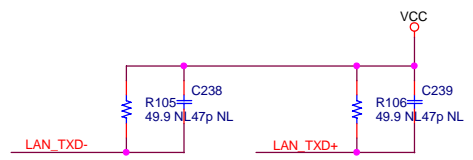
Secondary IDE Master/Slave



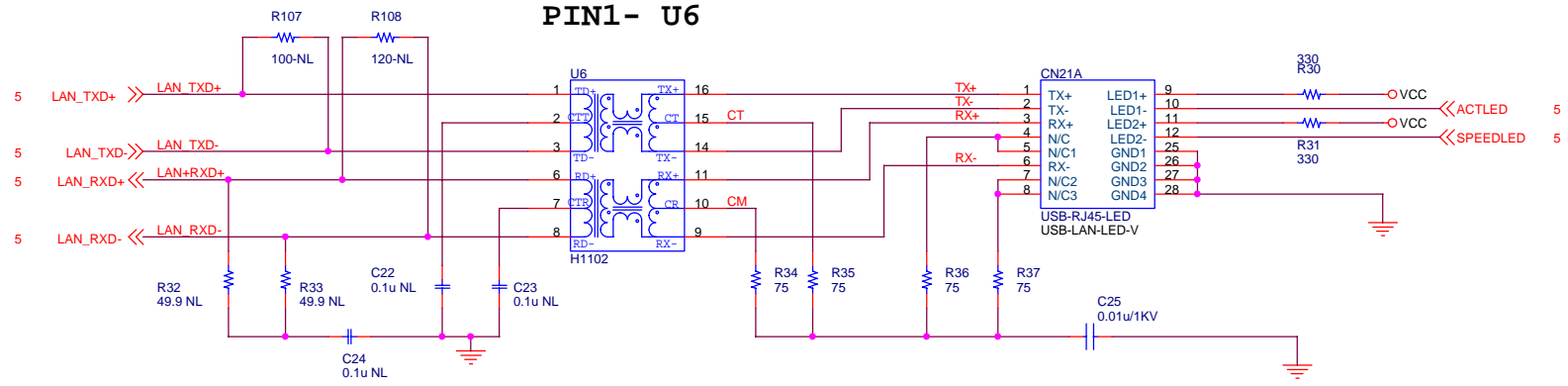
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Title		
Secondary IDE 44 Pin Master/Slave		
Size	Document Number	Rev
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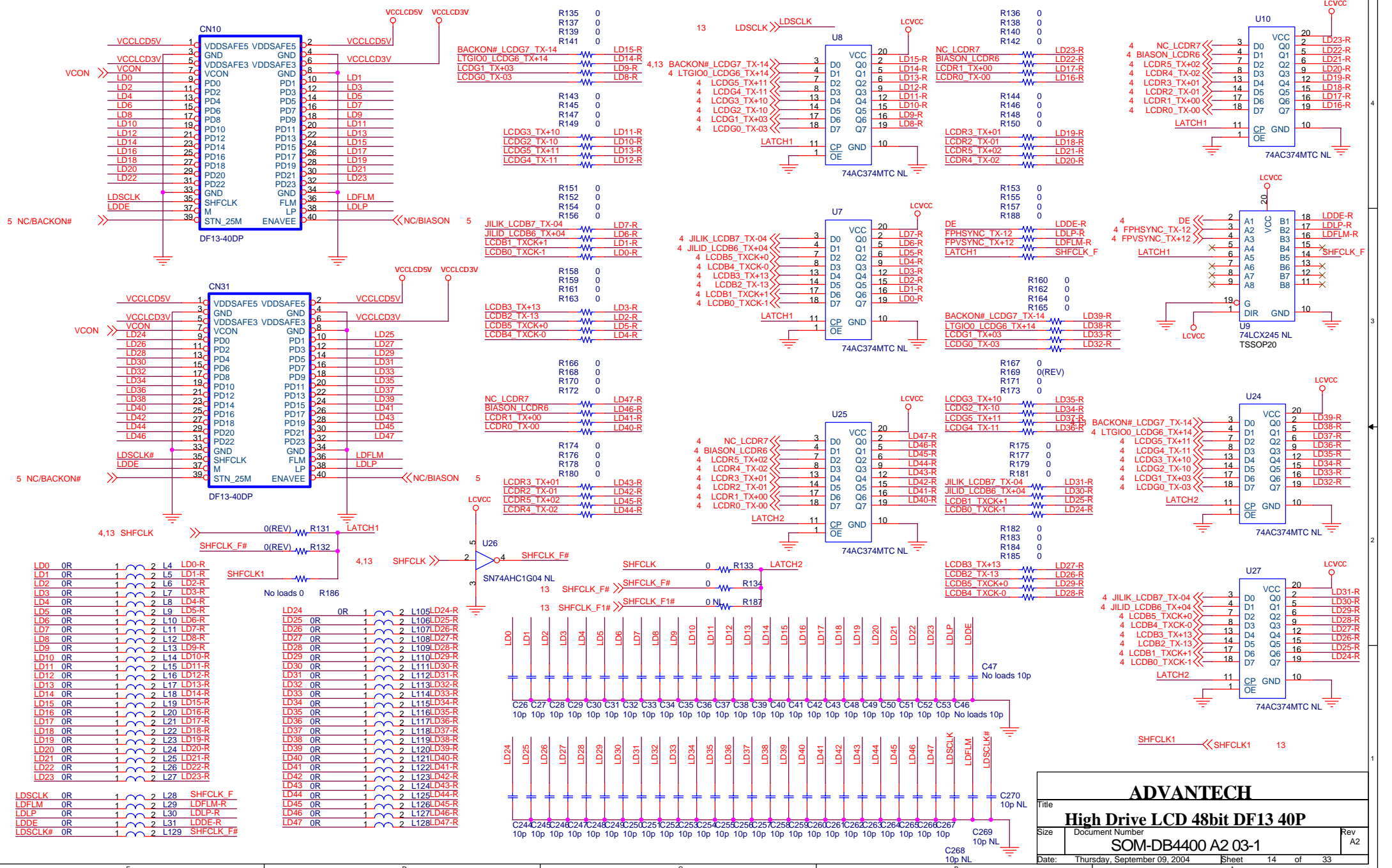
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Title FDD STD connector		
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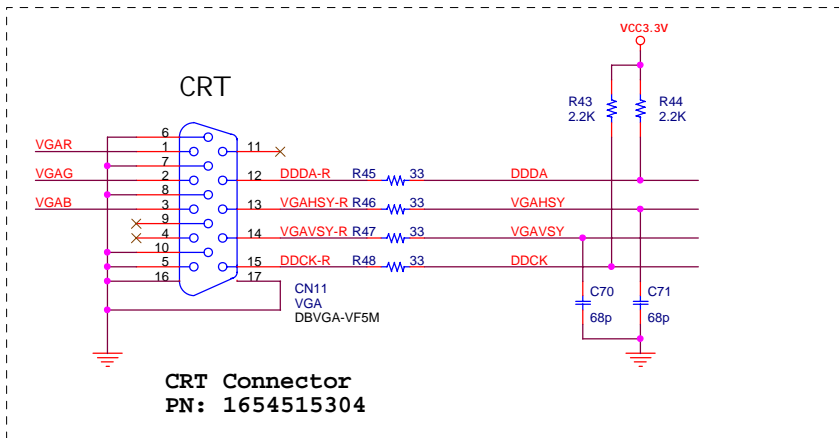
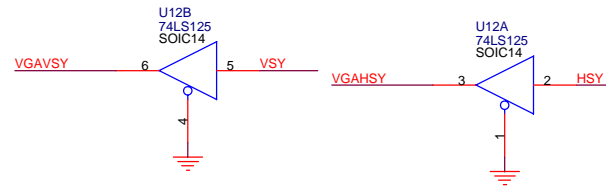
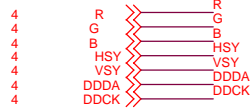
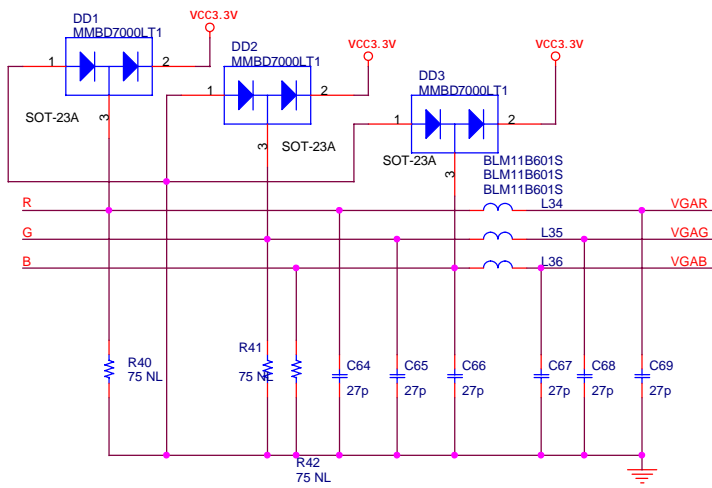
Please Add the
PIN1- U6



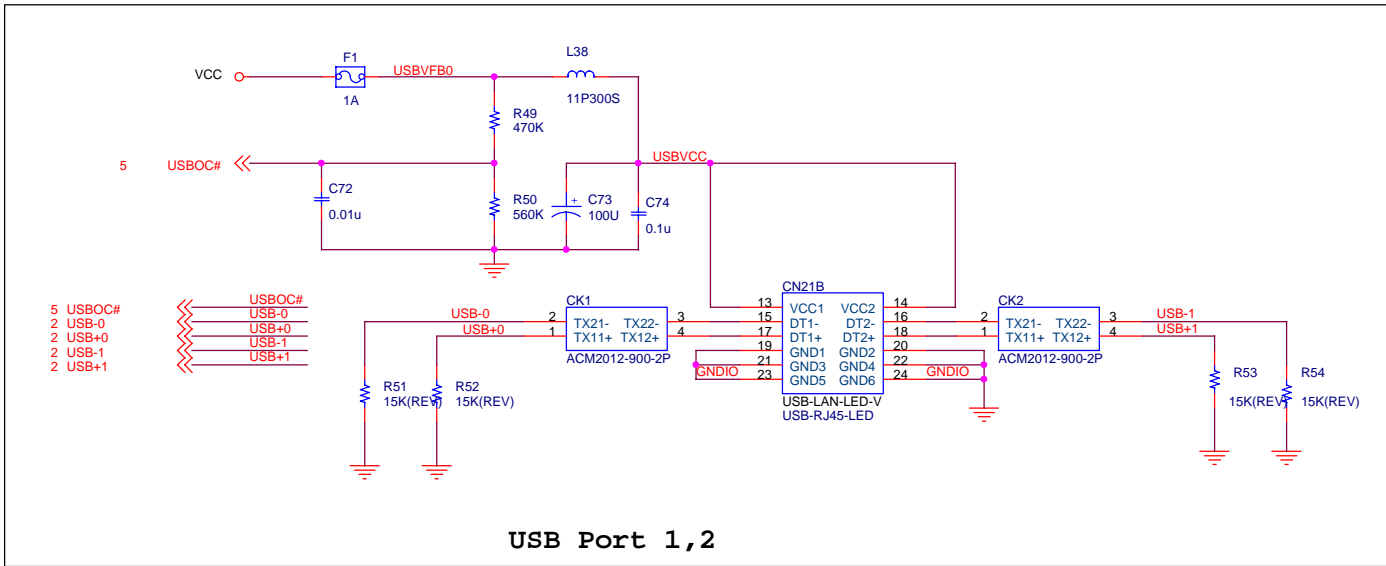
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LAN RJ45 Right Angle connector		
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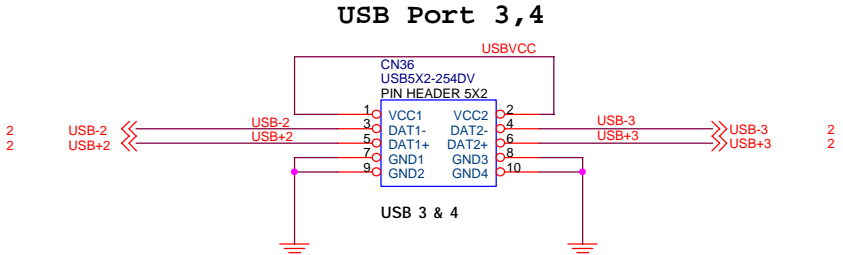
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High Drive LCD 48bit DF13 40P		
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ADVANTECH		
Title		
CRT DB15 right angle		
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USB Port 1,2

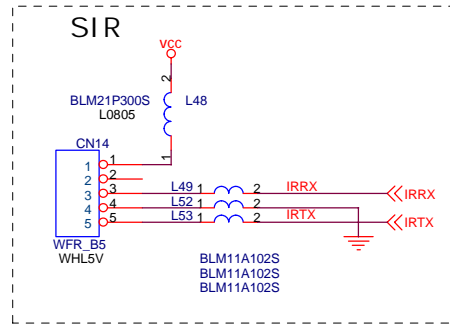
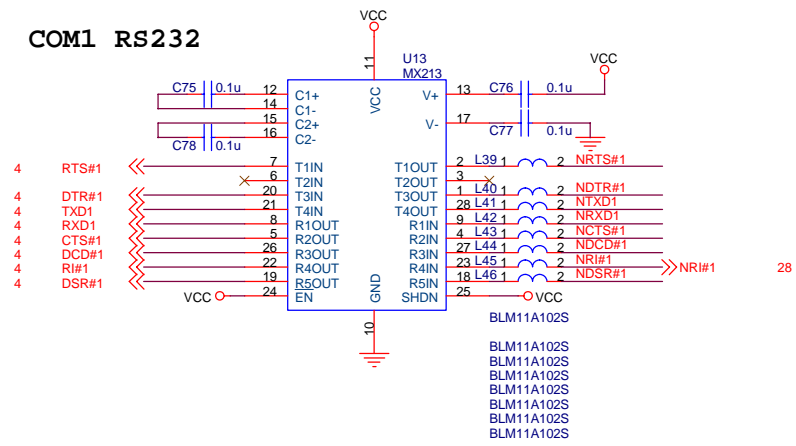


USB Port 3,4

USB 3 & 4

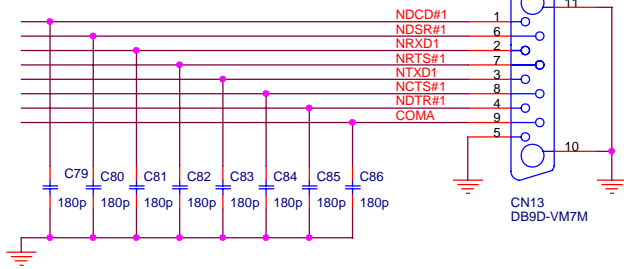
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Title		
USB Right Angle Connector		
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COM1 RS232



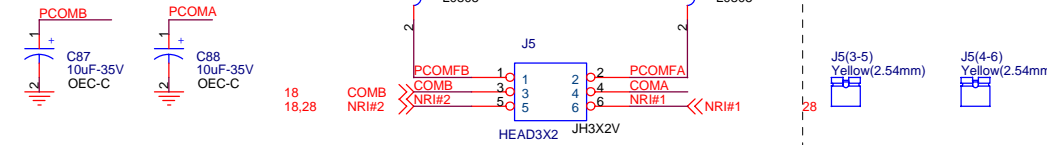
COM1

CN13: PN:1654409113 Footprint: DB9D-VM7M



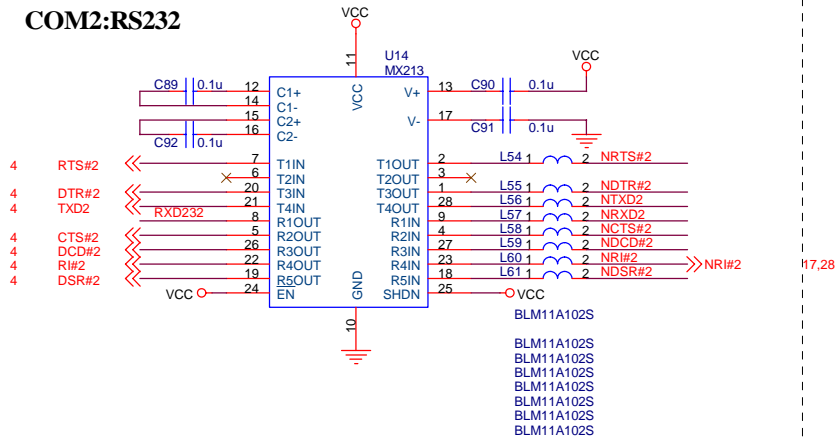
COM 1/COM 2 :

	J1	J2
RING	DON'T CARE	3-5/4-6
VCC	1-3/2-4	1-3/2-4
+12V	3-5/4-6	1-3/2-4

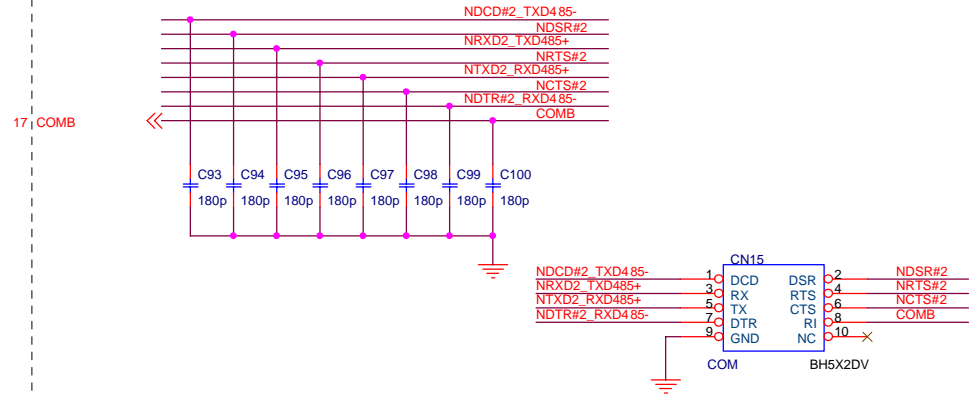


ADVANTECH		
COM1,2 DB25 RA & SIR		
Title		
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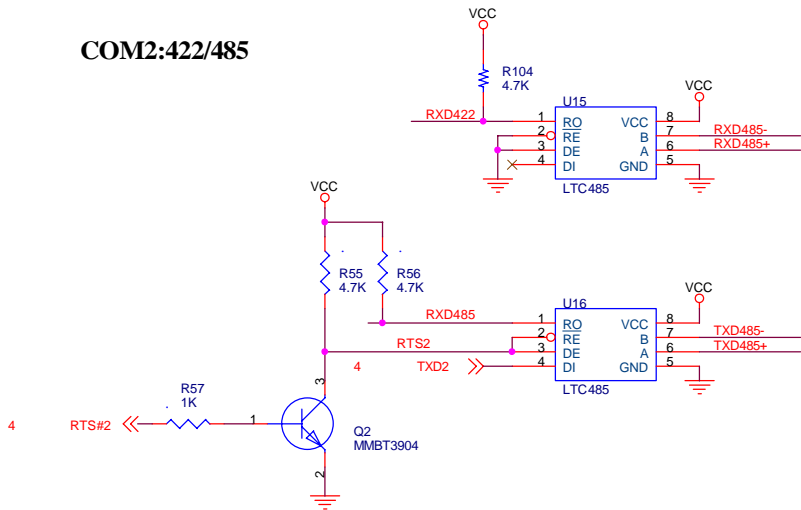
COM2:RS232



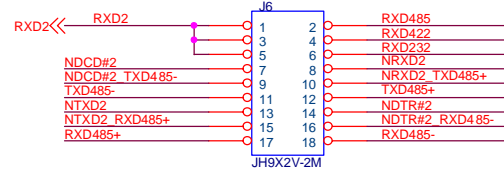
COM2 RS232/422/485



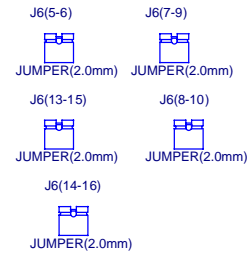
COM2:422/485



COM2 :RS232/422/485 SEL. JUMPER

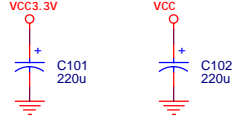


J6	RS232	RS422	RS485
5-6	3-4	1-2	
7-9	9-11	9-11	
8-10	10-12	10-12	
13-15	15-17	15-17	
14-16	16-18	16-18	

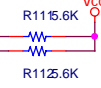
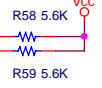
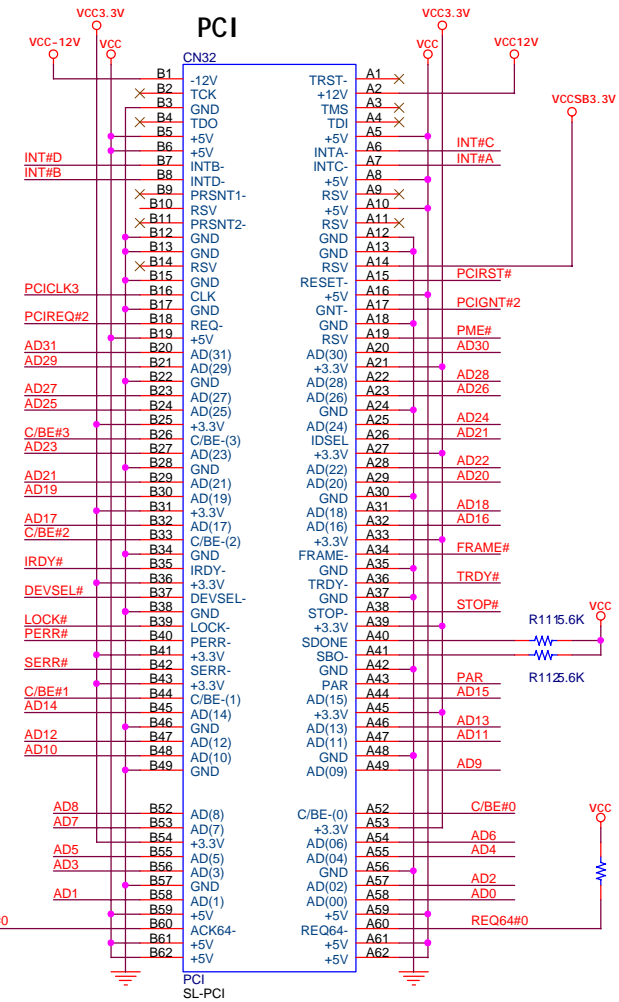
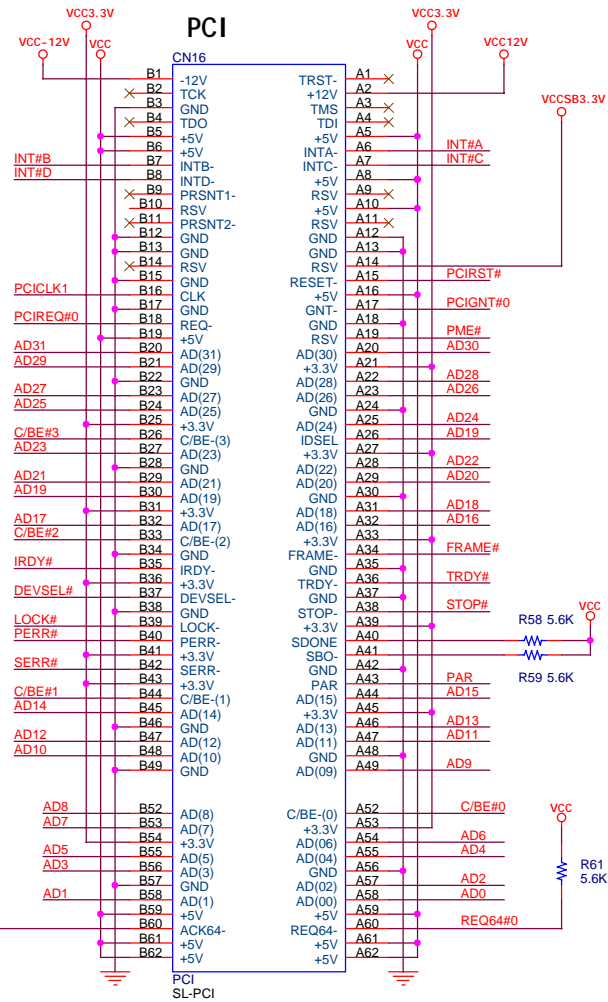
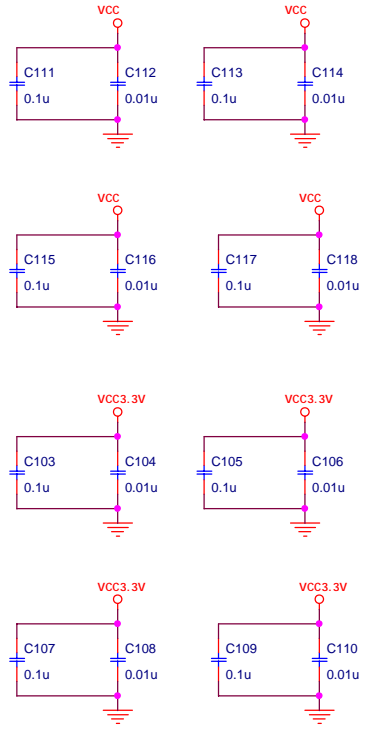


ADVANTECH

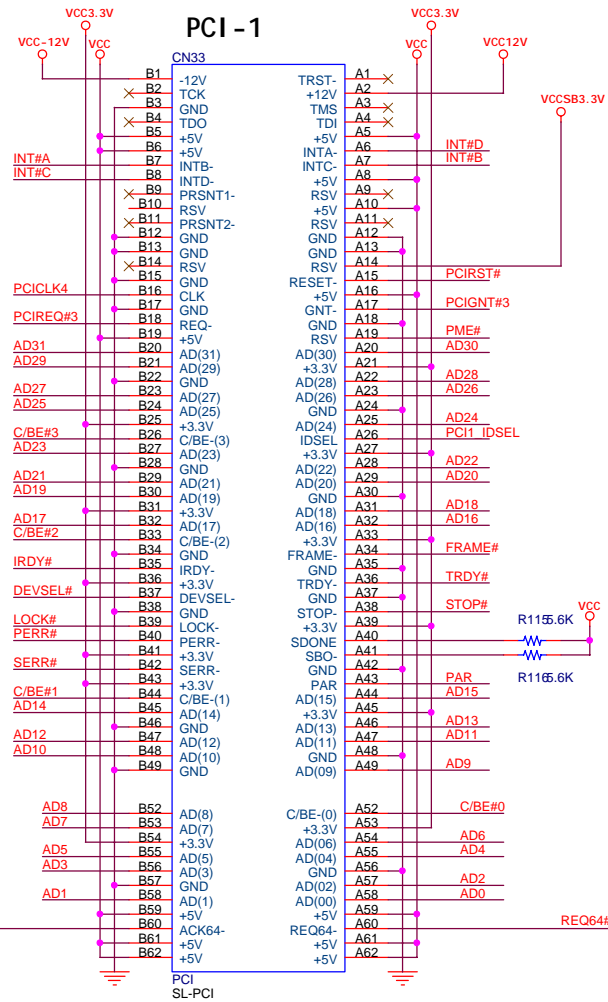
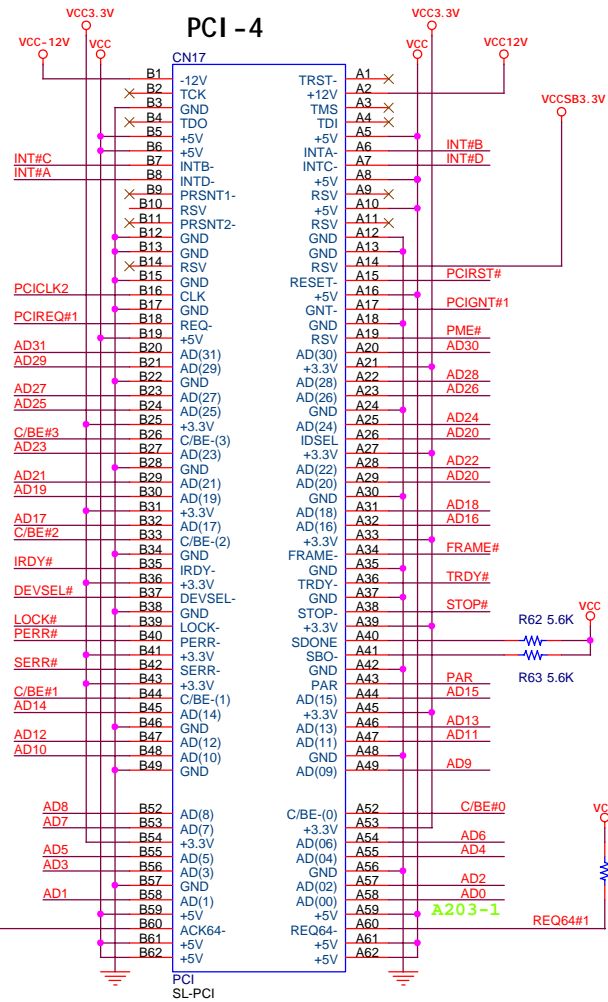
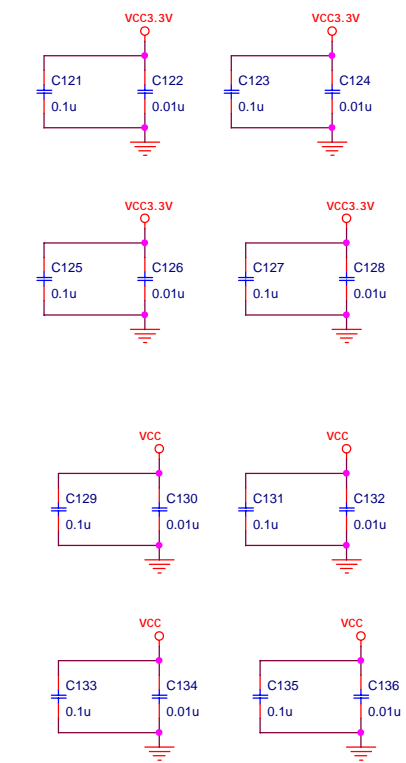
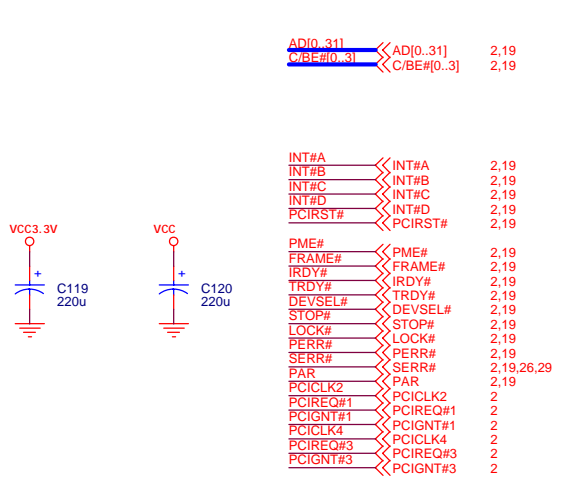
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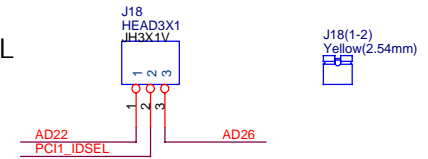
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INT#B	INT#B	2,20
INT#C	INT#C	2,20
INT#D	INT#D	2,20
PCIRST#	PCIRST#	2,20
PME#	PME#	2,20
FRAME#	FRAME#	2,20
IRDY#	IRDY#	2,20
TRDY#	TRDY#	2,20
DEVSEL#	DEVSEL#	2,20
STOP#	STOP#	2,20
LOCK#	LOCK#	2,20
PERR#	PERR#	2,20
SERR#	SERR#	2,20,26,29
PAR	PAR	2,20
PCICLK1	PCICLK1	2
PCIGNT#0	PCIGNT#0	2
PCIREQ#0	PCIREQ#0	2
PCICLK3	PCICLK3	2
PCIGNT#2	PCIGNT#2	2
PCIREQ#2	PCIREQ#2	2



ADVANTECH		
PCI Slot 0,2		
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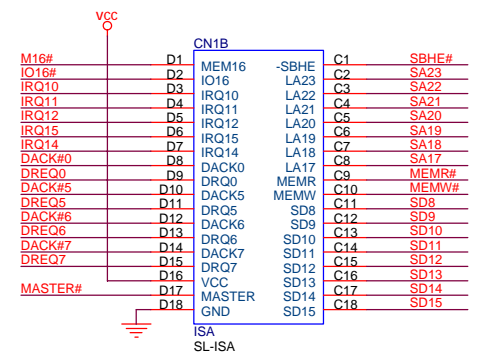
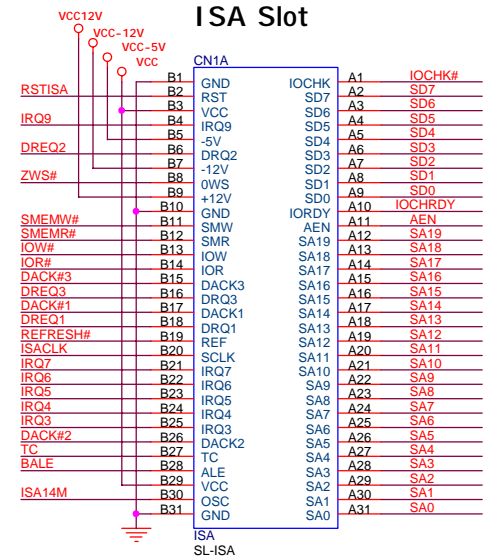
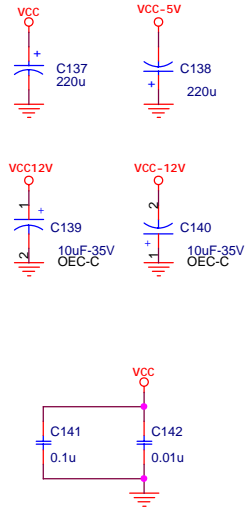


CN33 IDSEL
 1-2 AD22
 2-3 AD26



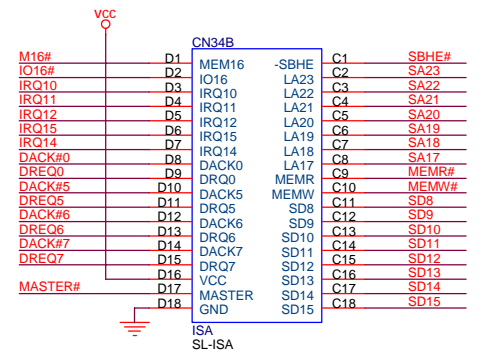
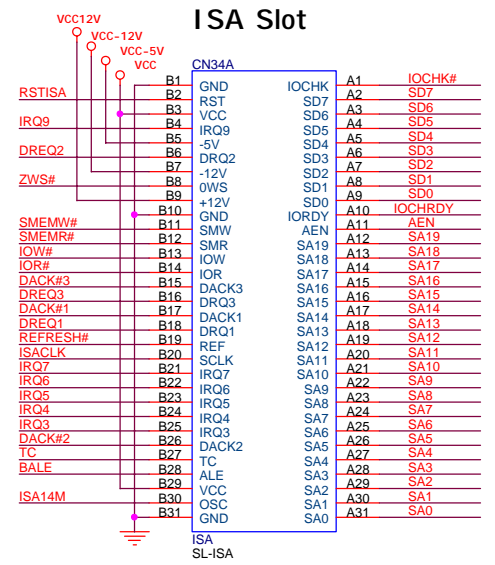
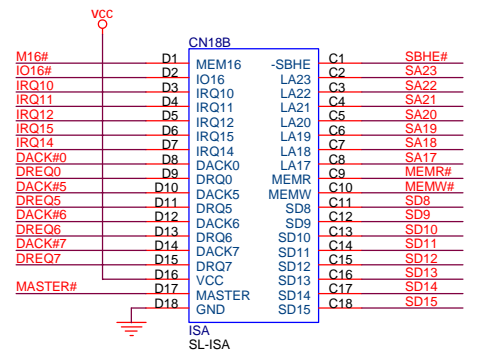
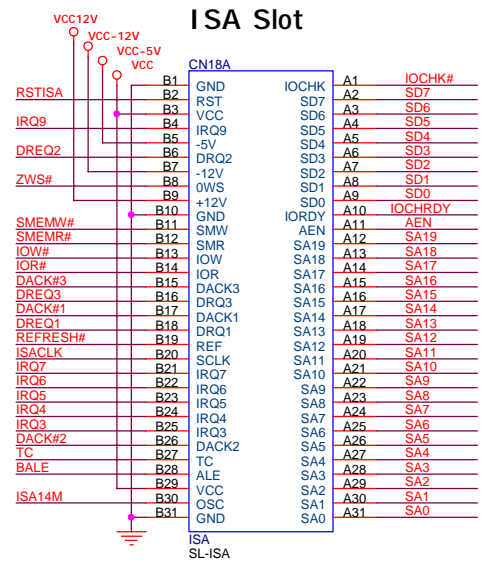
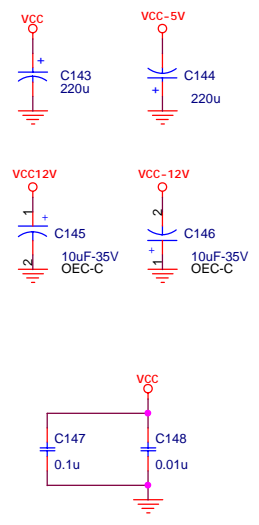
ADVANTECH		
PCI Slot 1,3		
Title	Document Number	
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IRQ3	<< IRQ3	3,22,26
IRQ4	<< IRQ4	3,22,26
IRQ5	<< IRQ5	3,22,26
IRQ6	<< IRQ6	3,22,26
IRQ7	<< IRQ7	3,22,26
IRQ9	<< IRQ9	3,22,26
IRQ10	<< IRQ10	3,22,26
IRQ11	<< IRQ11	3,6,22,26
IRQ12	<< IRQ12	3,22
IRQ14	<< IRQ14	3,22,26
IRQ15	<< IRQ15	3,22
DREQ0	<< DREQ0	3,22
DREQ1	<< DREQ1	3,22,26
DREQ2	<< DREQ2	3,22,26
DREQ3	<< DREQ3	3,22,26
DREQ5	<< DREQ5	3,22
DREQ6	<< DREQ6	3,22
DREQ7	<< DREQ7	3,22
DACK#0	<< DACK#0	3,22
DACK#1	<< DACK#1	3,22,26
DACK#2	<< DACK#2	3,22,26
DACK#3	<< DACK#3	3,22,26
DACK#5	<< DACK#5	3,22
DACK#6	<< DACK#6	3,22
DACK#7	<< DACK#7	3,22
IOCHK#	<< IOCHK#	3,22
IOCHRDY	<< IOCHRDY	3,22,26
AEN	<< AEN	3,22,25,26
SD[0..15]	<< SD[0..15]	3,22,25,26,31
SA[0..23]	<< SA[0..23]	3,22,25,26,31
SBHE#	<< SBHE#	3,22
MEMR#	<< MEMR#	3,22,31
MEMW#	<< MEMW#	3,22,31
M16#	<< M16#	3,22
IO16#	<< IO16#	3,22
MASTER#	<< MASTER#	3,22
RSTISA	<< RSTISA	3,22,25,26
ZWS#	<< ZWS#	3,22,25,26
SMEMR#	<< SMEMR#	3,22,25
SMEMR#	<< SMEMR#	3,22,25
IOW#	<< IOW#	3,22,25,26
IOW#	<< IOW#	3,22,25,26
REFRESH#	<< REFRESH#	3,22
ISACK	<< ISACK	3,22
TC	<< TC	3,22,26
BALE	<< BALE	3,22
ISA14M	<< ISA14M	3,22,25,26

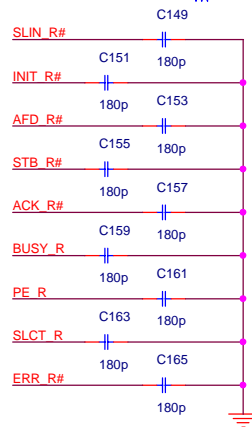
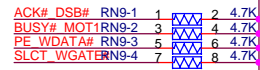
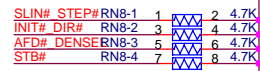
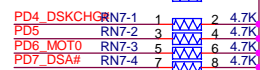
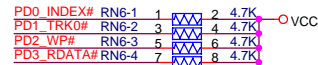
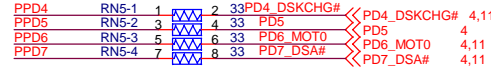
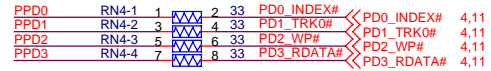
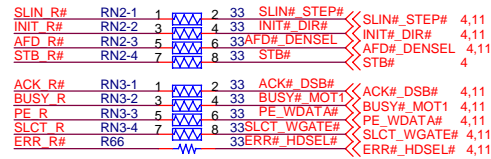
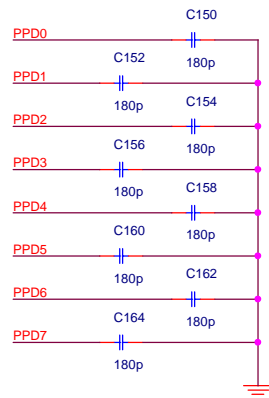
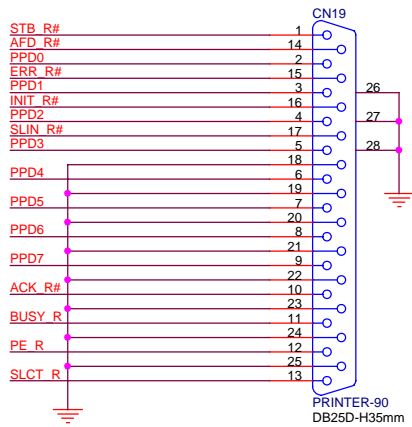


ADVANTECH		
Title ISA Slot		
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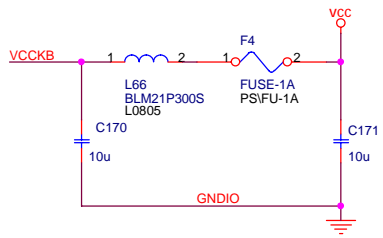
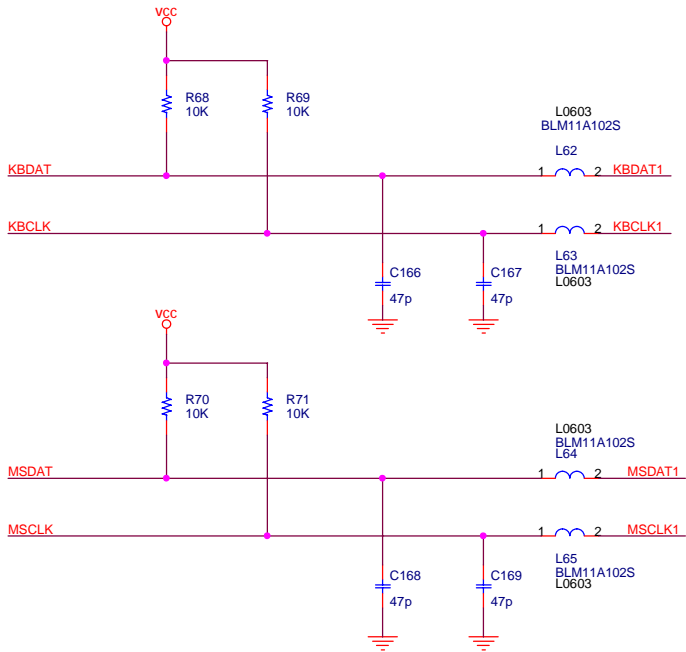
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IRQ4	<>	IRQ4	3,21,26
IRQ5	<>	IRQ5	3,21,26
IRQ6	<>	IRQ6	3,21,26
IRQ7	<>	IRQ7	3,21,26
IRQ9	<>	IRQ9	3,21,26
IRQ10	<>	IRQ10	3,21,26
IRQ11	<>	IRQ11	3,21,26
IRQ12	<>	IRQ12	3,6,21,26
IRQ14	<>	IRQ14	3,21
IRQ15	<>	IRQ15	3,21
DREQ0	<>	DREQ0	3,21
DREQ1	<>	DREQ1	3,21,26
DREQ2	<>	DREQ2	3,21,26
DREQ3	<>	DREQ3	3,21,26
DREQ5	<>	DREQ5	3,21
DREQ6	<>	DREQ6	3,21
DREQ7	<>	DREQ7	3,21
DACK#0	<>	DACK#0	3,21
DACK#1	<>	DACK#1	3,21,26
DACK#2	<>	DACK#2	3,21,26
DACK#3	<>	DACK#3	3,21,26
DACK#5	<>	DACK#5	3,21
DACK#6	<>	DACK#6	3,21
DACK#7	<>	DACK#7	3,21
IOCHK#	<>	IOCHK#	3,21
IOCHRDY	<>	IOCHRDY	3,21,26
AEN	<>	AEN	3,21,25,26
SDI0..15]	<>	SDI0..15]	3,21,25,26,31
SAI0..23]	<>	SAI0..23]	3,21,25,26,31
SBHE#	<>	SBHE#	3,21
MEMR#	<>	MEMR#	3,21,31
MEMW#	<>	MEMW#	3,21,31
M16#	<>	M16#	3,21
IO16#	<>	IO16#	3,21
MASTER#	<>	MASTER#	3,21
RSTISA	<>	RSTISA	3,21
ZWS#	<>	ZWS#	3,21,25,26
SMEMW#	<>	ZWSH	3,21
SMEMR#	<>	SMEMW#	3,21,25
IOW#	<>	SMEMR#	3,21,25
IOR#	<>	IOW#	3,21,25,26
REFRESH#	<>	IOR#	3,21,25,26
ISACLK	<>	REFRESH#	3,21
TC	<>	ISACLK	3,21
BALE	<>	TC	3,21,26
ISA14M	<>	BALE	3,21
	<>	ISA14M	3,21,25,26



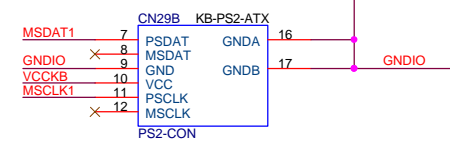
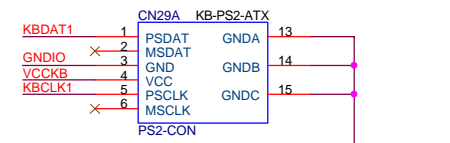
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Title Parallel Port DB25 High 90 conn		
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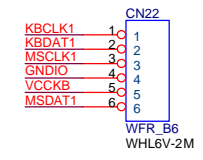


KBD/MS PS/2



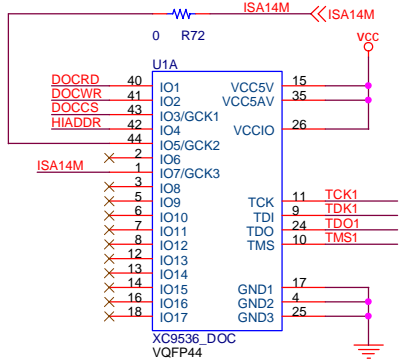
KBD/MS PS/2

KBD/MS CONN 2.0mm

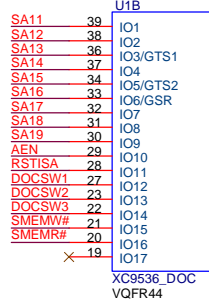


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Title		
PS2 MINIDIN6&6PIN WAFER		
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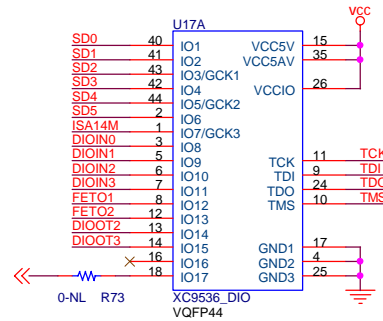
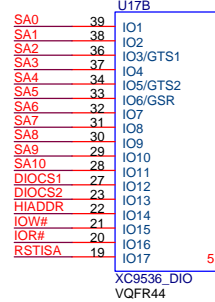
DOC2000



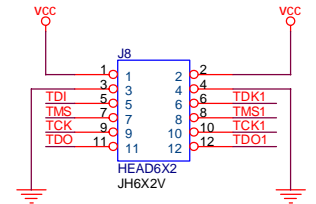
3,21,22,26



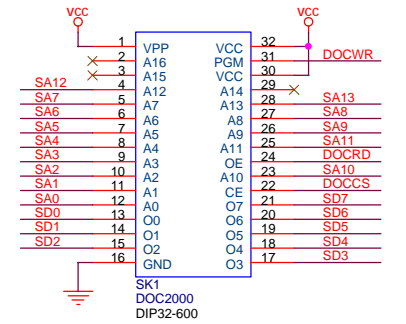
WDT/DIO



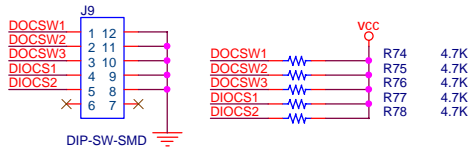
Note:IF need watch dog function support on the SOM module please set resistor on.



DOC2000 SOCKET



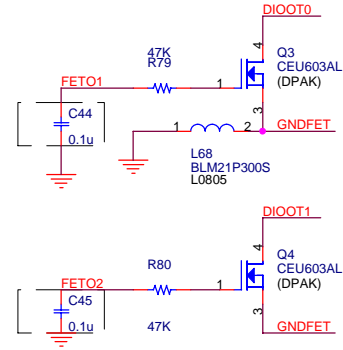
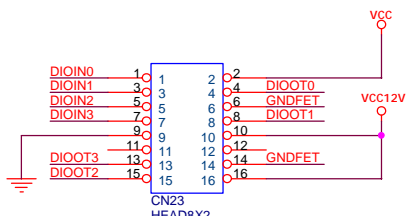
DOC2000/DIO ADDRESS SELECT



J1	DOC2000
1-2/3-4/5-6	C800
3-4/5-6	CA00
1-2/5-6	CC00
5-6	CE00
1-2/3-4	D000
3-4	D200
1-2	D400
NC	D600*
DIO	
NC	200
7-8	210
9-10	220
7-8/9-10	230*

Setting DOC2000 Seg. and DIO address
 -HAADDR ==> SA19 - SA11 = 0H
 DOCS ==> SA19 - SA12
 C8000 D0000
 CA000 D2000
 CC000 D4000
 CE000 D6000

Digital I/O Connector

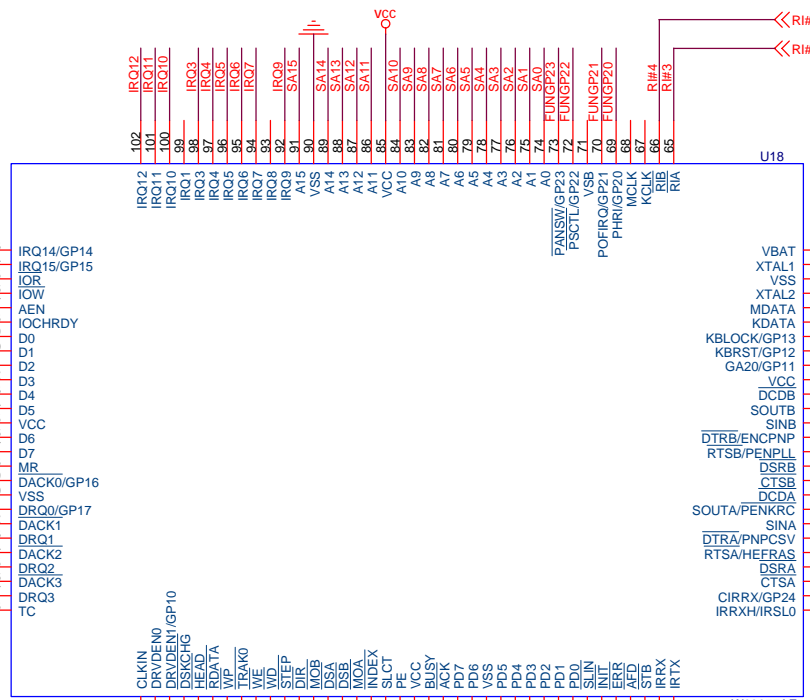


2002/5/10
 Add. C44 and C45

ISA14M	ISA14M	3,21,22,26
SMEMW#	SMEMW#	3,21,22
SMEMR#	SMEMR#	3,21,22
IOW#	IOW#	3,21,22,26
IOR#	IOR#	3,21,22,26
AEN	AEN	3,21,22,26
RSTISA	RSTISA	3,21,22,26

SA[0..23]	SA[0..23]	3,21,22,26,31
SD[0..15]	SD[0..15]	3,21,22,26,31

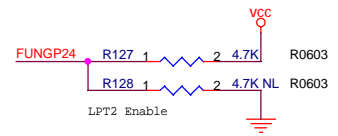
ADVANTECH		
WDG & DIO & DOC2000		
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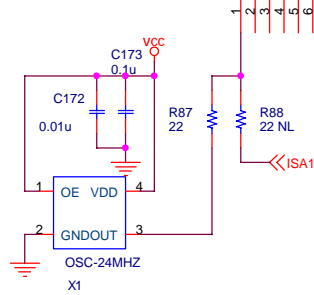
3,21,22,25 IOR#
 3,21,22,25 IOW#
 3,21,22,25 AEN
 3,21,22 IOCHRDY

3,21,22,25 RSTISA
 3,21,22 DACK#1
 3,21,22 DREQ1

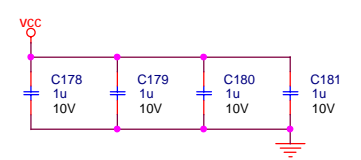
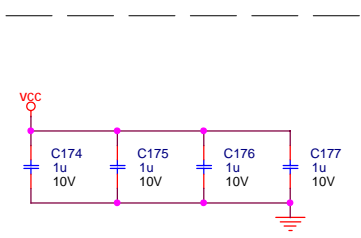
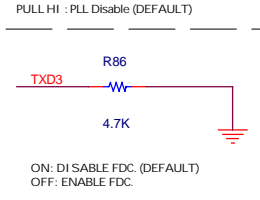
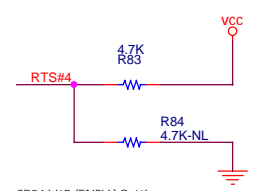
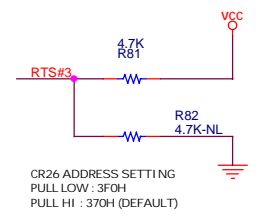
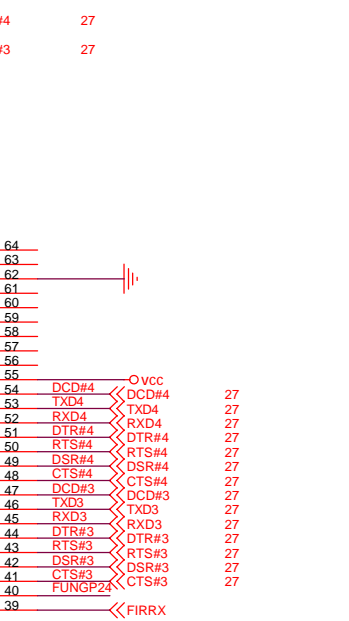
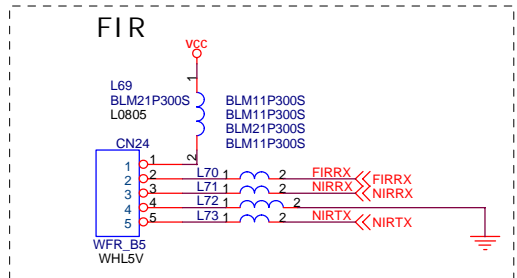
3,21,22 DREQ2
 3,21,22 DACK#3
 3,21,22 DREQ3
 3,21,22 TC



	Pull Hi	Pull Low
FUNG20	COM3 Enable	COM3 Disable
FUNG21	COM4 Enable	COM4 Disable
FUNG22	IR Enable	IR Disable
FUNG23	FDD Enable	FDD Disable
FUNG24	LPT2 Enable	LPT2 Disable



SDI0..15I	SDI0..15I	3,21,22,25,31
SAI0..23I	SAI0..23I	3,21,22,25,31
SPDI0..7I	SPDI0..7I	29
IRQ3	IRQ3	3,21,22
IRQ4	IRQ4	3,21,22
IRQ5	IRQ5	3,21,22
IRQ6	IRQ6	3,21,22
IRQ7	IRQ7	3,21,22
IRQ9	IRQ9	3,21,22
IRQ10	IRQ10	3,21,22
IRQ11	IRQ11	3,6,21,22
IRQ12	IRQ12	3,21,22

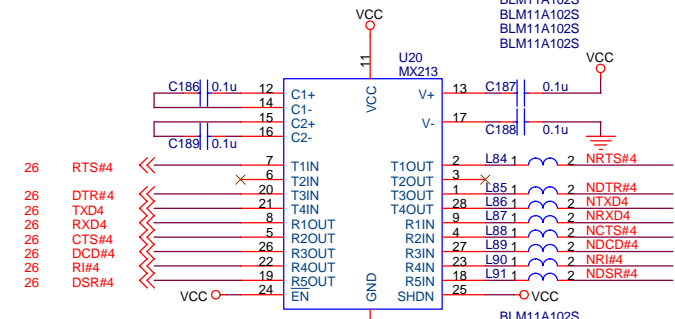
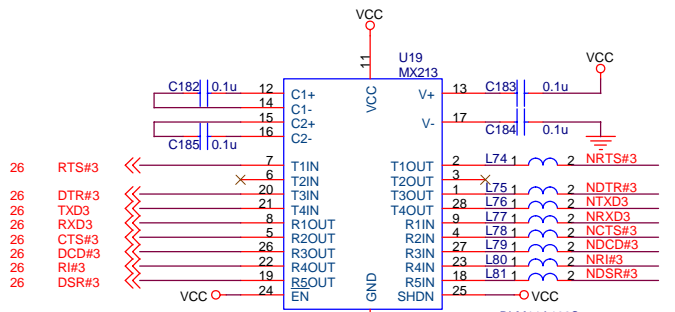


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Second Super I/O & FIR

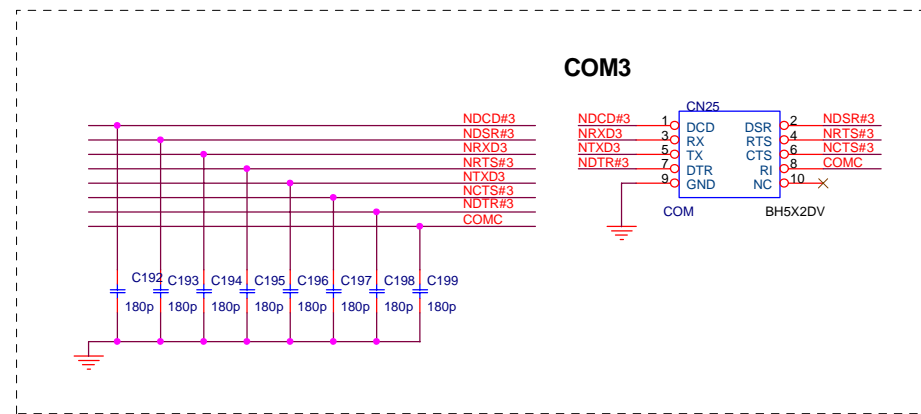
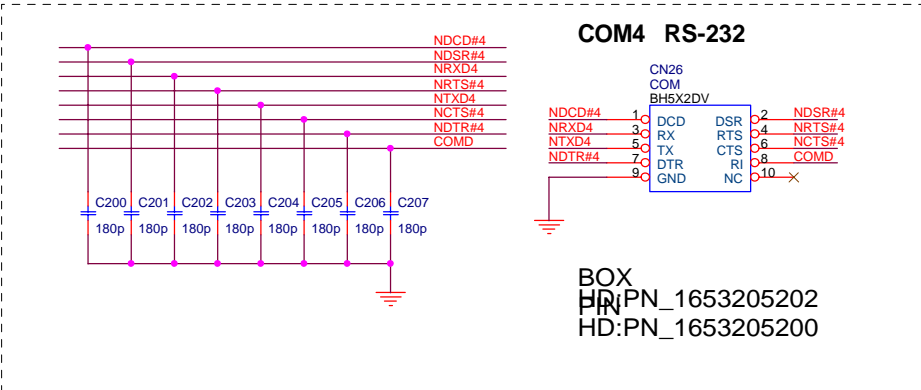
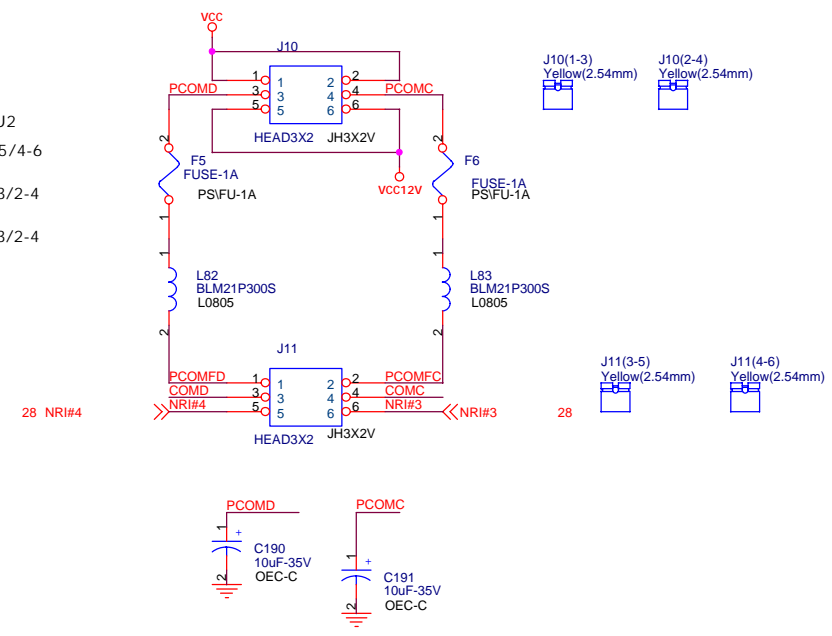
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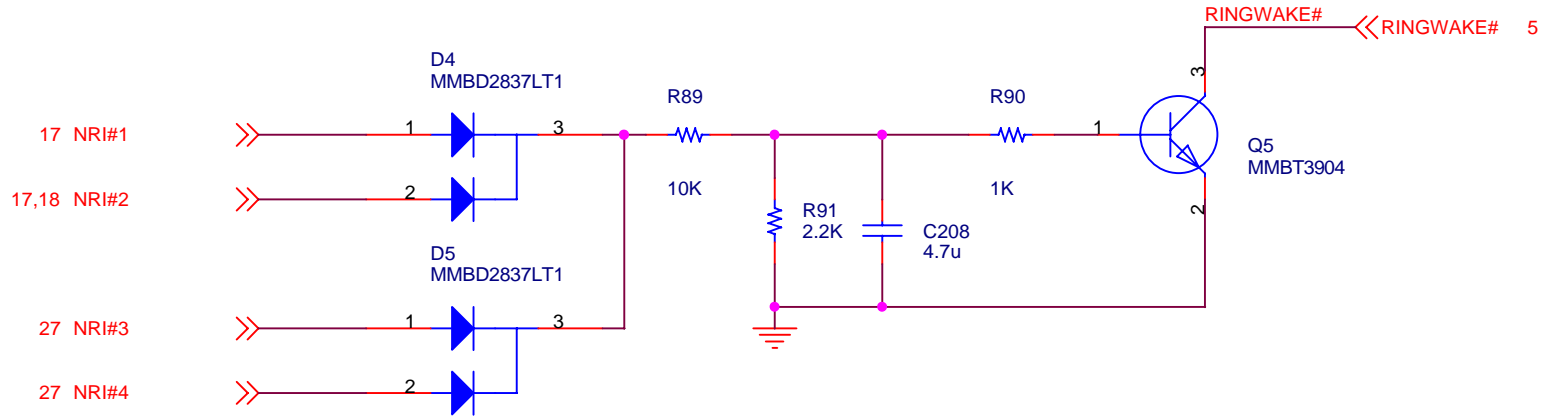
COM 3/COM 4 :

J1	J2
RING	DON'T CARE
VCC	1-3/2-4
+12V	3-5/4-6



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COM3,4 BOX Header		
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Wake On Modem



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Title

Wake On Modem

Size Document Number

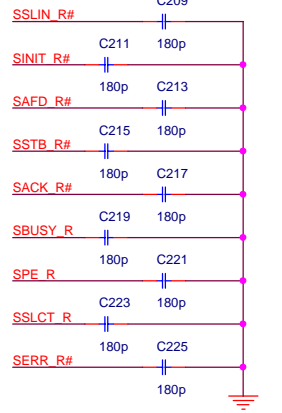
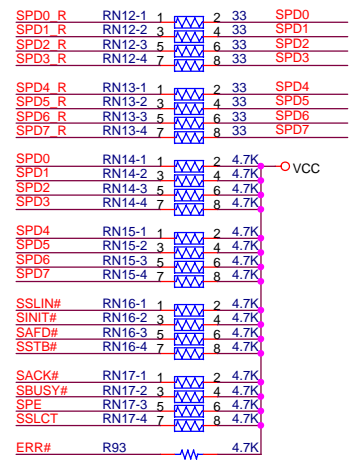
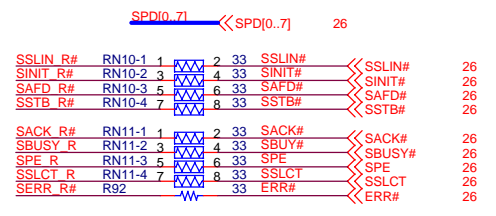
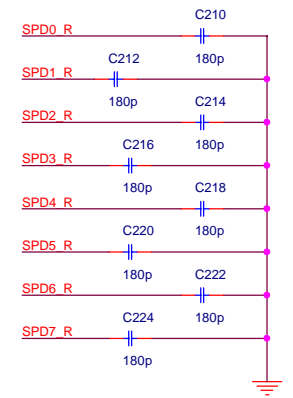
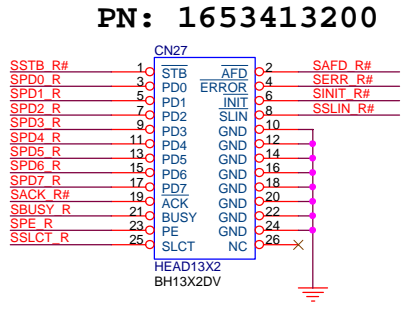
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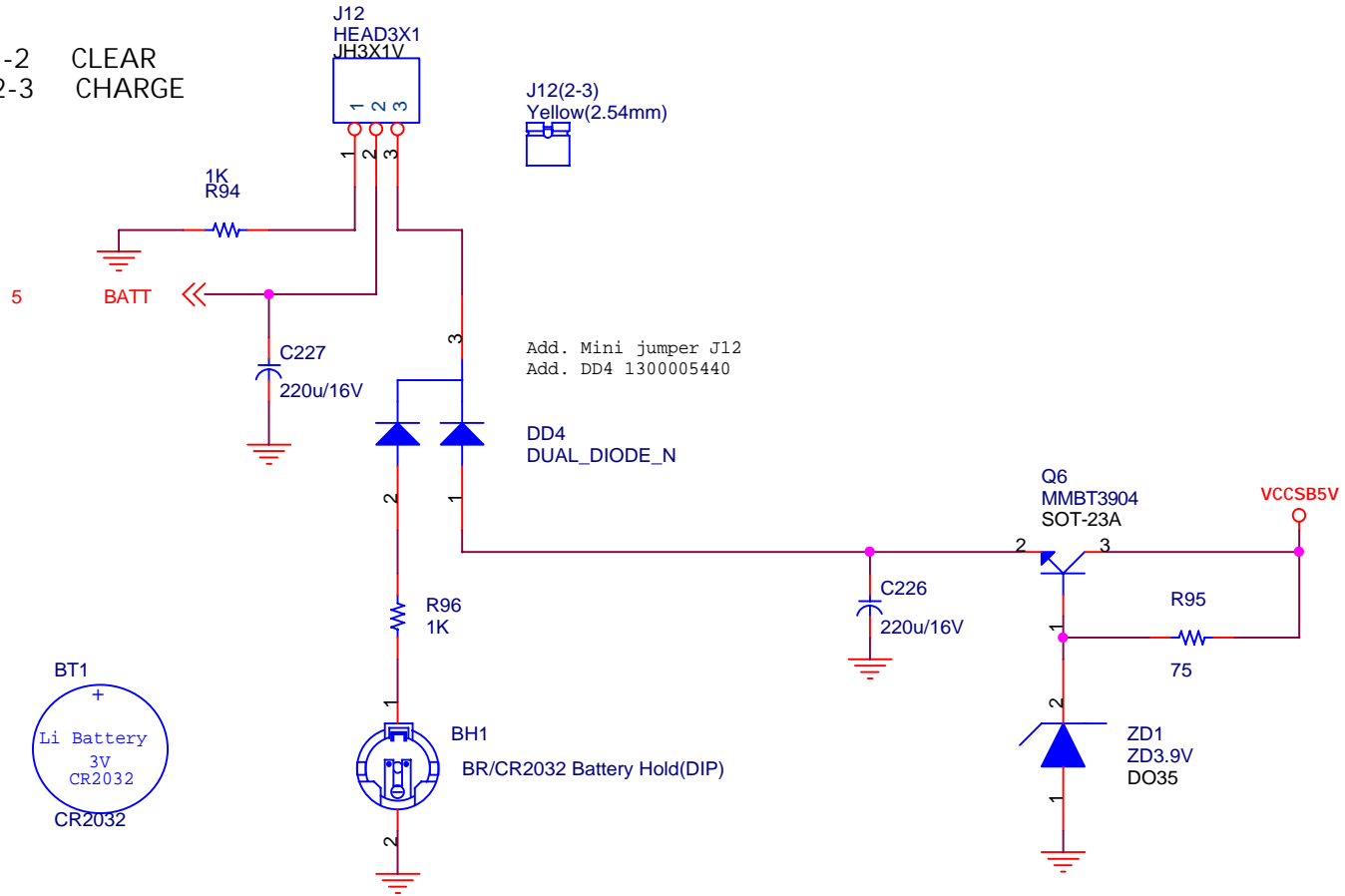
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Second Parallel Port BOX Header		
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RTC POWER

1-2 CLEAR
2-3 CHARGE



ADVANTECH

Title

RTC Power CR2032

Size Document Number

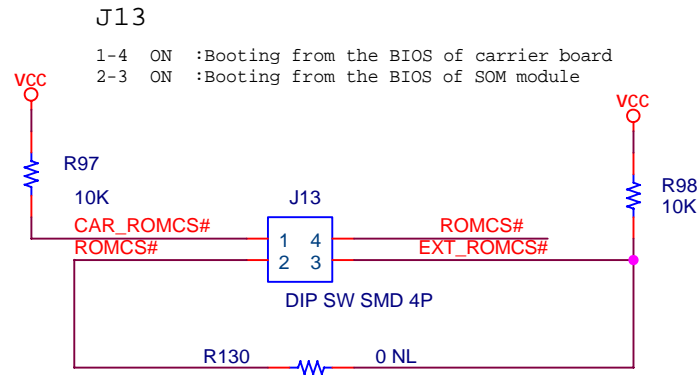
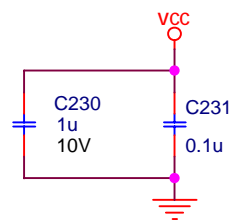
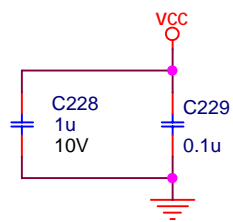
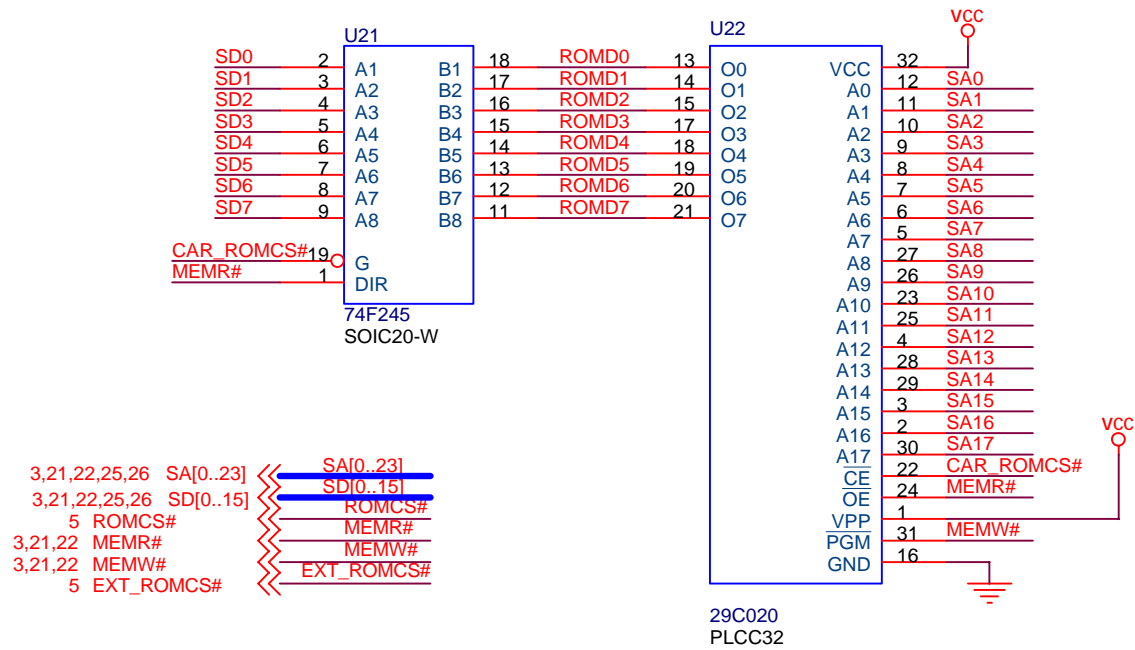
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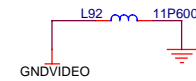
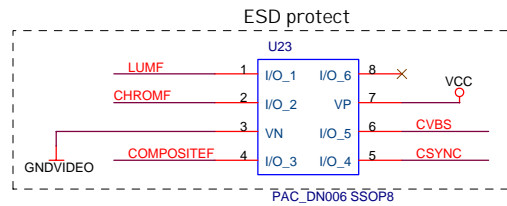
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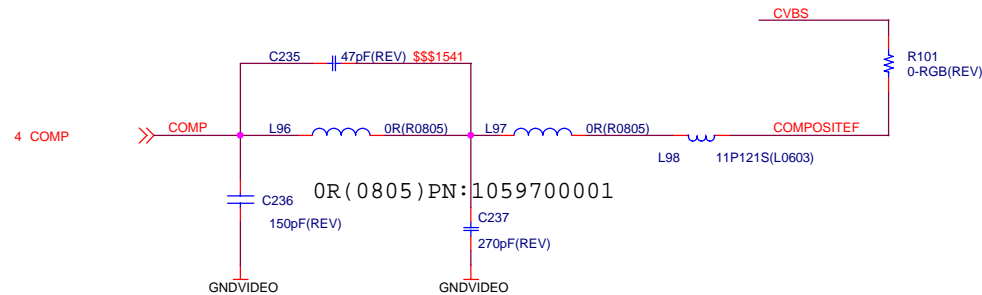
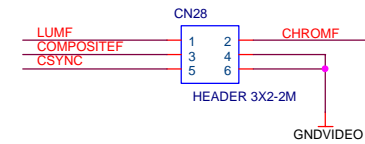
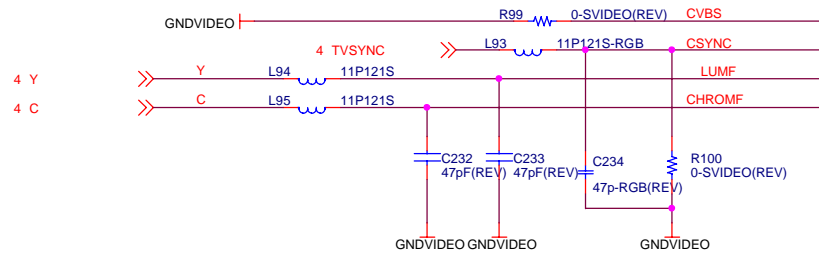
PLCC BIOS



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Second Flash PLCC32		
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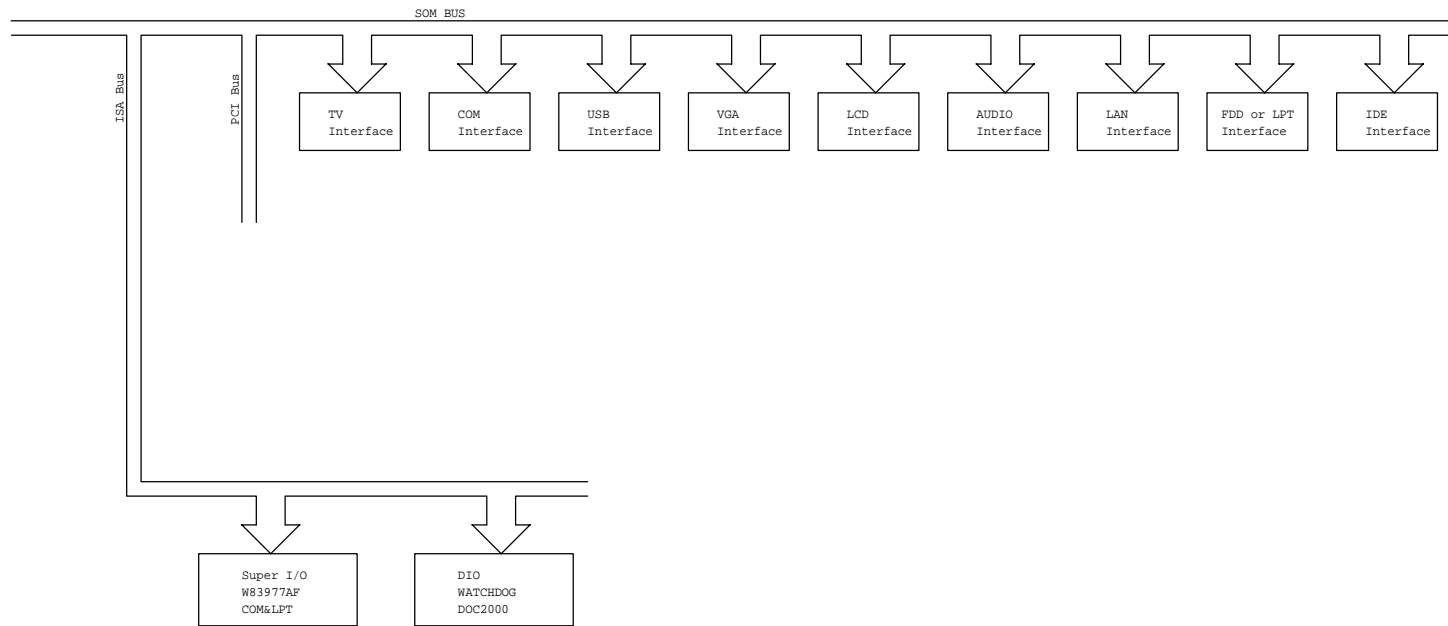


keep the GNDVIDEO at
a wide trace 40mils
and connect to HOLE



COMPOSITE VIDEO OUTPUT
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